R5F100BCANA

Product Spec

Price	-	PWM Output.	-
CPU	RL78	8-bit A/D Converter (ch)	0
Max. Freq (MHz)	32	10-bit A/D Converter (ch)	8
Subclock (32.768kHz)	NO	12-bit A/D Converter (ch)	0
Bit Size	16	16Bit A/D Converter	0
Family	RL78	8-bit D/A Converter (ch)	0
Series	RL78/G1x	10-bit D/A Converter (ch)	0
Group	RL78/G13	12-bit D/A Converter (ch)	0
Memory Management Unit.	NO	14-bit or more D/A Converter (ch)	0
FPU	NO	Operating Temperature Max (°C)	85
RAM	2	Operating Temperature Min (°C)	-40
Program memory	32	CAN (ch)	0
Pin Count	32	Ethernet	NO
Data Flash (KB)	4	USB Host	NO
Program Memory Type	Flash memory	USB Peripheral	NO
Cache type	-	USB OTG	-
Int. main oscillator freq	32	I2C-bus	4
PLL	NO	Ext. address bus	-
RTC	YES	Ext. data bus (Bit)	-
Operating Voltage Max	5.5	External Interrupt Pins	6
Operating Voltage Min	1.6	LCD_Com	-
Note1	-	Low Voltage Detection	YES
Note2	-	Power-On Reset	YES
I/O Ports CMOS I/O	25	DMAC	2
On-Chip Debug	YES	EXDMAC	-
8-bit Timer (ch)	0	CSI	3
16-bit Timer (ch)	8	UART	3
32-bit Timer (ch)	0	Lin	1
Watchdog Timer (ch)	1	IEBus	-
Other timer	Interval Timer x 1 ch	Remarks	-
PWM Output (up to 8Bit)	-	Application	-
PWM Output (9-16Bit)	3	Production Status	Mass Production

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Order Information

Orderable Part No.	Production Status.	Packing	Environmental	remark
R5F100BCANA#U0	Mass Production	Tray	Pb Free	-

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Package & Packing

p32k8-50-3b4 fig0012e

Package	
Renesas code	PWQN0032KB-A
Previous code	P32K8-50-3B4-3
JEITA code	P-HWQFN32-5x5-0.50
Name	HWQFN
Terminal count	32
Terminal pitch (mm)	0.5
Dimensions (mm)	5x5
Mass (g) [Reference value]	0.06
Mounting height (mm) [MAX]	0.8
Terminal material - Base	Cu alloy
Terminal material - Surface	Ni/Pd/Au
Drawing Link	p32k8-50-3b4
Mount pad	fig0012e
Remarks	Exposeddiepadtype

Packing	
Container JEDEC tray	H8036-2
Packing Tape Complete dry pack	H8052
Container Tape	H8039

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