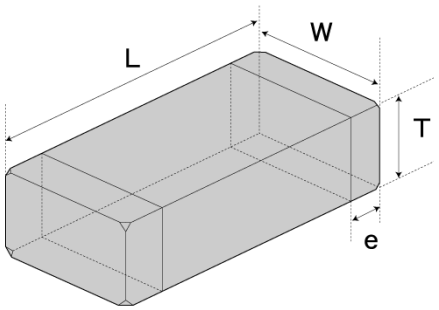
Spec Sheet

Super Low Distortion Multilayer Ceramic Capacitors

TMK105SD821KV-F



Features

- Item Summary

  820pF±10%, 25V,

  LowDistortionStandard, 0402/1005 (EIA/JIS)
- Lifecycle Stage

  Mass Production (Preferred)
- Standard packaging quantity (minimum)

  Taping Paper 10000pcs

Products characteristics table

Capacitance	820 pF ± 10 %
Case Size (EIA/JIS)	0402/1005
Rated Voltage	25 V
tan δ (max)	0.1 %
Operating Temp. Range (EIA)	-55 to +125 ℃
Operating Temp. Range (JIS)	-55 to +125 ℃
High Temperature Loading (% Rated Voltage)	200 %
Insulation Resistance (min)	10 GΩ
RoHS Compliance (10 subst.)	Yes
REACH Compliance (209 subst.)	Yes
Halogen Free	Yes
Soldering	Reflow

External Dimensions

Dimension L	1.0 ±0.05 mm
Dimension W	0.5 ±0.05 mm
Dimension T	0.5 ±0.05 mm
Dimension e	0.25 ±0.10 mm

2021.09.21

The data is reference only. Electrical characteristics vary depending on environment or measurement condition.
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Before making final selection, please check product specification.

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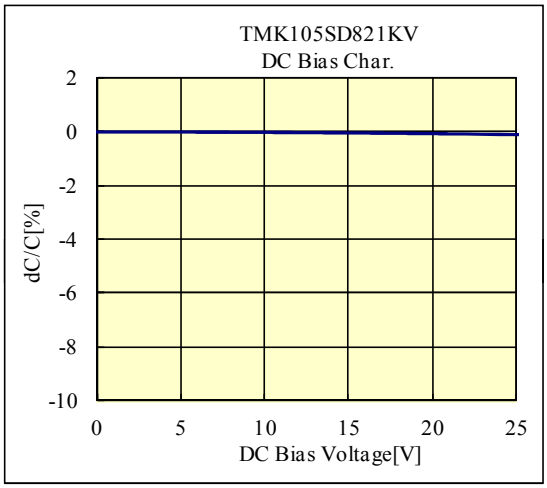
Multi Layer Ceramic Capacitor

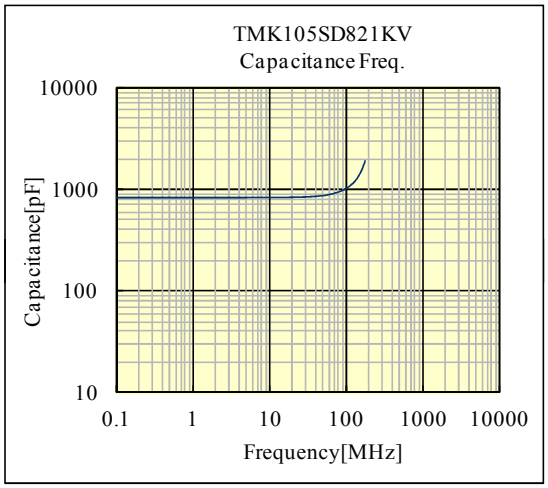
TAIYO YUDEN -Electrical Characteristics Data- 16CJD03

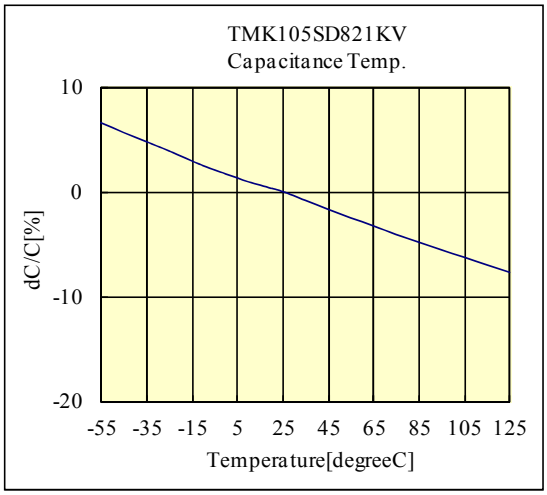
RoHS compliance product

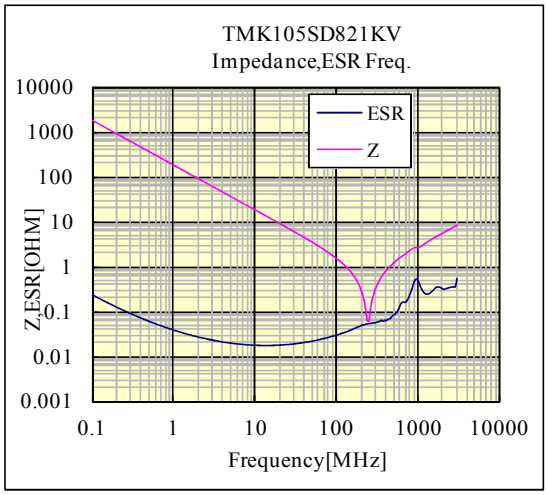
○*TMK105SD821KV*

25V 1005 820pF +/- 10% t=0.55mmMAX
25V 0402 820pF +/- 10% t=0.022inchMAX









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REMINDERS

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  Please do not incorporate our products into any equipment requiring high levels of safety and/or reliability (e.g., aerospace equipment, aviation equipment*, medical equipment classified as Class IV by IMDRF, nuclear control equipment, undersea equipment, military equipment).

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