

CoolGaN™ BDS 650 V G5

CoolGaN™ Bidirectional switch, enhancement-mode

Infineon's CoolGaN™ Bi-Directional Switch (BDS) is an innovative solution in gallium nitride (GaN) transistor technology. The CoolGaN™ BDS 650 V G5 enables efficient voltage blocking in both directions, making it a versatile option for a wide range of applications. The CoolGaN™ BDS monolithically integrates a substrate voltage control circuit, thanks to Infineon's proprietary technology, this reduces design complexity. IGLT65R110B2 is offered in Infineon's TOLT, top-side cooling package specially designed to enable the highest power densities for power-demanding industrial applications.

Features

- 650 V CoolGaN™ technology with 850 V_{SS} surge immunity
- Superior rugged Gate Injection Transistor (GIT) structure
- Dual-gate for independent bi-directional functionality
- Superior performance of R_{SS(on)} over operating frequency
- Reliable Thermal Cycling on-Board (TCoB) performance
- Optimized for soft switching operation
- 2 kV HBM ESD standard

Benefits

- Effective replacement of back-to-back uni-directional products
- Improves system efficiency and power density
- Enables higher switching frequency
- System cost reduction
- Enables new single stage isolated topologies

Potential applications

- Cycloconverter in solar μ -inverter
- Vienna type rectifier in industrial/server SMPS and UPS
- T-type PFC, inverter in motor drives
- HERIC inverter in string solar inverter

Product validation

Qualified for industrial applications according to the relevant tests of JEDEC JESD47, JESD22 and J-STD-020.

Please note: The substrate thermal pad is for thermal coupling only, it should be electrically isolated from any electrical node in the system via isolation material.

Table 1 Key performance parameters

Parameter	Value	Unit
V _{SS,cont}	340	V _{AC}
R _{SS(on),typ}	110	m Ω
Q _{GxSx,switch}	1.61	nC
I _{SS,pulse}	±37.5	A
Q _{oss} @ 400 V _{SS} (switch)	31.8	nC

Part number	Package	Marking	Related links
IGLT65R110B2	PG-HDSOP-16	65R110B2	see Appendix A

TOLT

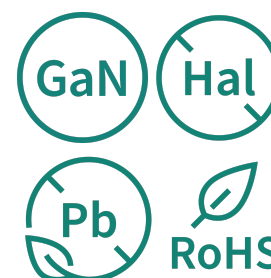
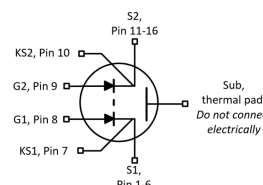
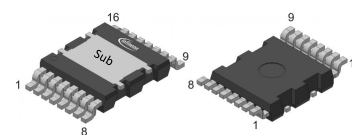


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1 Maximum ratings

at $T_j = 25^\circ\text{C}$, unless otherwise specified. Continuous application of maximum ratings can deteriorate transistor lifetime. For further information, contact your local Infineon sales office.

Table 2 Maximum ratings

Parameter	Symbol	Values			Unit	Note / Test condition
		Min.	Typ.	Max.		
Source-to-source voltage, continuous	$V_{SS,cont}$	-	-	340	V_{AC}	$V_{GS}=0\text{ V}$
Source-to-source voltage, transient	$V_{SS,trans}$	-650	-	650	V	$t_{TRANS}=5\text{ ms}$; $V_{GS}=0\text{ V}$
Source-to-source voltage, surge	$V_{SS,surge}$	-850	-	850	V	$t_{SURGE}=1\text{ }\mu\text{s}$; $V_{GS}=0\text{ V}$
Source-to-source AC line frequency	f_{SS}	45	-	-	Hz	$V_{SS}=305\text{ V}_{AC}$; $\pm 431\text{ V}_{pk}$
RMS current, source-to-source	$I_{SS,RMS}$	-14	-	14	A	$T_C=25^\circ\text{C}$;
		-6.3		6.3		$T_C=125^\circ\text{C}$; $T_j=T_{j,max}$
Pulsed current, source-to-source	$I_{SS,pulse}$	-37.5	-	37.5	A	$T_j=25^\circ\text{C}$; $I_{GS}=13.6\text{ mA}$
		-24.2		24.2		$T_j=125^\circ\text{C}$; $I_{GS}=13.6\text{ mA}$
Gate current, steady state (single gate)	$I_{G,ss}$	-	-	13.6	mA	$T_j=-40^\circ\text{C}$ to $T_j=150^\circ\text{C}$; see Table 8
Gate current, pulsed (single gate) ¹⁾	$I_{G,pulse}$	-	-	1360	mA	$T_j=-40^\circ\text{C}$ to $T_j=150^\circ\text{C}$; $t_{PULSE}=50\text{ ns}$, $f=100\text{ kHz}$; see Table 8
Gate source voltage, continuous ¹⁾	V_{GS}	-10	-	-	V	$T_j=-40^\circ\text{C}$ to $T_j=150^\circ\text{C}$; see Table 8
Gate source voltage, pulsed ¹⁾	$V_{GxSx,pulse}$	-25	-	-	V	$T_j=-40^\circ\text{C}$ to $T_j=150^\circ\text{C}$; $t_{PULSE}=50\text{ ns}$, $f=100\text{ kHz}$; open source
Power dissipation	P_{tot}	-	-	55	W	$T_C=25^\circ\text{C}$
Operating junction temperature	T_j	-40	-	150	$^\circ\text{C}$	-
Storage temperature	T_{stg}	-40	-	150	$^\circ\text{C}$	Max shelf life depends on storage conditions
Source-to-source voltage slew-rate	dv/dt	-	-	40	V/ns	-

¹⁾ We recommend using an advanced driving technique to optimize the device performance. Please see gate drive application note for more details.

2 Thermal characteristics

Table 3 Thermal characteristics

Parameter	Symbol	Values			Unit	Note / Test condition
		Min.	Typ.	Max.		
Thermal resistance, junction - case	R_{thJC}	-	-	2.28	°C/W	$T_{jmax}=150^{\circ}\text{C}$
Reflow soldering temperature	T_{sold}	-	-	260	°C	reflow MSL3

3 Electrical characteristics

at $T_j=25^{\circ}\text{C}$, unless specified otherwise

Table 4 Gate characteristics

Parameter	Symbol	Values			Unit	Note / Test condition
		Min.	Typ.	Max.		
Gate threshold voltage: switch mode (OFF) to diode mode	$V_{GxSx(th), diode}$	0.8	1.15	1.6	V	$I_{SS}=1.36\text{ mA}$; $V_{GySy}=0\text{ V}$; $V_{SS}=10\text{ V}$; $T_j=25^{\circ}\text{C}$;
		-	0.95	-		$I_{SS}=1.36\text{ mA}$; $V_{GySy}=0\text{ V}$; $V_{SS}=10\text{ V}$; $T_j=150^{\circ}\text{C}$; see Table 9
Gate threshold voltage: diode mode to switch mode (ON)	$V_{GxSx(th), switch}$	0.7	1.0	1.5	V	$I_{SS}=1.36\text{ mA}$; $I_{GySy}=2.27\text{ mA}$; $V_{SS}=10\text{ V}$; $T_j=25^{\circ}\text{C}$;
		-	0.83	-		$I_{SS}=1.36\text{ mA}$; $I_{GySy}=2.27\text{ mA}$; $V_{SS}=10\text{ V}$; $T_j=150^{\circ}\text{C}$; see Table 9
Gate-Source reverse clamping voltage	$V_{GS, clamp}$	-8	-	-	V	$I_{GS,ss}=-1\text{ mA}$
Gate charge: diode mode to switch mode (ON)	$Q_{GxSx, switch}$	-	1.61	-	nC	$V_{GxSx}=0\text{ to }3\text{ V}$; $I_{GySy}=2.27\text{ mA}$; $V_{SS}=400\text{ V}$, $I_{SS}=4.5\text{ A}$
Gate resistance	$R_{GS,int}$	-	0.84	-	Ω	LCR impedance measurement; $f=f_{res}$

Table 5 On-state characteristics

Parameter	Symbol	Values			Unit	Note / Test condition
		Min.	Typ.	Max.		
Source-to-source on-state resistance	$R_{SS(on)}$	-	110	140	m Ω	$I_{GS}=13.6\text{ mA}$; $I_{SS}=4.5\text{ A}$; $T_j=25^{\circ}\text{C}$
			230	-		$I_{GS}=13.6\text{ mA}$; $I_{SS}=4.5\text{ A}$; $T_j=150^{\circ}\text{C}$

Table 6 Dynamic characteristics - switch mode, see Table 10

Parameter	Symbol	Values			Unit	Note / Test condition
		Min.	Typ.	Max.		
Input capacitance: switch mode	$C_{iss, switch}$	-	149	-	pF	$V_{GS}=0\text{ V}$; $V_{SS}=400\text{ V}$; $f=1\text{ MHz}$
Output capacitance: switch mode	$C_{oss, switch}$		67			
Reverse Transfer capacitance: switch mode	$C_{rss, switch}$		1			
Effective output capacitance, energy related: switch mode ²⁾	$C_{o(er), switch}$	-	73.1	-	pF	$V_{GS}=0\text{ V}$; $V_{SS}=400\text{ V}$

Table 6 Dynamic characteristics - switch mode, see Table 10

Parameter	Symbol	Values			Unit	Note / Test condition
		Min.	Typ.	Max.		
Effective output capacitance, time related: switch mode ³⁾	$C_{o(tr),switch}$	-	79.5	-	pF	$V_{GS}=0\text{ V}$; $V_{SS}=0\text{ to }400\text{ V}$; $I_{SS}=\text{const}$
Output charge: switch mode	$Q_{oss,switch}$	-	31.8	-	nC	$V_{GS}=0\text{ V}$; $V_{SS}=400\text{ V}$

²⁾ $C_{o(er)}$ is a fixed capacitance that gives the same stored energy as C_{oss} while V_{SS} is rising from 0 to 400 V

³⁾ $C_{o(tr)}$ is a fixed capacitance that gives the same charging time as C_{oss} while V_{SS} is rising from 0 to 400 V

Table 7 Dynamic characteristics - diode mode, see Table 11

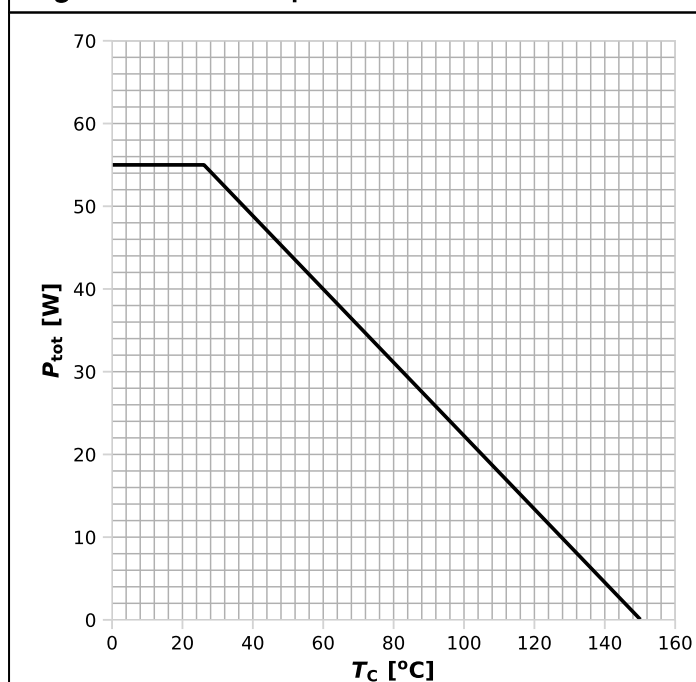
Parameter	Symbol	Values			Unit	Note / Test condition
		Min.	Typ.	Max.		
Input capacitance: diode mode	$C_{iss,diode}$	-	152	-	pF	$V_{GxSx}=0\text{ V}$; $I_{GySy}=0.14\text{ mA}$; $V_{SySx}=400\text{ V}$; $f=1\text{ MHz}$
Output capacitance: diode mode	$C_{oss,diode}$		74			
Reverse Transfer capacitance: diode mode	$C_{rss,diode}$		1			
Effective output capacitance, energy related: diode mode ⁴⁾	$C_{o(er),diode}$	-	84.2	-	pF	$V_{GxSx}=0\text{ V}$; $I_{GySy}=0.14\text{ mA}$; $V_{SySx}=400\text{ V}$
Effective output capacitance, time related: diode mode ⁵⁾	$C_{o(tr),diode}$	-	95.9	-	pF	$V_{GxSx}=0\text{ V}$; $I_{GySy}=0.14\text{ mA}$; $V_{SySx}=0\text{ to }400\text{ V}$; $I_{SS}=\text{const}$
Output charge: diode mode	$Q_{oss,diode}$	-	38.4	-	nC	$V_{GxSx}=0\text{ V}$; $I_{GySy}=0.14\text{ mA}$; $V_{SySx}=400\text{ V}$

⁴⁾ $C_{o(er)}$ is a fixed capacitance that gives the same stored energy as C_{oss} while V_{SS} is rising from 0 to 400 V

⁵⁾ $C_{o(tr)}$ is a fixed capacitance that gives the same charging time as C_{oss} while V_{SS} is rising from 0 to 400 V

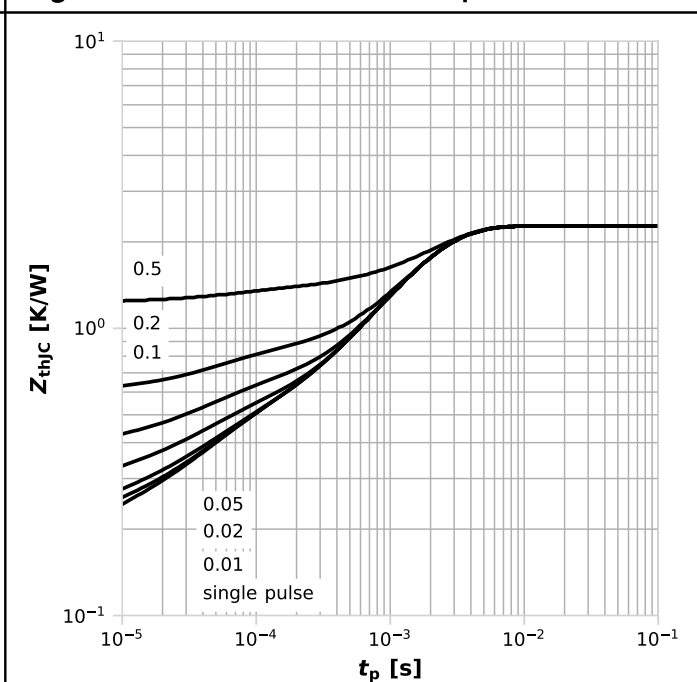
4 Electrical characteristics diagrams

Diagram 1: Power dissipation



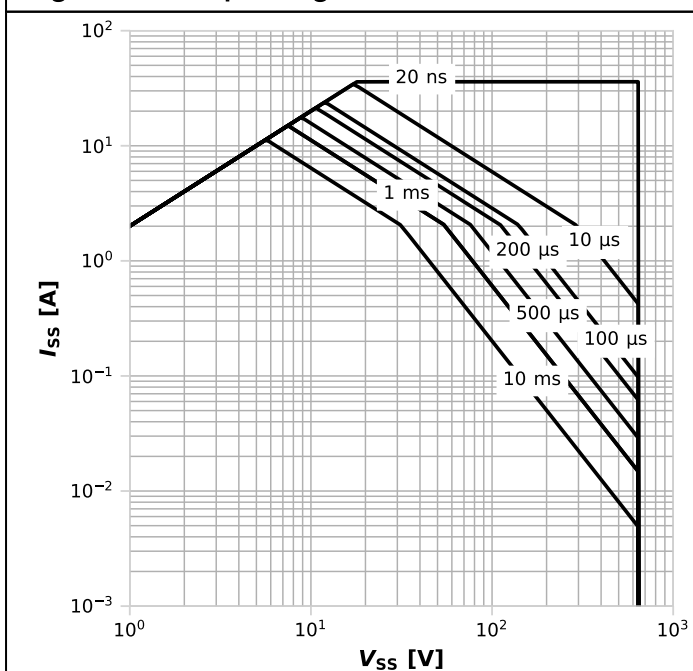
$$P_{\text{tot}} = f(T_c)$$

Diagram 2: Max. transient thermal impedance



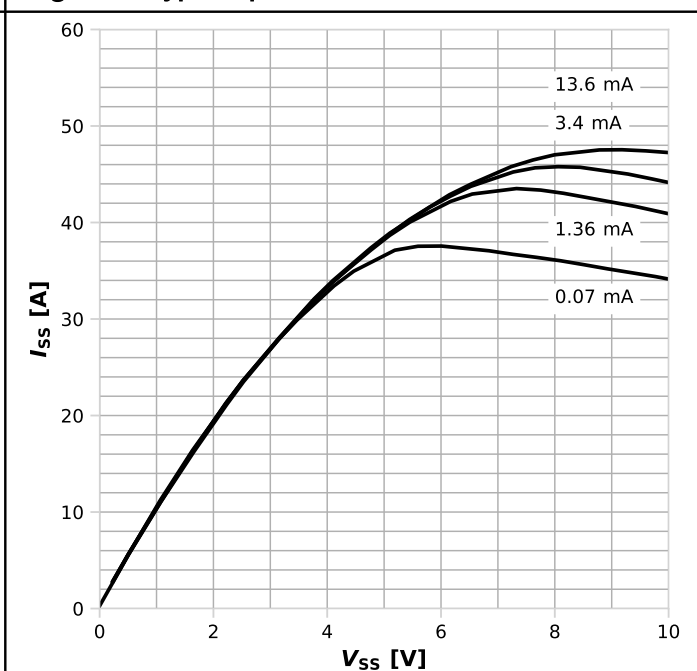
$$Z_{\text{thJC}} = f(t_p); \text{ parameter: } D = t_p / T$$

Diagram 3: Safe operating area



$$I_{\text{D}} = f(V_{\text{DS}}); T_c = 25^\circ\text{C}; D = 0; \text{ parameter: } t_p$$

Diagram 4: Typ. output characteristics



$$I_{\text{SS}} = f(V_{\text{SS}}); I_{\text{GYSy}} = 13.6 \text{ mA}; T_j = 25^\circ\text{C}; \text{ parameter: } I_{\text{GXSx}}$$

Diagram 5: Typ. output characteristics

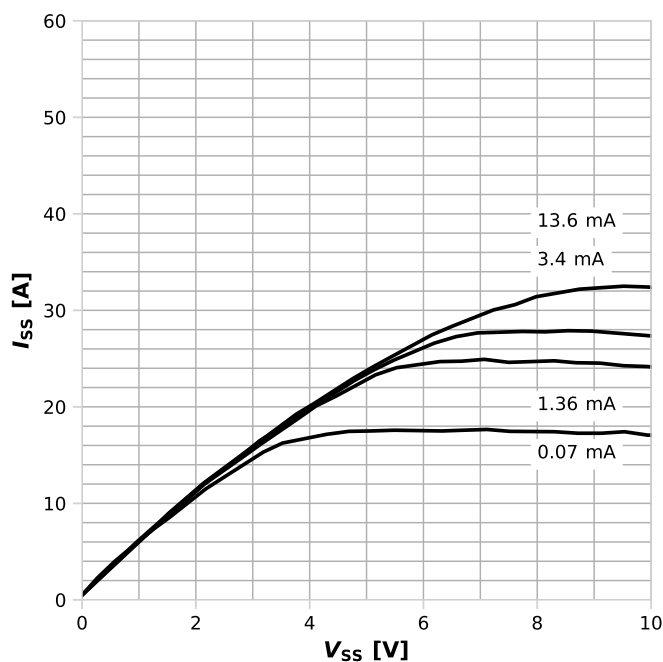

 $I_{SS} = f(V_{SS}); I_{GySy} = 13.6 \text{ mA}; T_j = 125^\circ\text{C}; \text{parameter: } I_{GxSx}$

Diagram 6: Typ. source on-state resistance

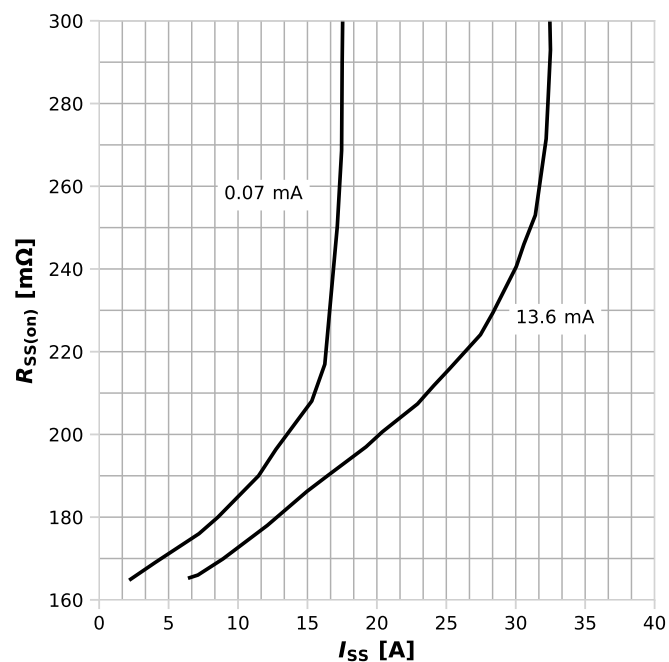

 $R_{SS(on)} = f(I_{SS}); I_{GySy} = 13.6 \text{ mA}; T_j = 125^\circ\text{C}; \text{parameter: } I_{GxSx}$

Diagram 7: Typ. source-source on-state resistance

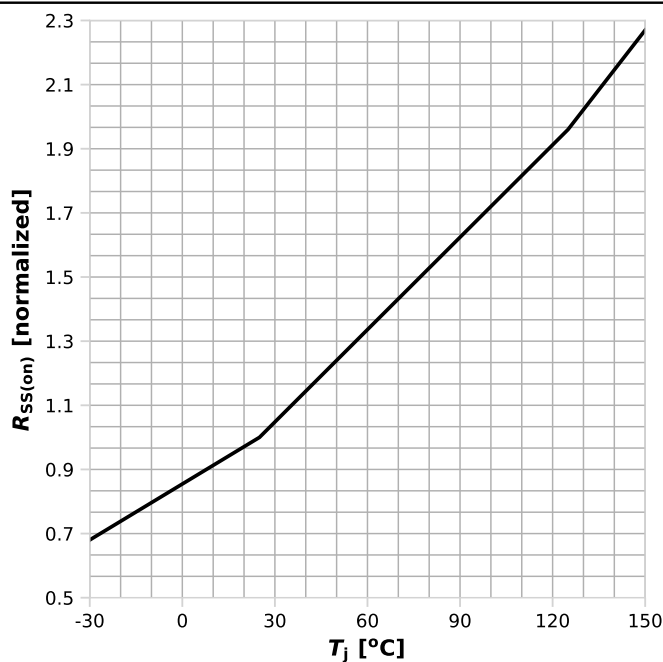
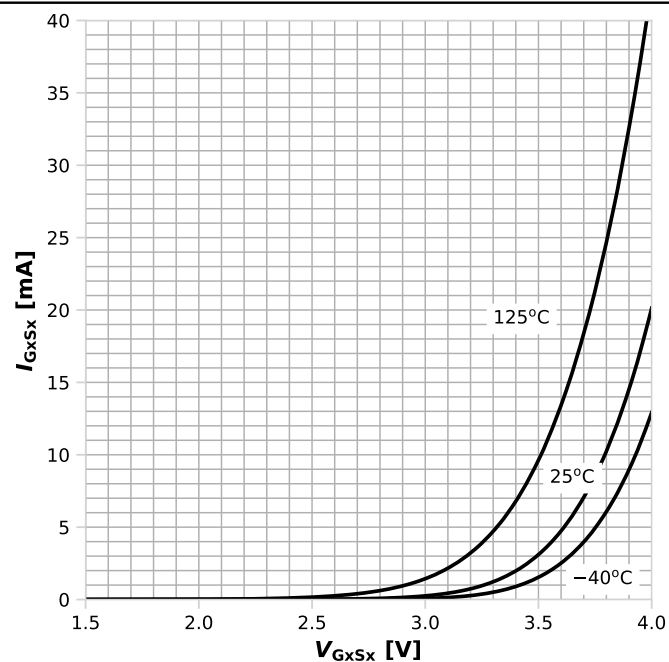
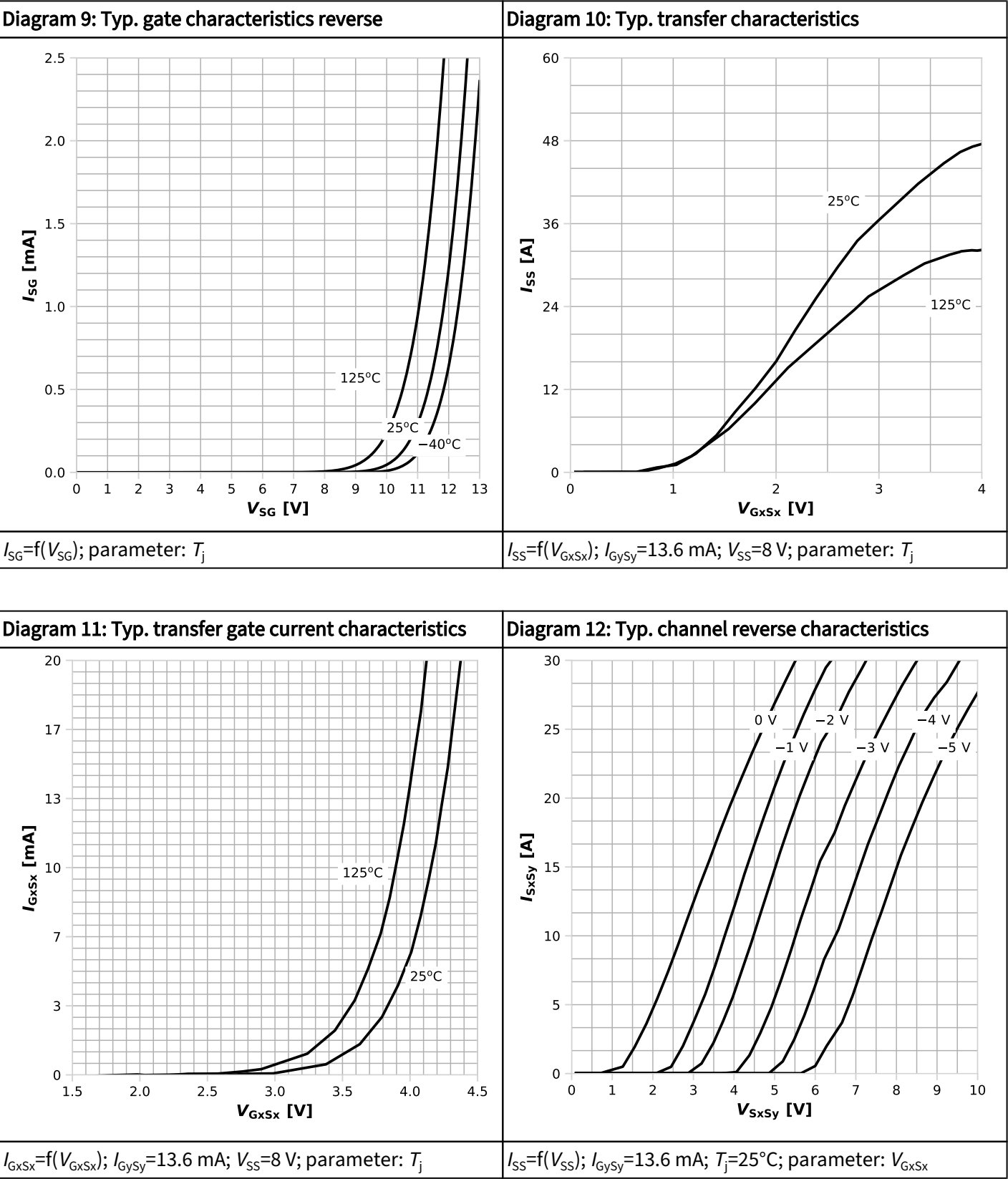
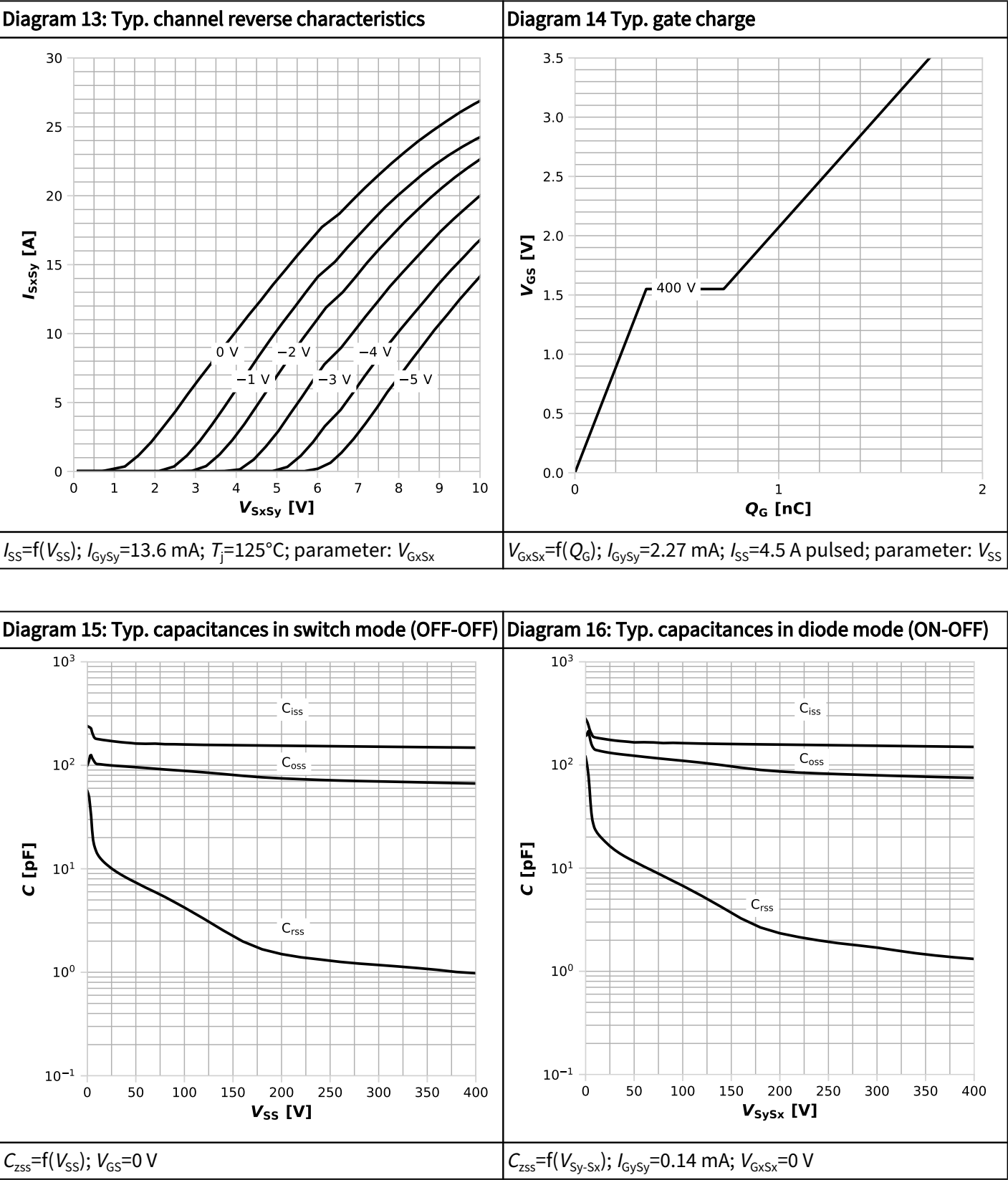
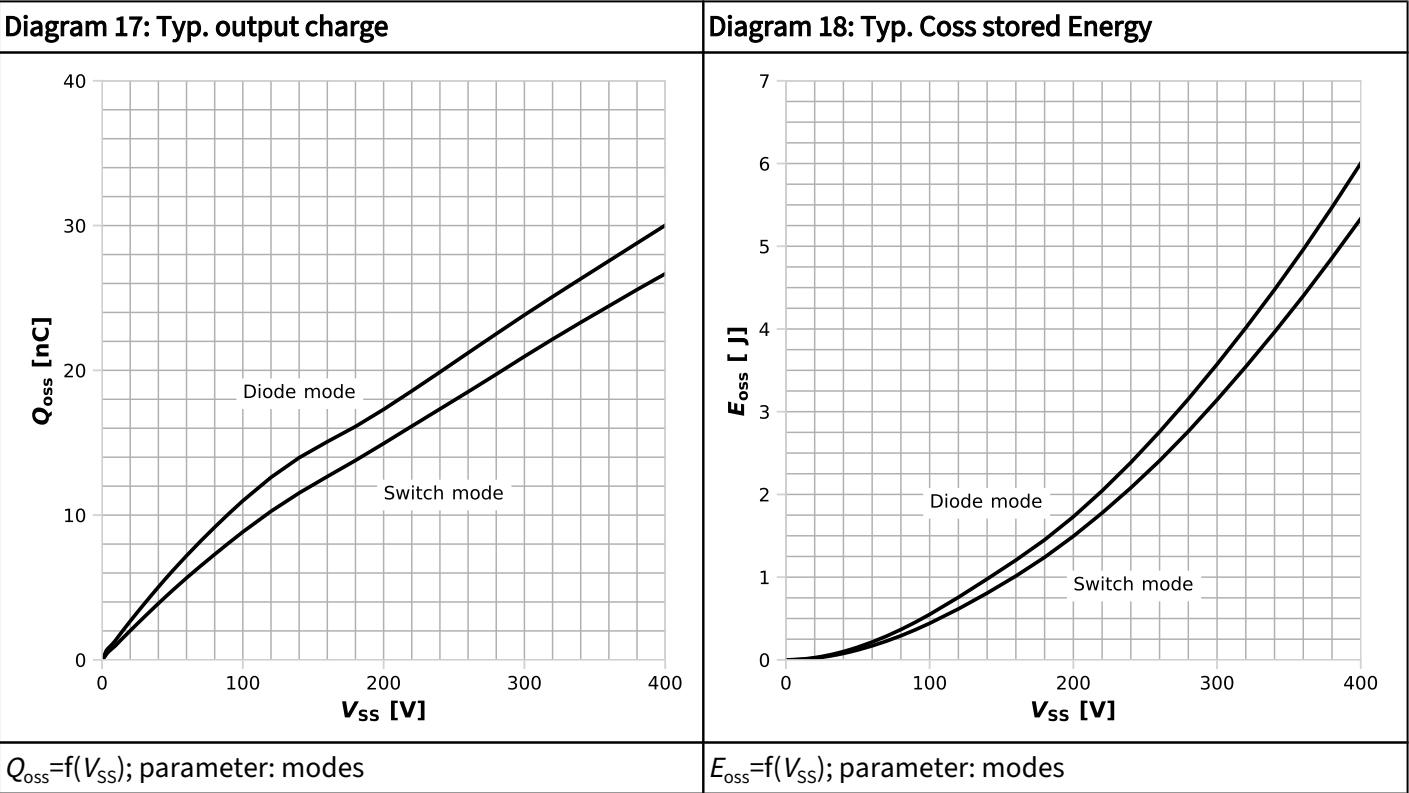

 $R_{SS(on)} = f(T_j); I_{GySy}, I_{GxSx} = 13.6 \text{ mA}; I_{SS} = 4.5 \text{ A}$

Diagram 8: Typ. gate characteristics forward


 $I_{GS} = f(V_{GxSx}); \text{open } S_y, G_y; \text{parameter: } T_j$







5 Test circuits

Table 8 Gate current switching waveform

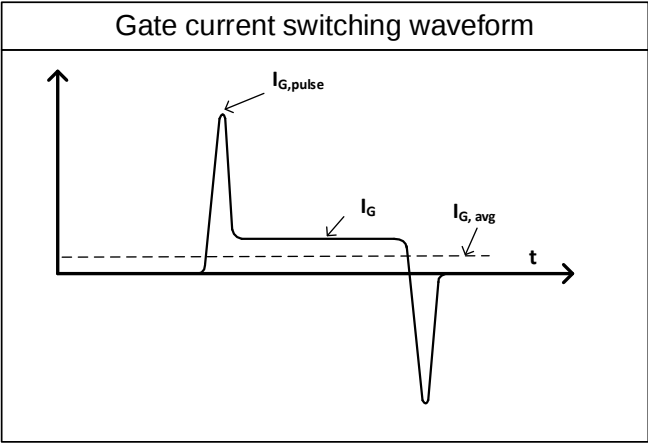
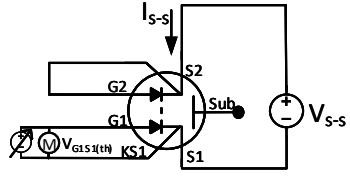
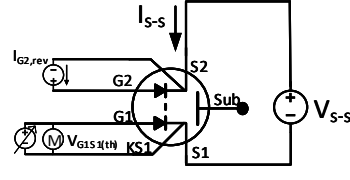


Table 9 Test condition

$V_{GSx(th)}$ test condition (diode mode)	$V_{GSx(th)}$ test condition (switch mode)
 Switch mode (OFF) → Diode mode	 Diode mode → Switch mode (ON)

6 Modes of operation

Table 10 Switch mode

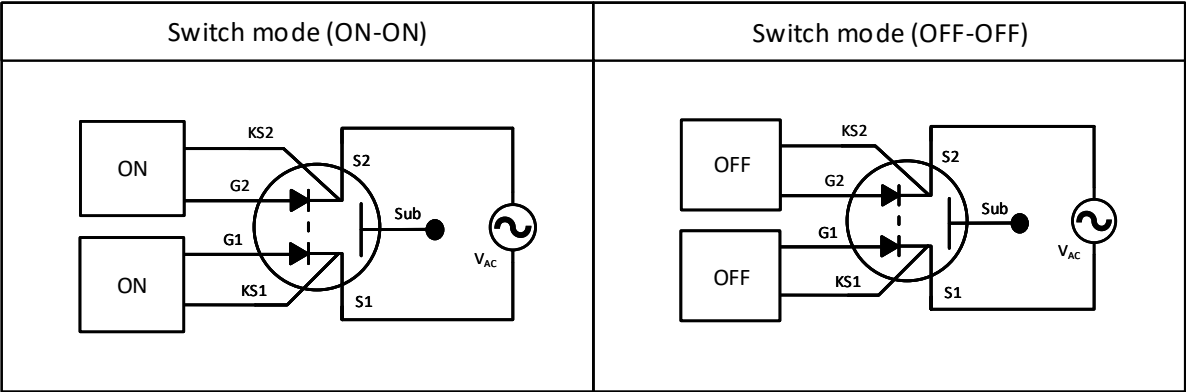
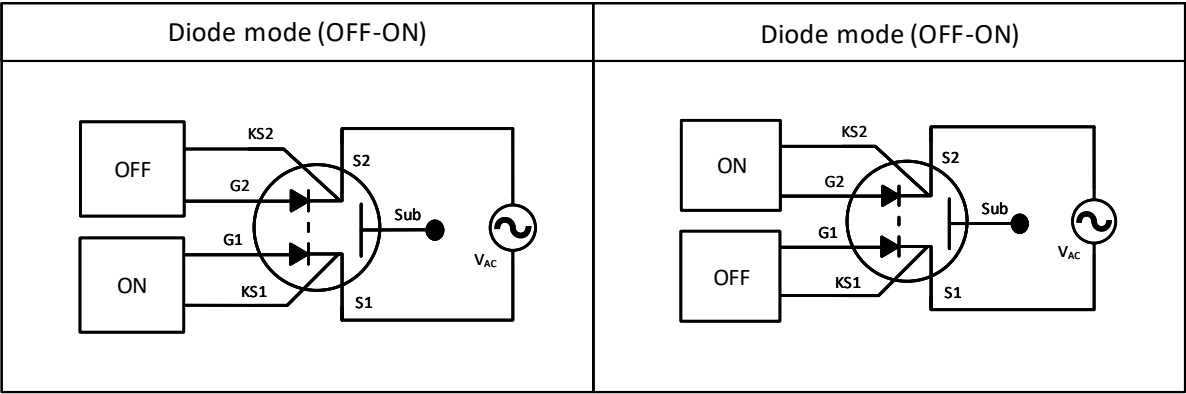
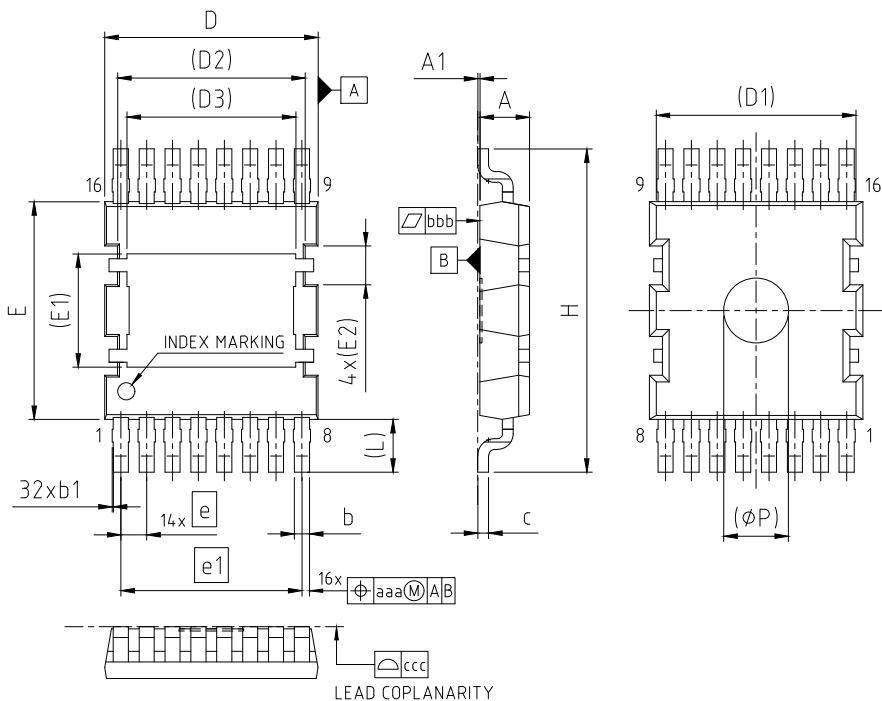


Table 11 Diode mode



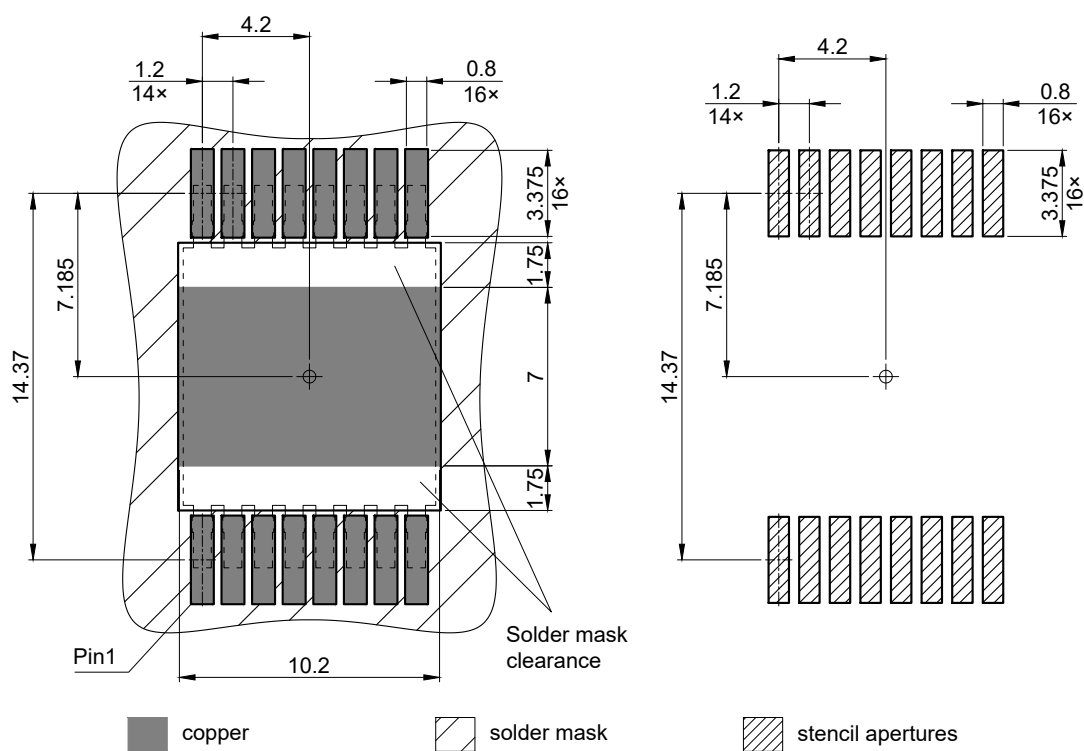
7 Package outlines



PACKAGE - GROUP NUMBER: PG-HDSOP-16-U06					
DIMENSIONS	MILLIMETERS		DIMENSIONS	MILLIMETERS	
	MIN.	MAX.		MIN.	MAX.
A	2.25	2.35	e	1.20	
A1	---	0.15	e1	8.40	
b	0.60	0.80	H	14.80	15.20
b1	---	0.15	L	2.45	
c	0.40	0.60	ØP	3.00	
D	9.70	10.10	aaa	0.25	
D1	9.27		bbb	0.02	
D2	8.70		ccc	0.10	
D3	7.83				
E	10.00	10.30			
E1	5.26				
E2	1.80				

NOTE: DIMENSIONS DO NOT INCLUDE MOLD FLASH, PROTRUSION OR GATE BURRS

Figure 1 Outline PG-HDSOP-16, dimensions in mm



All dimensions are in units mm
Based on stencil thickness 0.2 mm
All pads are non-solder mask defined

Figure 2 Footprint drawing PG-HDSOP-16, dimensions in mm

8 Appendix A

Table 12 **Related links**

- [IFX CoolGaN™ GaN 650 V webpage](#)
- [IFX CoolGaN™ GaN 650 V reliability white paper](#)
- [IFX CoolGaN™ GaN 650 V gate driver application note](#)
- [IFX CoolGaN™ GaN 650 V applications information](#)

Revision history

IGLT65R110B2

Revision 2025-10-02, Rev. 1.0

Previous revisions

Revision	Date	Subjects (major changes since last revision)
1.0	2025-10-02	Release of final version

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