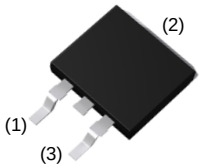
RGT20NL65

650V 10A Field Stop Trench IGBT Datasheet

V_{CES}	650V
$I_{C\ (100°C)}$	10A
$V_{CE(sat)\ (Typ.)}$	1.65V
P_D	106W

●Outline

LPDL (TO-263L)



●Features

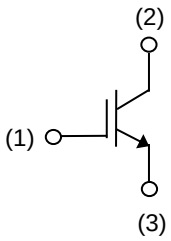
1) Low Collector - Emitter Saturation Voltage

2) Low Switching Loss

3) Short Circuit Withstand Time 5μs

4) Pb - free Lead Plating ; RoHS Compliant

●Inner Circuit



(1) Gate
(2) Collector
(3) Emitter

●Application

General Inverter

UPS

Power Conditioner

Welder

●Packaging Specifications

Type	Packaging	Taping
	Reel Size (mm)	330
	Tape Width (mm)	24
	Basic Ordering Unit (pcs)	1,000
	Packing Code	TL
	Marking	RGT20NL65

●Absolute Maximum Ratings (at T_C = 25°C unless otherwise specified)

Parameter		Symbol	Value	Unit
Collector - Emitter Voltage		V_{CES}	650	V
Gate - Emitter Voltage		V_{GES}	±30	V
Collector Current	T_C = 25°C	I_C	20	A
	T_C = 100°C	I_C	10	A
Pulsed Collector Current		I_{CP} *1	30	A
Power Dissipation	T_C = 25°C	P_D	106	W
	T_C = 100°C	P_D	53	W
Operating Junction Temperature		T_j	-40 to +175	°C
Storage Temperature		T_{stg}	-55 to +175	°C

*1 Pulse width limited by $T_{jmax.}$

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●Thermal Resistance

Parameter	Symbol	Values			Unit
		Min.	Typ.	Max.	
Thermal Resistance IGBT Junction - Case	$R_{\theta(j-c)}$	-	-	1.41	°C/W

●IGBT Electrical Characteristics (at T_j = 25°C unless otherwise specified)

Parameter	Symbol	Conditions	Values			Unit
			Min.	Typ.	Max.	
Collector - Emitter Breakdown Voltage	BV_{CES}	I_C = 10μA, V_{GE} = 0V	650	-	-	V
Collector Cut - off Current	I_{CES}	V_{CE} = 650V, V_{GE} = 0V	-	-	10	μA
Gate - Emitter Leakage Current	I_{GES}	V_{GE} = ±30V, V_{CE} = 0V	-	-	±200	nA
Gate - Emitter Threshold Voltage	$V_{GE(th)}$	V_{CE} = 5V, I_C = 6.7mA	5.0	6.0	7.0	V
Collector - Emitter Saturation Voltage	$V_{CE(sat)}$	I_C = 10A, V_{GE} = 15V,				V
		T_j = 25°C	-	1.65	2.1	
		T_j = 175°C	-	2.15	-	

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●**IGBT Electrical Characteristics** (at T_j = 25°C unless otherwise specified)

Parameter	Symbol	Conditions	Values			Unit
			Min.	Typ.	Max.	
Input Capacitance	C_{ies}	V_{CE} = 30V,	-	610	-	pF
Output Capacitance	C_{oes}	V_{GE} = 0V,	-	25	-	
Reverse transfer Capacitance	C_{res}	f = 1MHz	-	9	-	
Total Gate Charge	Q_g	V_{CE} = 300V,	-	22	-	nC
Gate - Emitter Charge	Q_{ge}	I_C = 10A,	-	6	-	
Gate - Collector Charge	Q_{gc}	V_{GE} = 15V	-	9	-	
Turn - on Delay Time	$t_{d(on)}$	I_C = 10A, V_{CC} = 400V, V_{GE} = 15V, R_G = 10Ω, T_j = 25°C Inductive Load	-	12	-	ns
Rise Time	t_r		-	18	-	
Turn - off Delay Time	$t_{d(off)}$		-	32	-	
Fall Time	t_f		-	104	-	
Turn - on Delay Time	$t_{d(on)}$	I_C = 10A, V_{CC} = 400V, V_{GE} = 15V, R_G = 10Ω, T_j = 175°C Inductive Load	-	13	-	ns
Rise Time	t_r		-	18	-	
Turn - off Delay Time	$t_{d(off)}$		-	34	-	
Fall Time	t_f		-	140	-	
Reverse Bias Safe Operating Area	RBSOA	I_C = 30A, V_{CC} = 520V, V_P = 650V, V_{GE} = 15V, R_G = 50Ω, T_j = 175℃	FULL SQUARE			-
Short Circuit Withstand Time	t_{sc}	V_{CC} ≦ 360V, V_{GE} = 15V, T_j = 25℃	5	-	-	μs

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●Electrical Characteristic Curves

Fig.1 Power Dissipation vs. Case Temperature

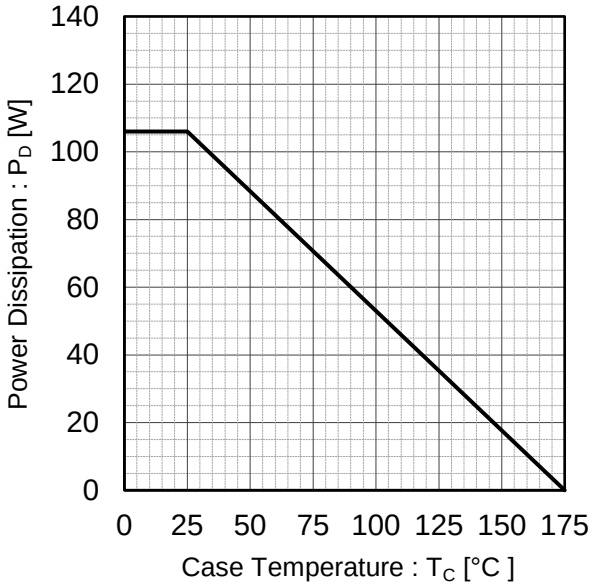


Fig.2 Collector Current vs. Case Temperature

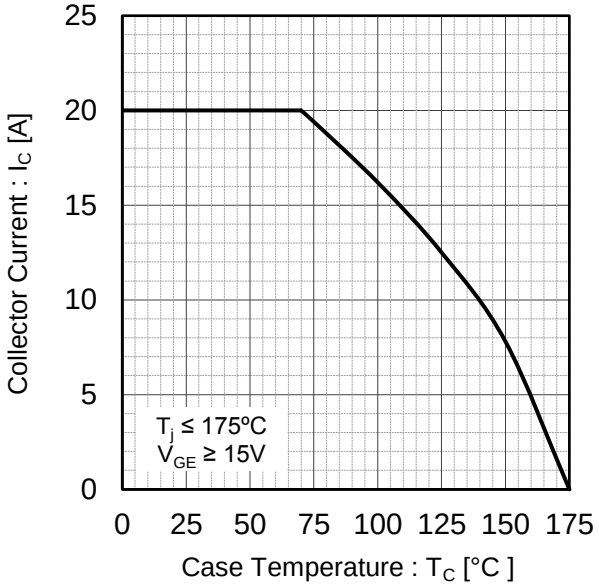


Fig.3 Forward Bias Safe Operating Area

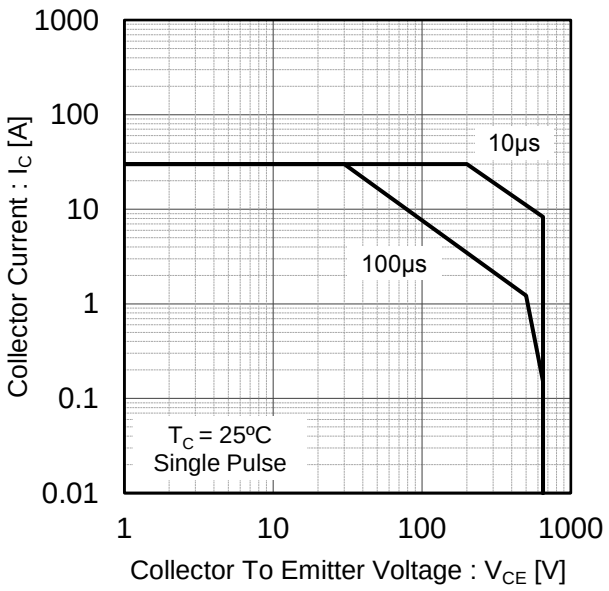
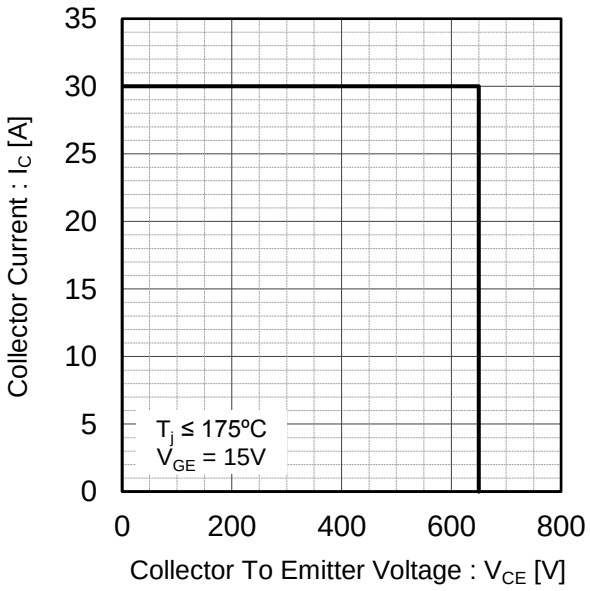


Fig.4 Reverse Bias Safe Operating Area



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●Electrical Characteristic Curves

Fig.5 Typical Output Characteristics

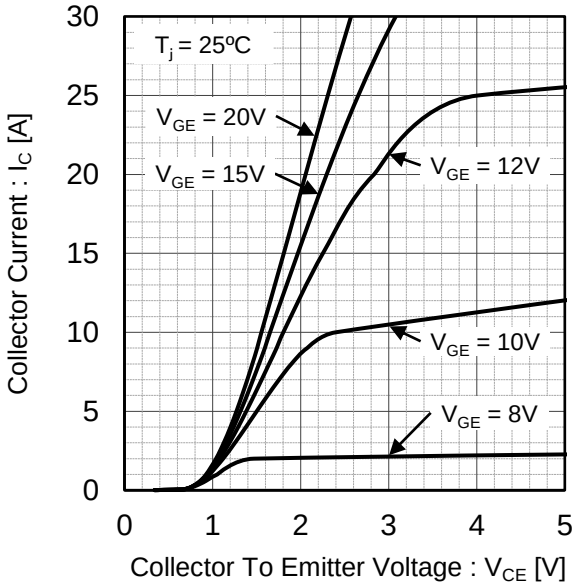


Fig.6 Typical Output Characteristics

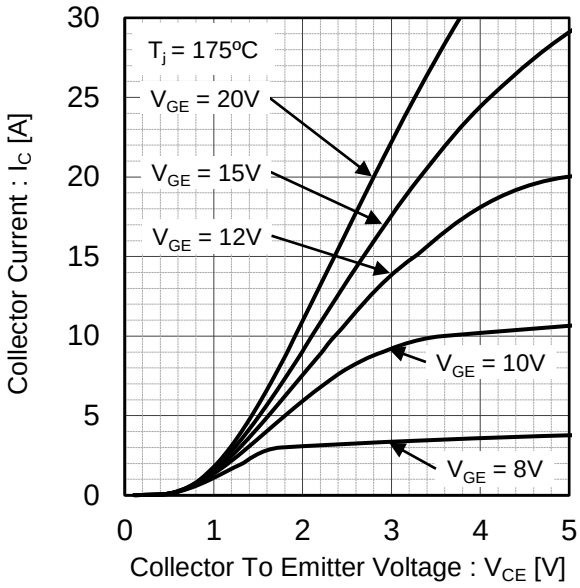


Fig.7 Typical Transfer Characteristics

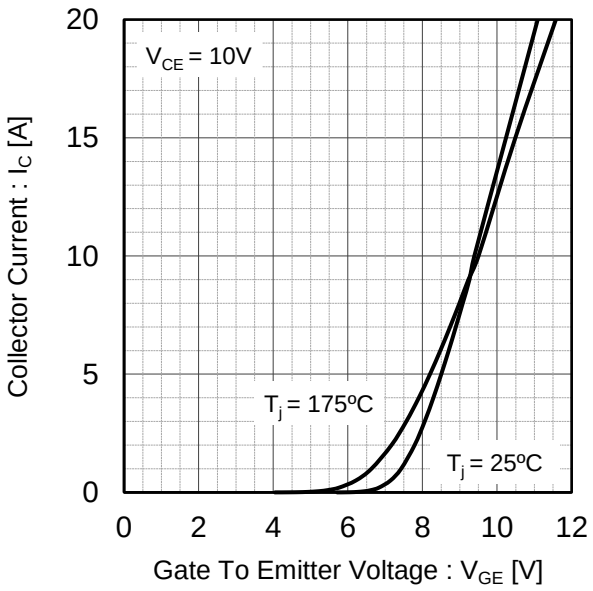
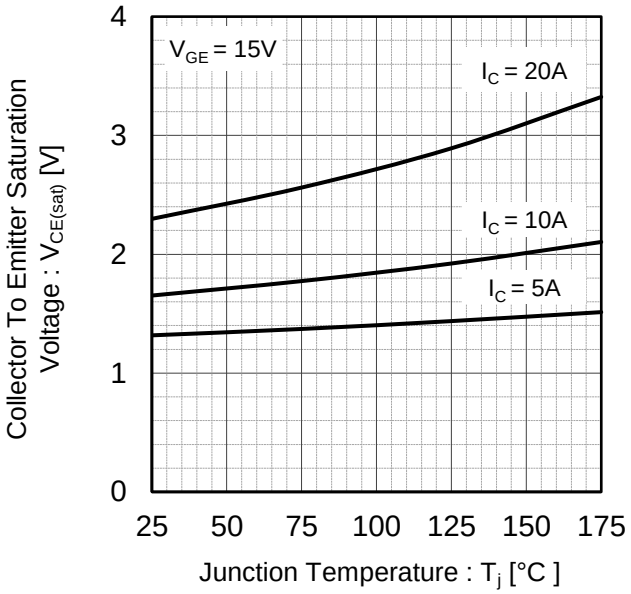


Fig.8 Typical Collector to Emitter Saturation Voltage vs. Junction Temperature



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●Electrical Characteristic Curves

Fig.9 Typical Collector to Emitter Saturation Voltage vs. Gate to Emitter Voltage

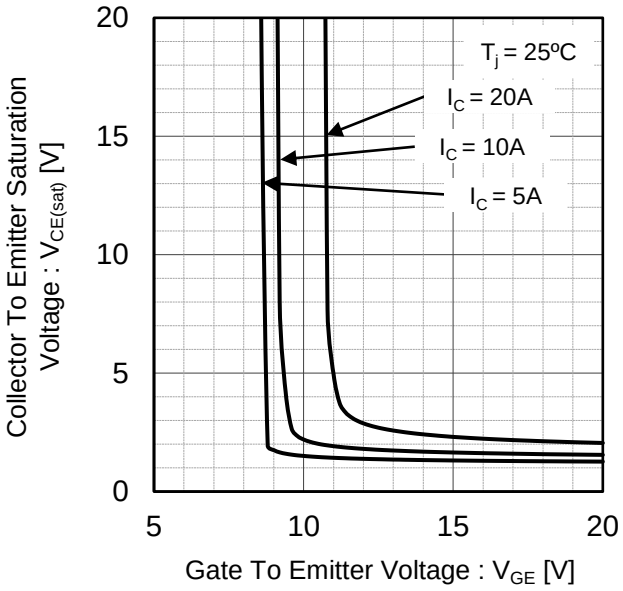


Fig.10 Typical Collector to Emitter Saturation Voltage vs. Gate to Emitter Voltage

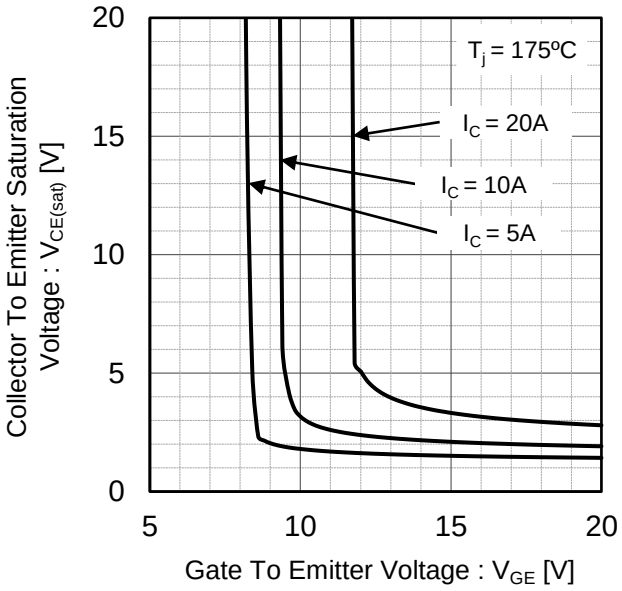


Fig.11 Typical Switching Time vs. Collector Current

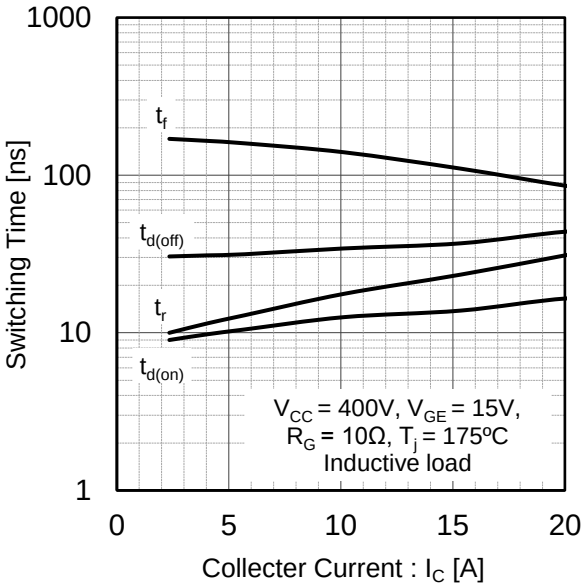
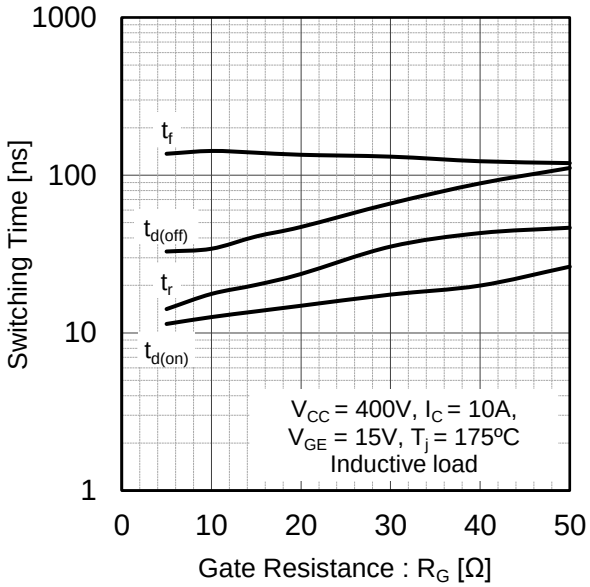


Fig.12 Typical Switching Time vs. Gate Resistance



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●Electrical Characteristic Curves

Fig.13 Typical Switching Energy Losses vs. Collector Current

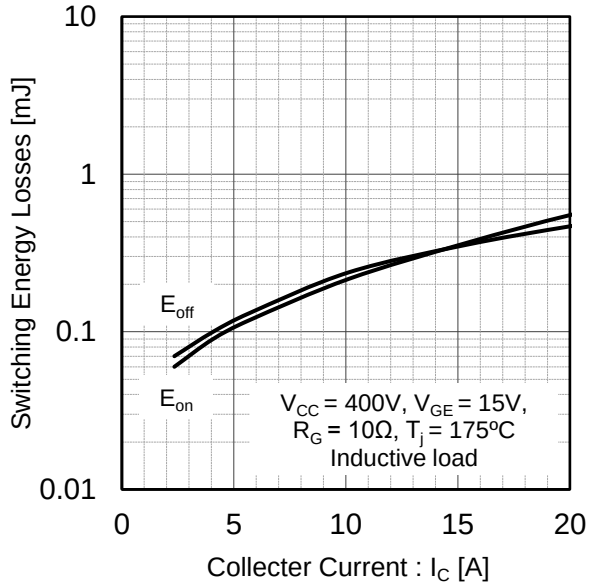


Fig.14 Typocal Switching Energy Losses vs. Gate Resistance

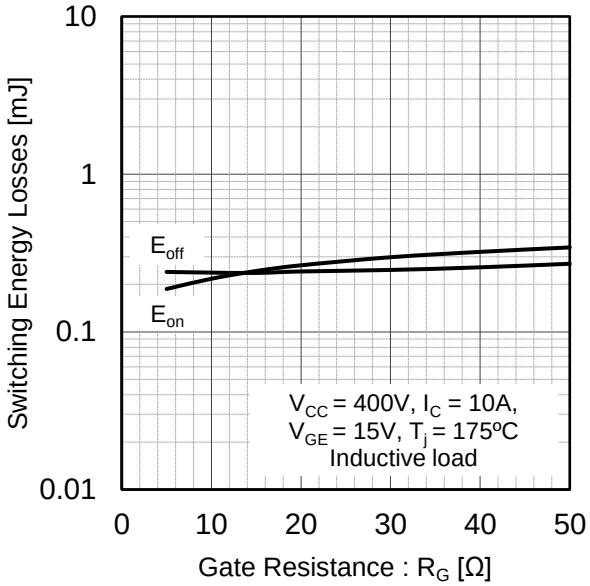


Fig.15 Typical Capacitance vs. Collector to Emitter Voltage

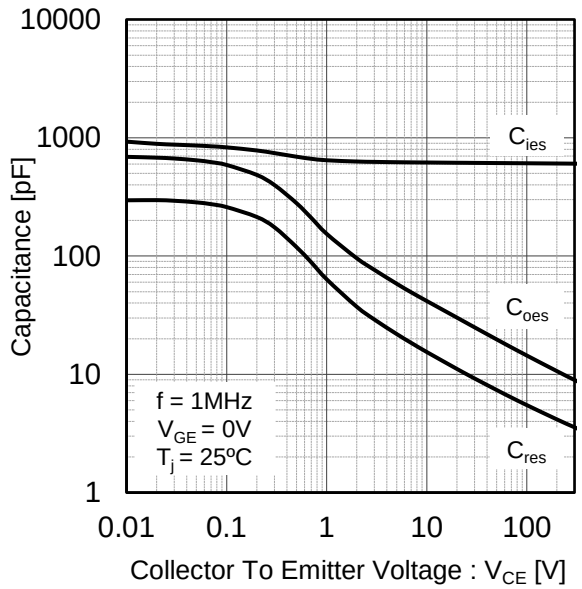
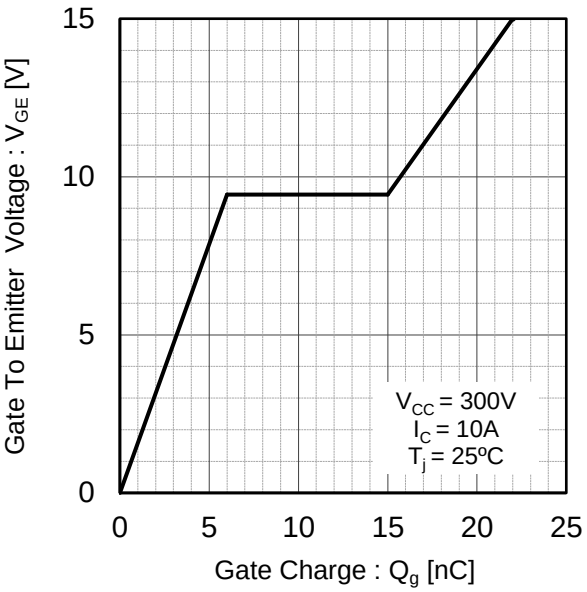


Fig.16 Typical Gate Charge

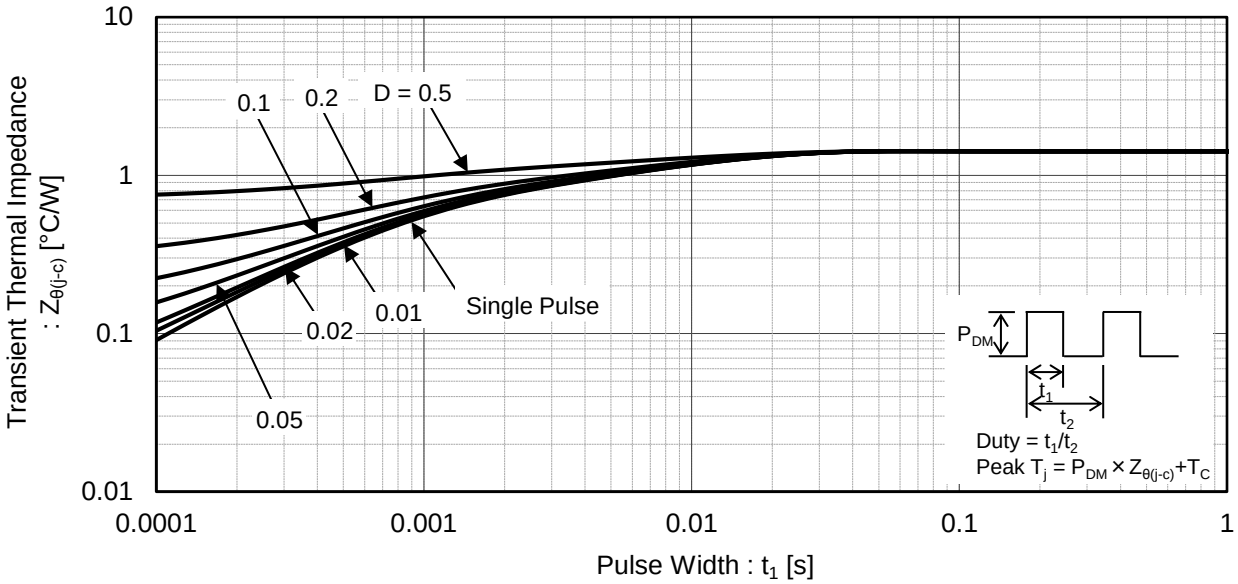


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●Electrical Characteristic Curves

Fig.17 IGBT Transient Thermal Impedance



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●Inductive Load Switching Circuit and Waveform

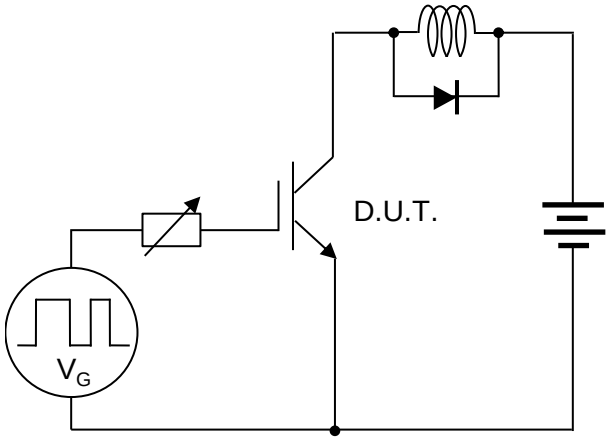


Fig.18 Inductive Load Circuit

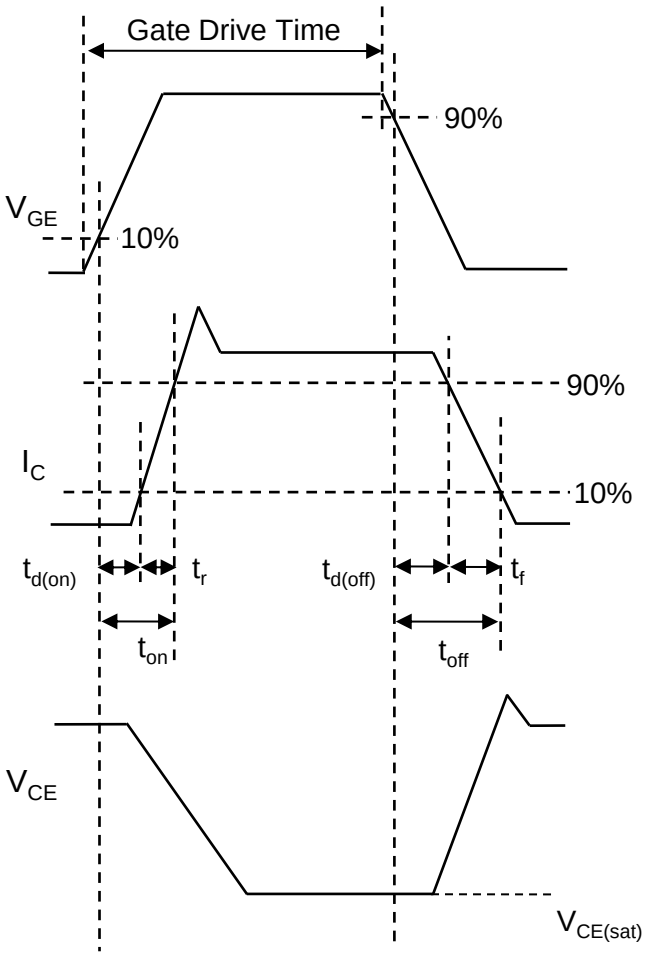


Fig.19 Inductive Load Waveform

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