

## P-Channel 30-V (D-S) MOSFET

### PRODUCT SUMMARY

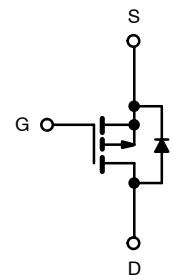
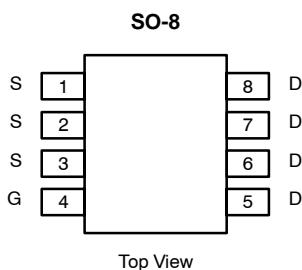
$V_{DS}$ (V)	$r_{DS(on)}$ ( $\Omega$ )	$I_D$ (A)	$Q_g$ (Typ)
-30	0.018 @ $V_{GS} = -10$ V	-9.6	-25
	0.030 @ $V_{GS} = -4.5$ V	-7.5	

### FEATURES

- TrenchFET® Power MOSFET
- Advanced High Cell Density Process
- 100%  $R_g$  Tested

### APPLICATIONS

- Load Switches
  - Notebook PCs
  - Desktop PCs



P-Channel MOSFET

Ordering Information: Si4835BDY  
 Si4835BDY-T1 (with Tape and Reel)  
 Si4835BDY—E3 (Lead (Pb)-Free)  
 Si4835BDY-T1—E3 (Lead (Pb)-Free with Tape and Reel)

### ABSOLUTE MAXIMUM RATINGS ( $T_A = 25^\circ\text{C}$ UNLESS OTHERWISE NOTED)

Parameter	Symbol	10 secs	Steady State	Unit
Drain-Source Voltage	$V_{DS}$	-30	$\pm 25$	V
Gate-Source Voltage	$V_{GS}$			
Continuous Drain Current ( $T_J = 150^\circ\text{C}$ ) <sup>a</sup>	$I_D$	-9.6	-7.4	A
		-7.7	-5.9	
Pulsed Drain Current	$I_{DM}$	-50		
continuous Source Current (Diode Conduction) <sup>a</sup>	$I_S$	-2.1	-1.3	
Maximum Power Dissipation <sup>a</sup>	$P_D$	2.5	1.5	W
		1.6	0.9	
Operating Junction and Storage Temperature Range	$T_J, T_{stg}$	-55 to 150		°C

### THERMAL RESISTANCE RATINGS

Parameter	Symbol	Typical	Maximum	Unit
Maximum Junction-to-Ambient <sup>a</sup>	$R_{thJA}$	39	50	°C/W
		70	85	
Maximum Junction-to-Foot (Drain)	$R_{thJF}$	18	22	

Notes

a. Surface Mounted on 1" x 1" FR4 Board.

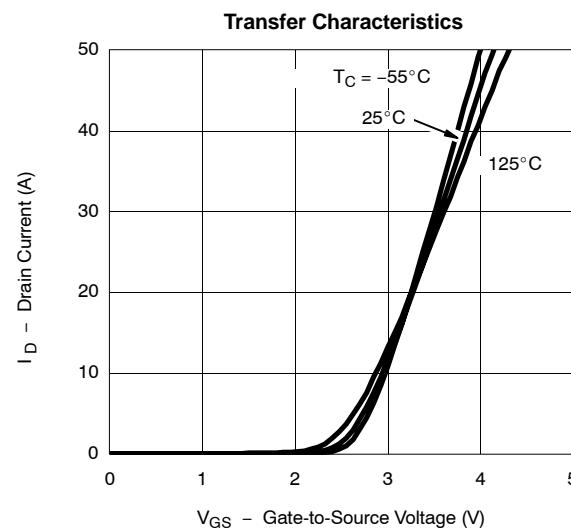
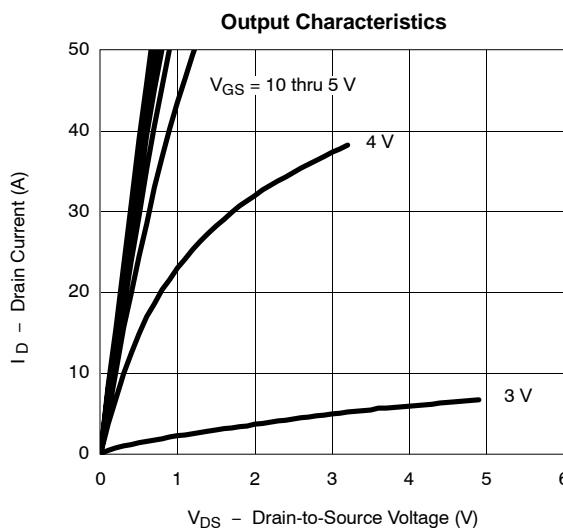
**SPECIFICATIONS ( $T_J = 25^\circ\text{C}$  UNLESS OTHERWISE NOTED)**

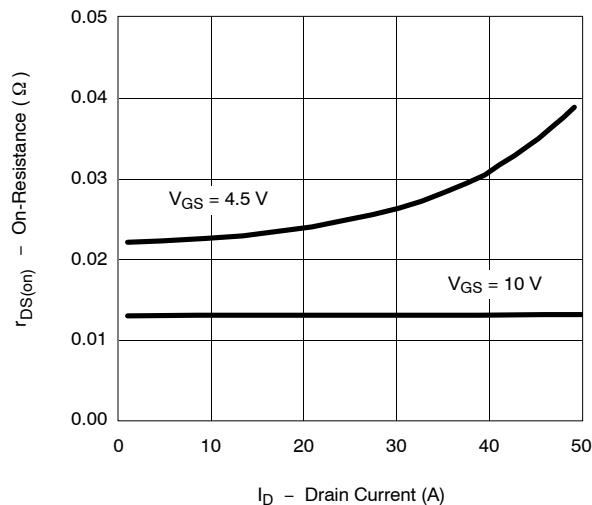
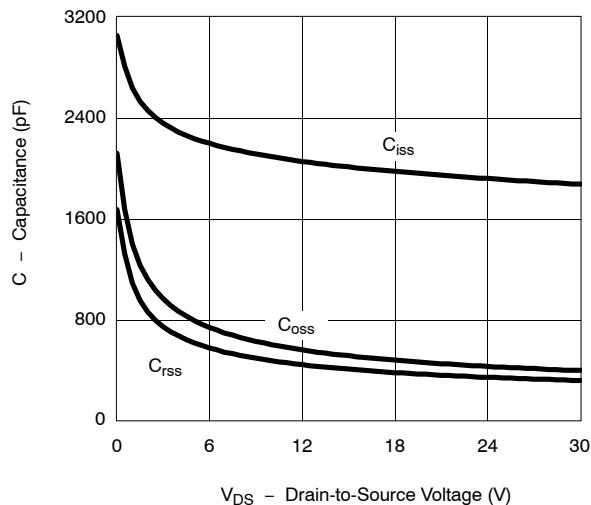
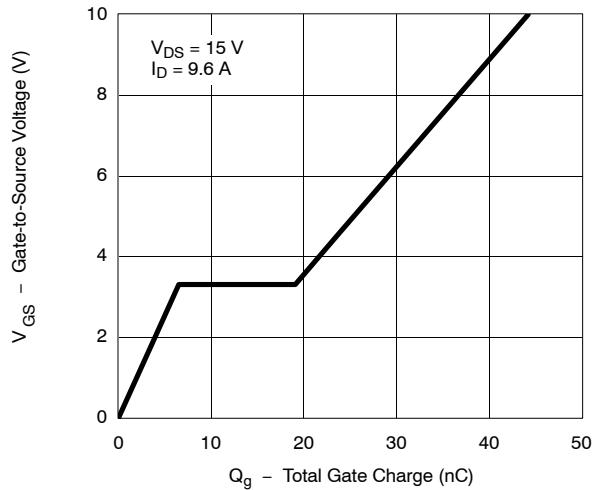
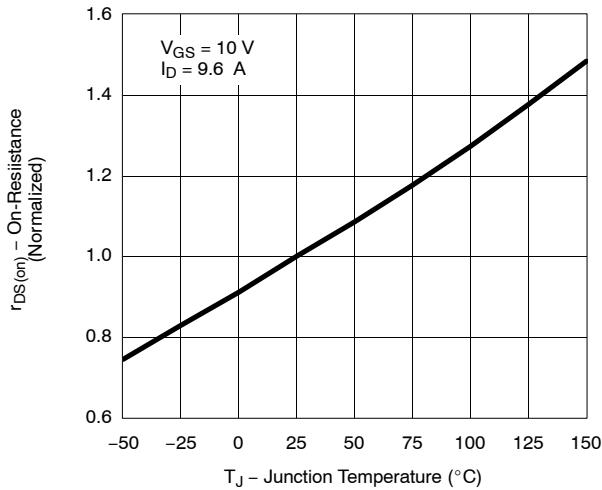
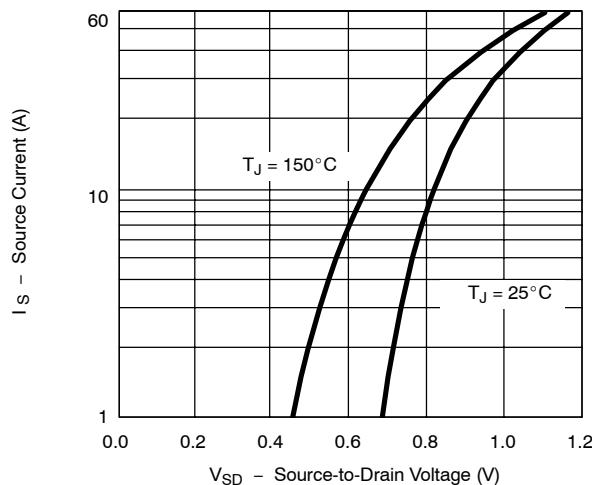
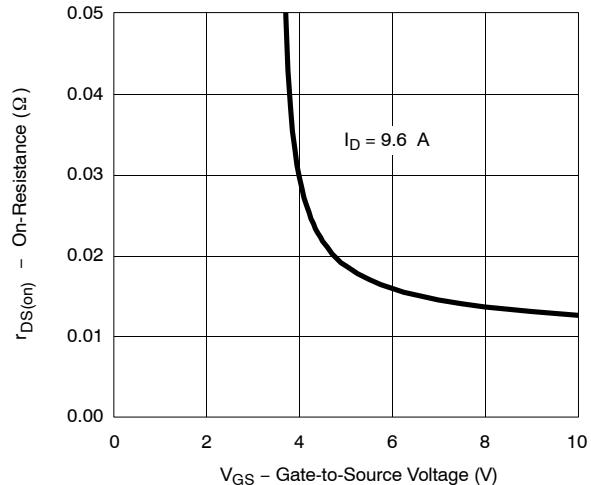
Parameter	Symbol	Test Condition	Min	Typ	Max	Unit
<b>Static</b>						
Gate Threshold Voltage	$V_{GS(\text{th})}$	$V_{DS} = V_{GS}$ , $I_D = -250 \mu\text{A}$	-1.0		-3.0	V
Gate-Body Leakage	$I_{GSS}$	$V_{DS} = 0 \text{ V}$ , $V_{GS} = \pm 25 \text{ V}$			$\pm 100$	nA
Zero Gate Voltage Drain Current	$I_{DSS}$	$V_{DS} = -30 \text{ V}$ , $V_{GS} = 0 \text{ V}$		-1		$\mu\text{A}$
		$V_{DS} = -30 \text{ V}$ , $V_{GS} = 0 \text{ V}$ , $T_J = 55^\circ\text{C}$		-5		
On-State Drain Current <sup>a</sup>	$I_{D(\text{on})}$	$V_{DS} \leq -5 \text{ V}$ , $V_{GS} = -10 \text{ V}$	-50			A
Drain-Source On-State Resistance <sup>a</sup>	$r_{DS(\text{on})}$	$V_{GS} = -10 \text{ V}$ , $I_D = -9.6 \text{ A}$		0.014	0.018	$\Omega$
		$V_{GS} = -4.5 \text{ V}$ , $I_D = -7.5 \text{ A}$		0.023	0.030	
Forward Transconductance <sup>a</sup>	$g_{fs}$	$V_{DS} = -15 \text{ V}$ , $I_D = -9.6 \text{ A}$		30		S
Diode Forward Voltage <sup>a</sup>	$V_{SD}$	$I_S = -2.1 \text{ A}$ , $V_{GS} = 0 \text{ V}$		-0.8	-1.2	V
<b>Dynamic<sup>b</sup></b>						
Total Gate Charge	$Q_g$	$V_{DS} = -15 \text{ V}$ , $V_{GS} = -5 \text{ V}$ , $I_D = -9.6 \text{ A}$		25	37	nC
Gate-Source Charge	$Q_{gs}$			6.5		
Gate-Drain Charge	$Q_{gd}$			12.5		
Gate Resistance	$R_g$		1.0	2.9	4.9	$\Omega$
Turn-On Delay Time	$t_{d(\text{on})}$	$V_{DD} = -15 \text{ V}$ , $R_L = 15 \Omega$ $I_D \approx -1 \text{ A}$ , $V_{GEN} = -10 \text{ V}$ , $R_g = 6 \Omega$		15	25	ns
Rise Time	$t_r$			13	20	
Turn-Off Delay Time	$t_{d(\text{off})}$			60	100	
Fall Time	$t_f$			45	70	
Source-Drain Reverse Recovery Time	$t_{rr}$		$I_F = -2.1 \text{ A}$ , $di/dt = 100 \text{ A}/\mu\text{s}$	45	80	

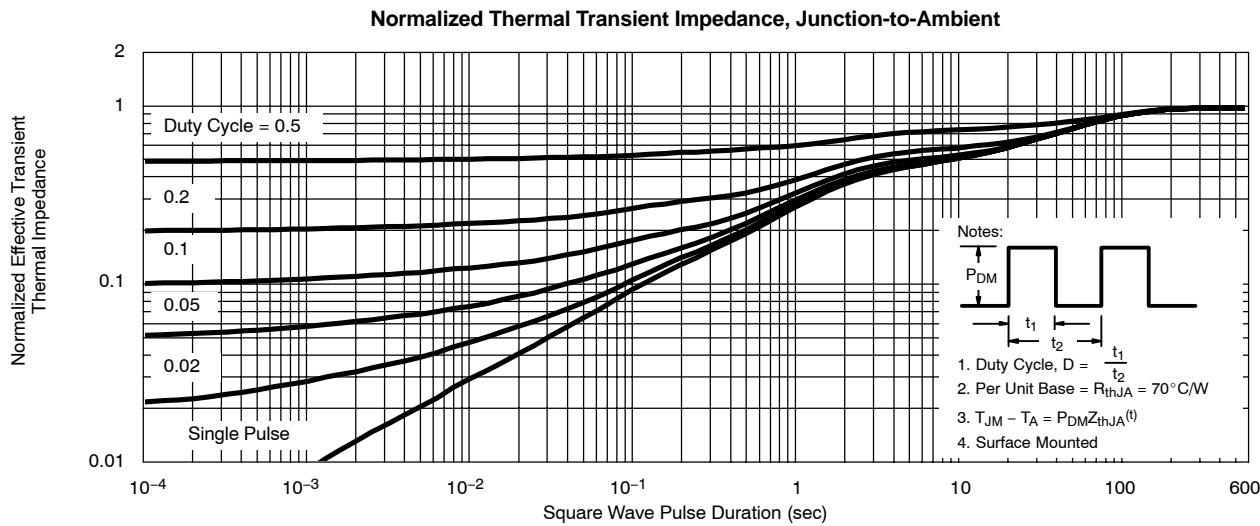
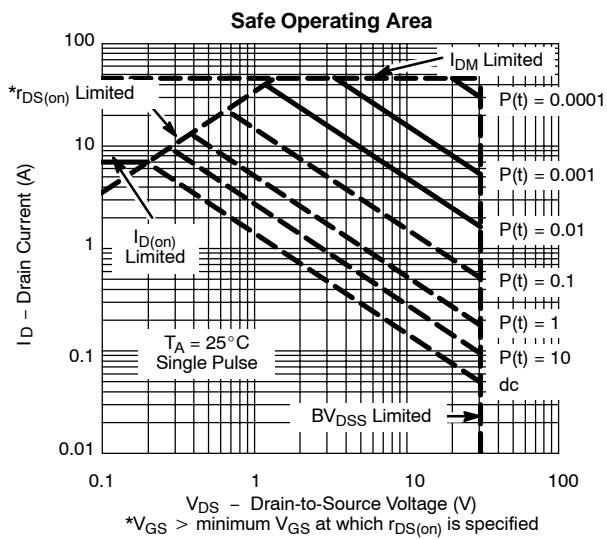
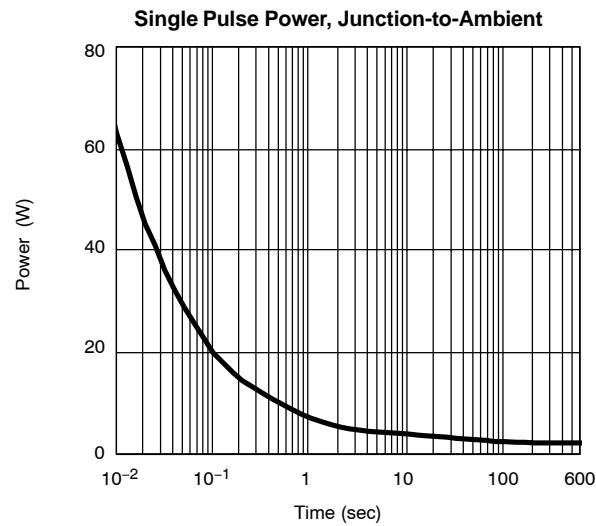
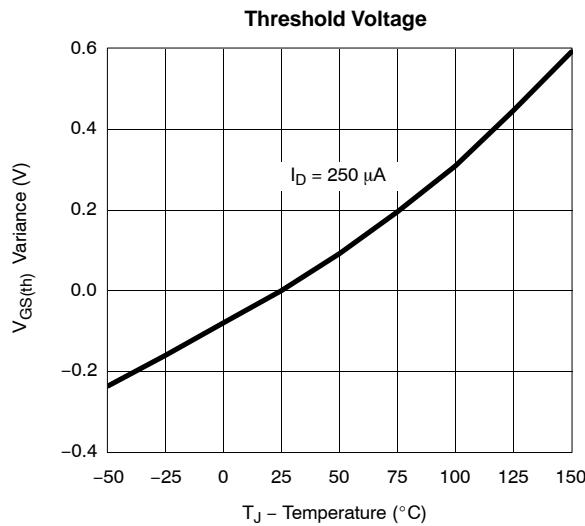
## Notes

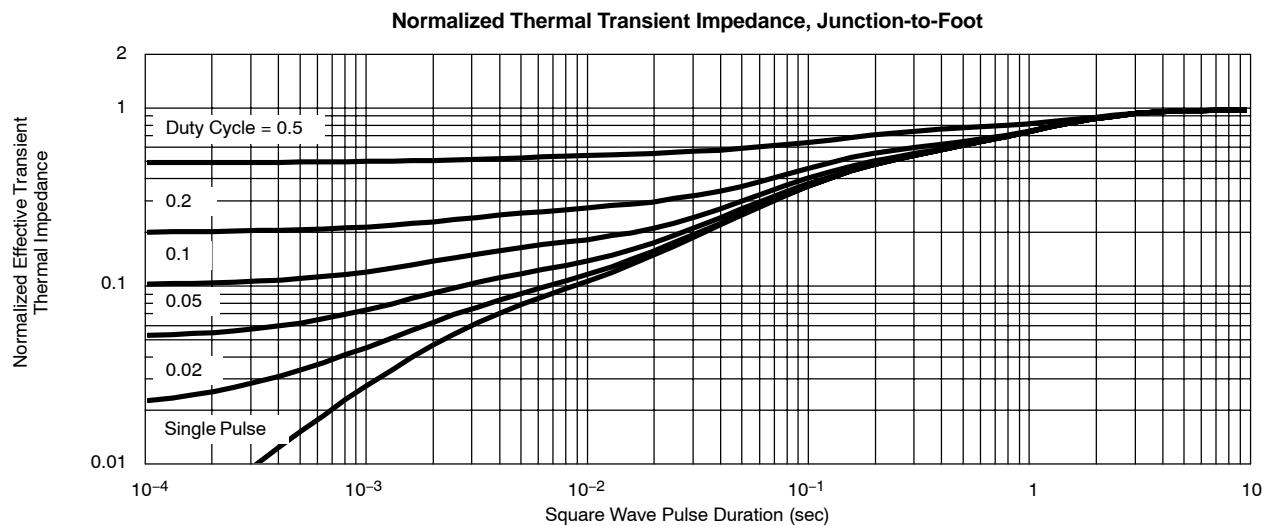
- a. Pulse test; pulse width  $\leq 300 \mu\text{s}$ , duty cycle  $\leq 2\%$ .  
 b. Guaranteed by design, not subject to production testing.

Stresses beyond those listed under "Absolute Maximum Ratings" may cause permanent damage to the device. These are stress ratings only, and functional operation of the device at these or any other conditions beyond those indicated in the operational sections of the specifications is not implied. Exposure to absolute maximum rating conditions for extended periods may affect device reliability.

**TYPICAL CHARACTERISTICS (25°C UNLESS NOTED)**


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**On-Resistance vs. Drain Current**

**Capacitance**

**Gate Charge**

**On-Resistance vs. Junction Temperature**

**Source-Drain Diode Forward Voltage**

**On-Resistance vs. Gate-to-Source Voltage**


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