Dual P-Channel 30-V (D-S) MOSFET

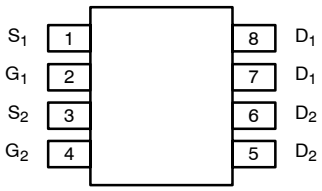
PRODUCT SUMMARY		
V_{DS} (V)	$r_{DS(on)}$ (Ω)	I_D (A)
–30	0.032 @ V_{GS} = –10 V	–6.3
	0.045 @ V_{GS} = –4.5 V	–5.3

FEATURES

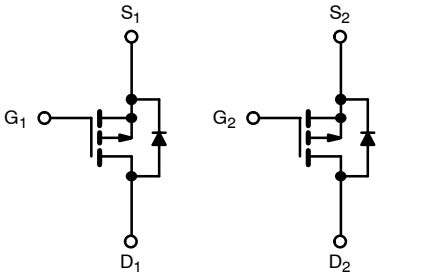
- TrenchFET® Power MOSFET

Pb-free Available

SO-8



Top View



P-Channel MOSFET P-Channel MOSFET

Ordering Information: Si4925DY
Si4925DY-T1 (with Tape and Reel)
: Si4925DY—E3 (Lead (Pb)-Free)
Si4925DY-T1—E3 (Lead (Pb)-Free with Tape and Reel)

ABSOLUTE MAXIMUM RATINGS (T_A = 25°C UNLESS OTHERWISE NOTED)					
Parameter		Symbol	10 secs	Steady State	Unit
Drain-Source Voltage		V_{DS}	–30		V
Gate-Source Voltage		V_{GS}	±20		
Continuous Drain Current (T_J = 150°C)[a]	T_A = 25°C	I_D	–6.3	–4.7	A
	T_A = 70°C		–5.0	–3.7	
Pulsed Drain Current		I_{DM}	–40		
Continuous Source Current (Diode Conduction)[a]		I_S	–1.7	–0.9	
Maximum Power Dissipation[a]	T_A = 25°C	P_D	2	1.1	W
	T_A = 70°C		1.3	0.70	
Operating Junction and Storage Temperature Range		T_J, T_{stg}	–55 to 150		°C

THERMAL RESISTANCE RATINGS					
Parameter		Symbol	Typ	Max	Unit
Maximum Junction-to-Ambient[a]	t ≤ 10 sec	R_{thJA}	45	62.5	°C/W
	Steady-State		85	110	
Maximum Junction-to-Foot (Drain)	Steady-State	R_{thJF}	28	35	

Notes
a. Surface Mounted on FR4 Board, t ≤ 10 sec.

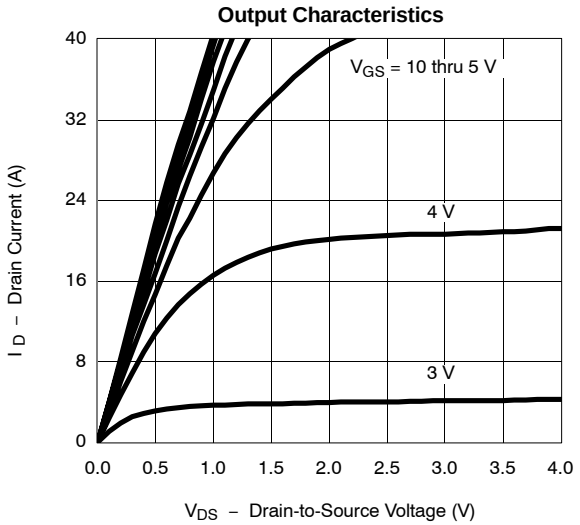
SPECIFICATIONS (T_J = 25°C UNLESS OTHERWISE NOTED)

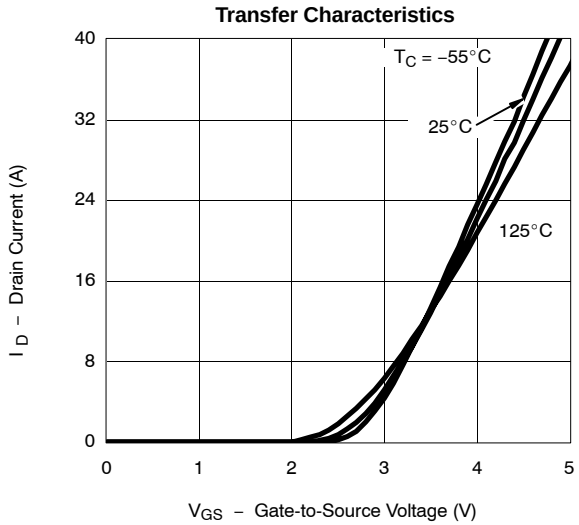
Parameter	Symbol	Test Condition	Min	Typ[a]	Max	Unit
Static						
Gate Threshold Voltage	$V_{GS(th)}$	V_{DS} = V_{GS}, I_D = −250 μA	−1		−3	V
Gate-Body Leakage	I_{GSS}	V_{DS} = 0 V, V_{GS} = ±20 V			±100	nA
Zero Gate Voltage Drain Current	I_{DSS}	V_{DS} = −30 V, V_{GS} = 0 V			−1	μA
		V_{DS} = −30 V, V_{GS} = 0 V, T_J = 55°C			−25	
On-State Drain Current[b]	$I_{D(on)}$	V_{DS} ≤ −5 V, V_{GS} = −10 V	−20			A
Drain-Source On-State Resistance[b]	$r_{DS(on)}$	V_{GS} = −10 V, I_D = −6.3 A		0.024	0.032	Ω
		V_{GS} = −4.5 V, I_D = −5.3 A		0.036	0.045	
Forward Transconductance[b]	g_{fs}	V_{DS} = −15 V, I_D = −6.3 A		14		S
Diode Forward Voltage[b]	V_{SD}	I_S = −1.7 A, V_{GS} = 0 V		−0.8	−1.2	V
Dynamic[a]						
Total Gate Charge	Q_g	V_{DS} = −15 V, V_{GS} = −10 V, I_D = −6.3 A		27	50	nC
Gate-Source Charge	Q_{gs}			6		
Gate-Drain Charge	Q_{gd}			4.5		
Turn-On Delay Time	$t_{d(on)}$	V_{DD} = −15 V, R_L = 15 Ω I_D ≅ −1 A, V_{GEN} = −10 V, R_g = 6 Ω		16	20	ns
Rise Time	t_r			10	20	
Turn-Off Delay Time	$t_{d(off)}$			55	80	
Fall Time	t_f			20	40	
Source-Drain Reverse Recovery Time	t_{rr}	I_F = −1.7 A, di/dt = 100 A/μs		40	90	

Notes
a. For design aid only; not subject to production testing.
b. Pulse test; pulse width ≤ 300 μs, duty cycle ≤ 2%.

Stresses beyond those listed under "Absolute Maximum Ratings" may cause permanent damage to the device. These are stress ratings only, and functional operation of the device at these or any other conditions beyond those indicated in the operational sections of the specifications is not implied. Exposure to absolute maximum rating conditions for extended periods may affect device reliability.

TYPICAL CHARACTERISTICS (25°C UNLESS NOTED)





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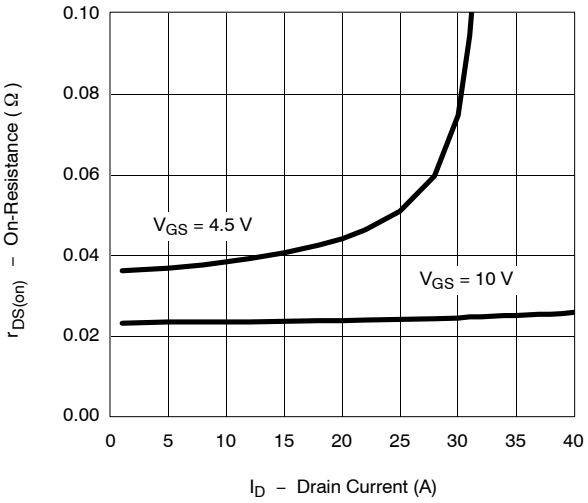
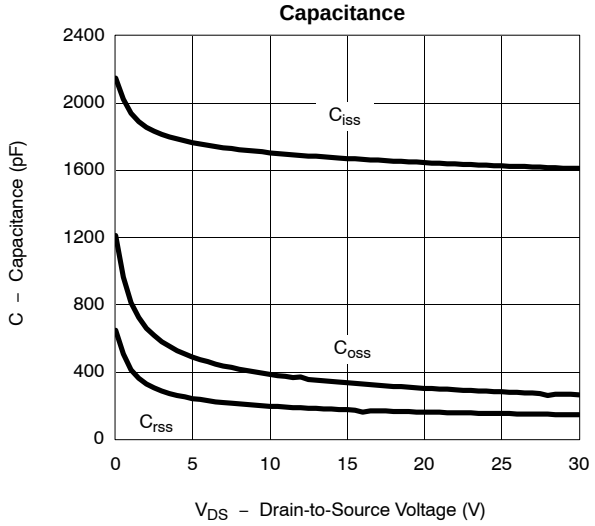
TYPICAL CHARACTERISTICS (25°C UNLESS NOTED)

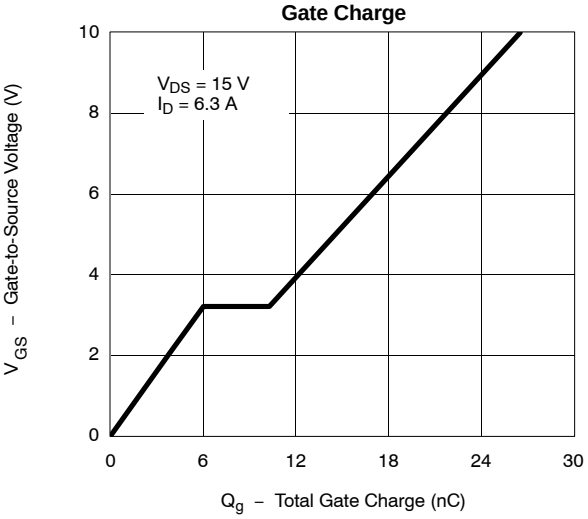
On-Resistance vs. Drain Current



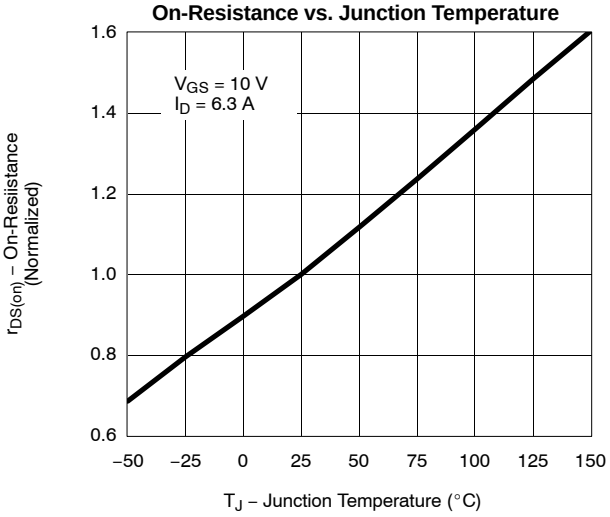
Capacitance



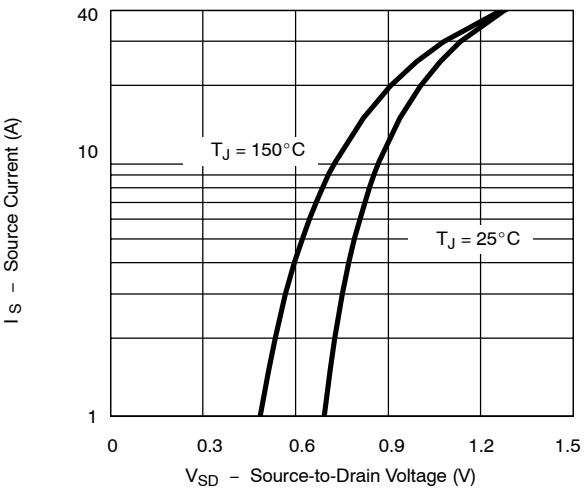
Gate Charge



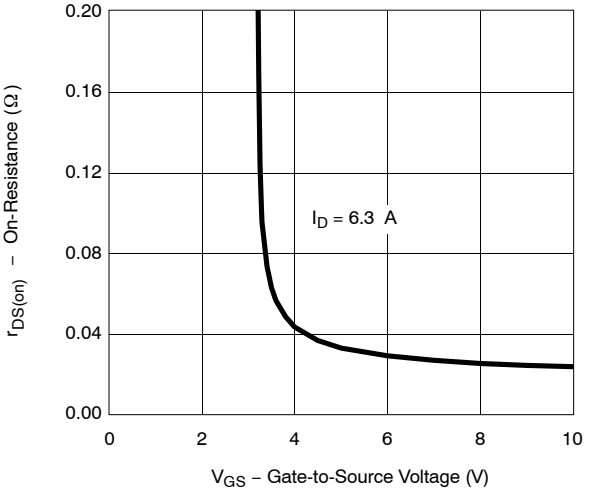
On-Resistance vs. Junction Temperature



Source-Drain Diode Forward Voltage



On-Resistance vs. Gate-to-Source Voltage



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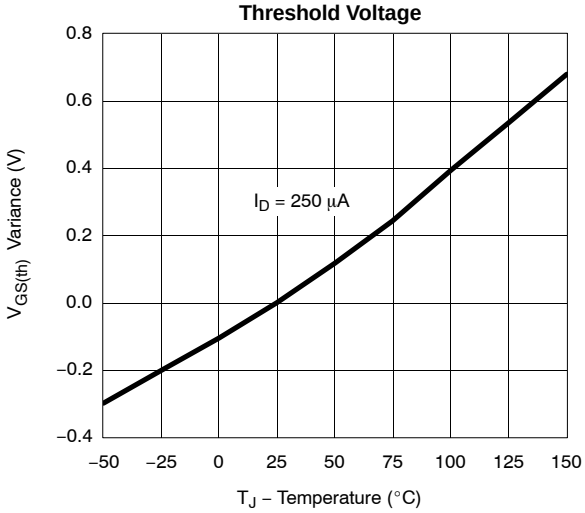

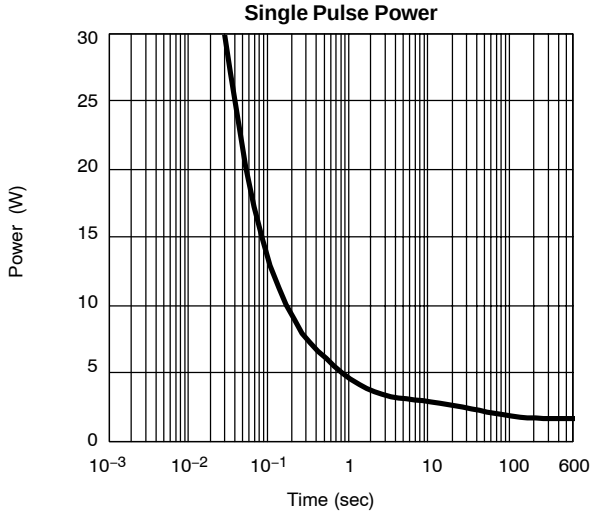

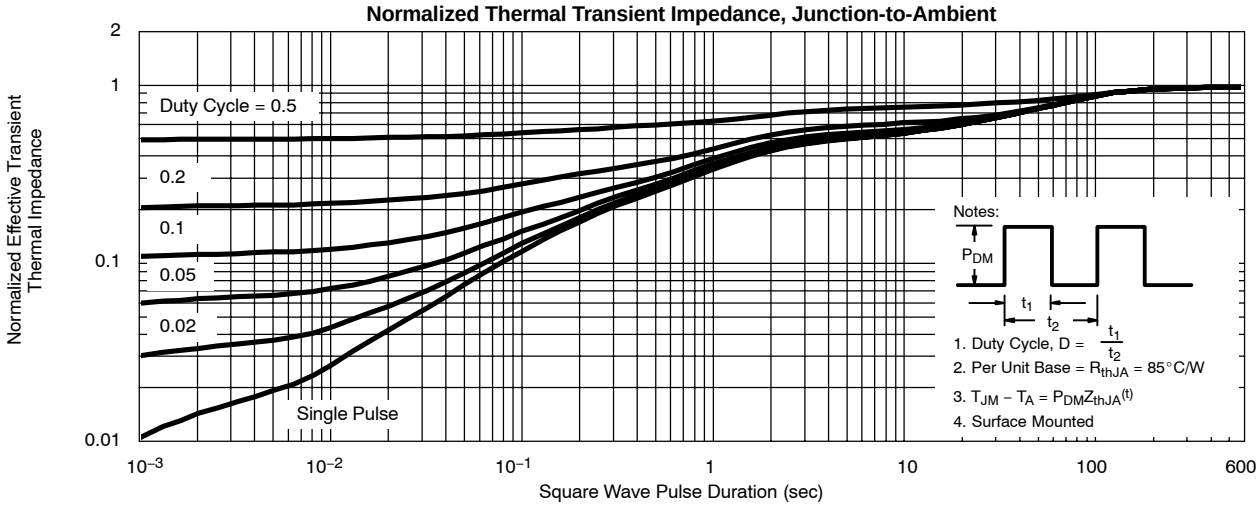

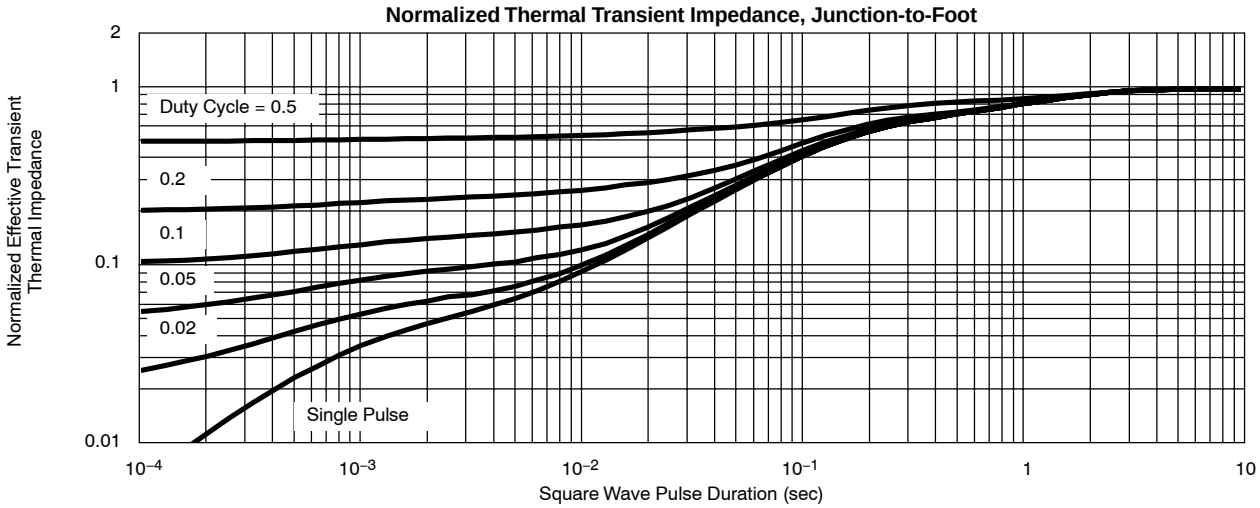

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