

KXCJK Evaluation Board

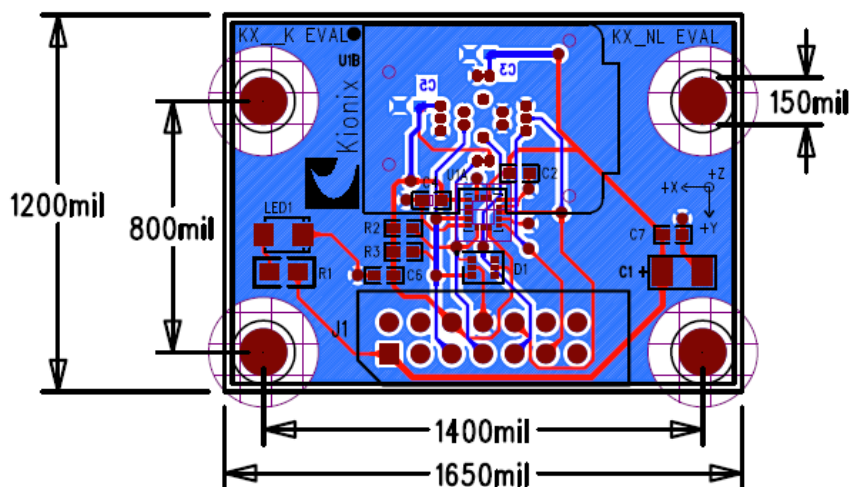


Part Number: EVAL-KXCJK-1013

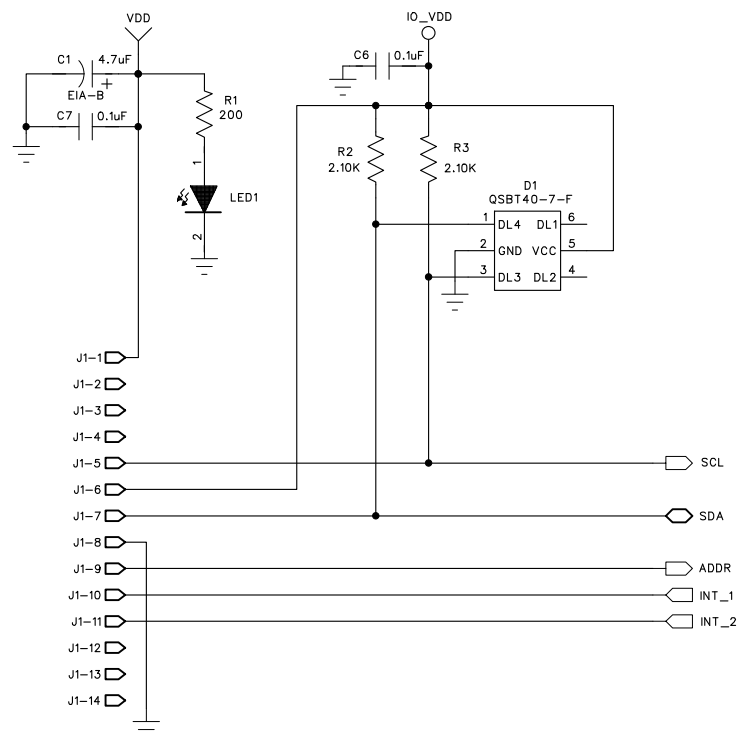
- Range: 2g, 4g, 8g, User-selectable
- Sensitivity: 1024 (14-bit), 64, 32, 16 (8-bit), 1024, 512, 256 (12-bit), (counts/g)
- Temperature (°C): -40 to +85

PLEASE SEE THE FOLLOWING PAGES FOR THE BOARD LAYOUT AND SCHEMATIC

KXCJK Evaluation Board Layout and Pin Description

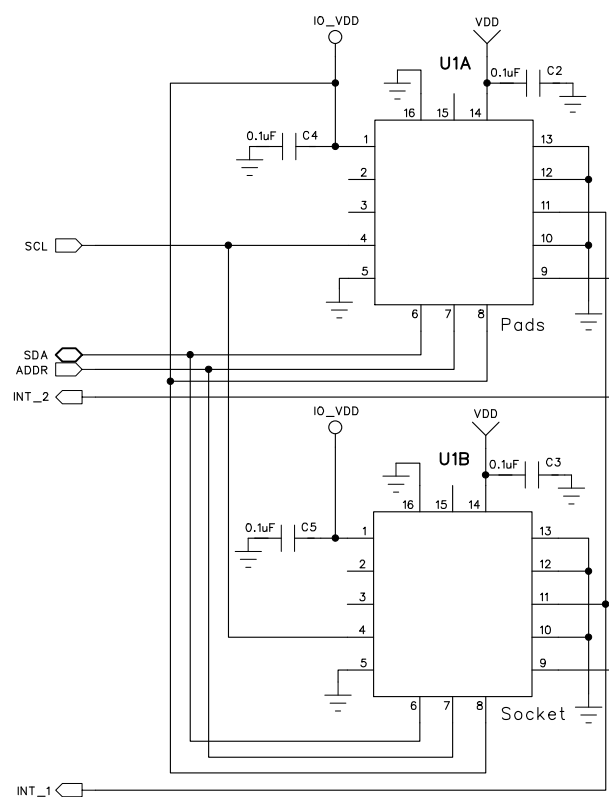


J1 Connector Pin Description	
pin 1	VDD
pin 2	DNC
pin 3	DNC
pin 4	DNC
pin 5	SCL
pin 6	IOVDD
pin 7	SDA
pin 8	GND
pin 9	ADDR
pin 10	INT
pin 11	DNC
pin 12	DNC
pin 13	DNC
pin 14	DNC



NOTES:

- 1) All resistors are 1%, 1/10W, 0603 unless otherwise specified.
- 2) All capacitors are 0.1uF, 10%, 16V, 0603 unless otherwise specified.
- 3) U1 LGA: Populate C2 and C4.
- 4) U2 Socket: Populate C3 and C5; mounted on solder copper side.
- 5) Supported devices: KX_IK, KX_KK, KX_NL



COMPANY:

Kionix Inc.

TITLE:

KX__K/KX_NL Evaluation Board

DRAWN: Bob Ross	DATED: 4/20/12
CHECKED:	DATED:
QUALITY CONTROL:	DATED:
RELEASED:	DATED:

CODE: DWG794	SIZE: B	DRAWING NO: KAMMDM001R55	REV: 0
SCALE: NONE			SHEET: 1 OF 2

	6	5	4	3	2	1
D	REVISION RECORD					
	DATE:	DRAWN BY:	DESCRIPTION:			CHECKED BY: DATE:
	4/20/12	Bob Ross	Initial release.			
C						
B						
A	LOG01 Kionix Inc. LOGO — MTHOLE1 ⊕ MTHOLE150 MTHOLE2 ⊕ MTHOLE150 MTHOLE3 ⊕ MTHOLE150 MTHOLE4 ⊕ MTHOLE150					COMPANY:
	DRAWN: Bob Ross CHECKED: QUALITY CONTROL: RELEASED: DATED: 4/20/12 DATED: DATED: DATED:					TITLE: KX__K/KX_NL Evaluation Board CODE: DWG794 SCALE: NONE
	SIZE: B DRAWING NO: KAMMDM001R55 REV: 0 SHEET: 2 OF 2					