



DMP31D0U

30V P-CHANNEL ENHANCEMENT MODE MOSFET

Product Summary

BV _{DSS}	Max R _{DS(ON)}	Max I _D @ T _A = 25°C
	1Ω @ $V_{GS} = -4.5V$	-0.67A
-30V	1.5Ω @ V _{GS} = -2.5V	-0.54A
	2Ω @ V _{GS} = -1.8V	-0.47A

Description and Applications

This MOSFET is designed to minimize the on-state resistance (R_{DS(ON)}), yet maintain superior switching performance, making it ideal for high efficiency power management applications.

Load Switch in Portable Electronics

SOT23





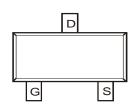
Top View

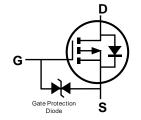
Features and Benefits

- Low Gate Threshold Voltage
- Fast Switching Speed
- ESD Protected Gate
- Totally Lead-Free & Fully RoHS Compliant (Notes 1 & 2)
- Halogen and Antimony Free. "Green" Device (Note 3)
- Qualified to AEC-Q101 Standards for High Reliability

Mechanical Data

- Case: SOT23
- Case Material: Molded Plastic, "Green" Molding Compound.
 UL Flammability Classification Rating 94V-0
- Moisture Sensitivity: Level 1 per J-STD-020
- Terminals: Finish Matte Tin Annealed over Copper Leadframe.
 Solderable per MIL-STD-202, Method 208 (3)
- Terminals Connections: See Diagram Below
- Weight: 0.009 grams (Approximate)





Top View Internal Schematic

Equivalent Circuit

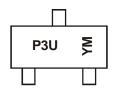
Ordering Information (Note 4)

Ī	Part Number	Case	Packaging
	DMP31D0U-7	SOT23	3,000/Tape & Reel

Notes:

- 1. No purposely added lead. Fully EU Directive 2002/95/EC (RoHS) & 2011/65/EU (RoHS 2) compliant.
- See http://www.diodes.com/quality/lead_free.html for more information about Diodes Incorporated's definitions of Halogen- and Antimony-free, "Green" and Lead-free.
- 3. Halogen- and Antimony-free "Green" products are defined as those which contain <900ppm bromine, <900ppm chlorine (<1500ppm total Br + Cl) and <1000ppm antimony compounds.
- 4. For packaging details, go to our website at http://www.diodes.com/products/packages.html

Marking Information



P3U = Product Type Marking Code YM = Date Code Marking Y = Year (ex: Y = 2011) M = Month (ex: 9 = September)

Date Code Key

Year	2011	~	2016	20	17	2018	2019	2020	202	21 :	2022	2023
Code	Υ	~	D	Е	Ē	F	G	Н	1		J	K
Month	Jan	Feb	Mar	Apr	May	Jun	Jul	Aug	Sep	Oct	Nov	Dec
Code	1	2	3	4	5	6	7	8	9	0	N	D



Maximum Ratings (@ $T_A = +25$ °C, unless otherwise specified.)

Cha	aracteristic		Symbol	Value	Unit
Drain-Source Voltage			V_{DSS}	-30	V
Gate-Source Voltage			V_{GSS}	±8	V
Continuous Drain Current	Steady State	$T_A = +25$ °C (Note 6) $T_A = +85$ °C (Note 6) $T_A = +25$ °C (Note 5)	I _D	-0.67 -0.48 -0.53	А
Pulsed Drain Current (Note 7)	1		I _{DM}	2.5	A

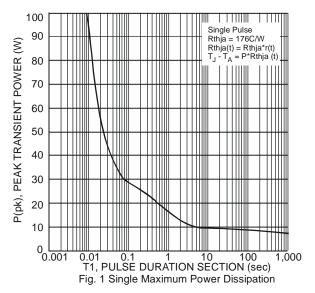
Thermal Characteristics (@TA = +25°C, unless otherwise specified.)

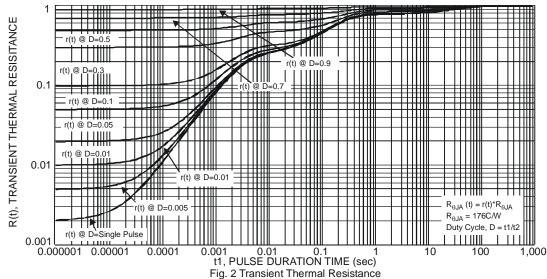
Characteristic	Symbol	Value	Unit	
Bower Dissinction	(Note 5)	D	0.45	W
Power Dissipation	(Note 6)	PD	0.71	W
Thermal Desistance, Junction to Ambient	(Note 5)	Б	275	°C/W
Thermal Resistance, Junction to Ambient	(Note 6)	$R_{ hetaJA}$	177	°C/W
Operating and Storage Temperature Range	T _J , T _{STG}	-55 to +150	°C	

Notes: 5. Device mounted on FR-4 PCB, with minimum recommended pad layout

6. Device mounted on 1" x 1" FR-4 PCB with high coverage 2oz. Copper, single sided.

7. Device mounted on minimum recommended pad layout test board, 10µs pulse duty cycle = 1%.







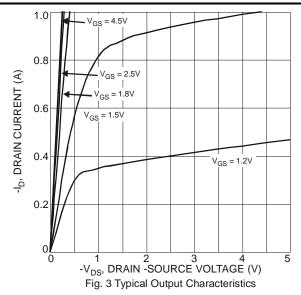
Electrical Characteristics (@T_A = +25°C, unless otherwise specified.)

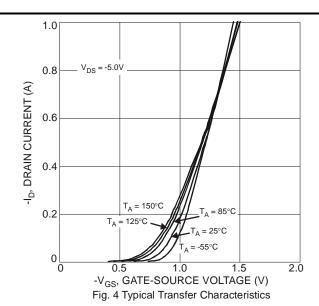
Characteristic	Symbol	Min	Тур	Max	Unit	Test Condition
OFF CHARACTERISTICS (Note 8)						
Drain-Source Breakdown Voltage	BV _{DSS}	-30	_	_	V	$V_{GS} = 0V, I_D = -250\mu A$
Zero Gate Voltage Drain Current T _J = 25°C	I _{DSS}	_	_	-1	μΑ	$V_{DS} = -30V, V_{GS} = 0V$
Gate-Source Leakage	I _{GSS}	_	_	±3	μΑ	$V_{GS} = \pm 8V$, $V_{DS} = 0V$
ON CHARACTERISTICS (Note 8)						
Gate Threshold Voltage	V _{GS(TH)}	-0.5	_	-1.1	V	$V_{DS} = V_{GS}, I_{D} = -250 \mu A$
				1		$V_{GS} = -4.5V$, $I_D = -400mA$
Static Drain-Source On-Resistance	R _{DS (ON)}	_	_	1.5	Ω	$V_{GS} = -2.5V$, $I_D = -200mA$
				2		$V_{GS} = -1.8V, I_D = -100mA$
Forward Transfer Admittance	Y _{FS}	50	_	_	mS	$V_{DS} = -3V, I_{D} = -300mA$
Diode Forward Voltage	V_{SD}	_	_	-1.2	V	$V_{GS} = 0V, I_{S} = -300mA$
DYNAMIC CHARACTERISTICS (Note 9)						
Input Capacitance	C _{ISS}	_	76	150	pF	\
Output Capacitance	Coss	_	9	_	pF	$V_{DS} = -15V, V_{GS} = 0V,$ -f = 1.0MHz
Reverse Transfer Capacitance	C _{RSS}	_	6.43	_	pF	1 – 1.001112
Gate Resistance	R_{G}	_	167	_	Ω	$V_{DS} = 0V$, $V_{GS} = 0V$, $f = 1MHz$
Total Gate Charge	Q_{G}	_	0.9	_	nC	$V_{GS} = -4.5V$, $V_{DS} = -15V$, $I_{D} = -1A$
Total Gate Charge	Q_{G}	_	1.5	_	nC	\/ 0\/ \/ 45\/
Gate-Source Charge	Q _{GS}	_	0.1	_	nC	$V_{GS} = -8V, V_{DS} = -15V,$ $I_{D} = -1A$
Gate-Drain Charge	Q_{GD}	_	0.2	_	nC	ID = - IA
Turn-On Delay Time	t _{D(ON)}	_	5.0	_	ns	
Turn-On Rise Time	t _R	_	5.9	_	ns	$V_{DD} = -10V, R_{L} = 10\Omega$
Turn-Off Delay Time	t _{D(OFF)}	_	35.7	_	ns	$V_{GS} = -4.5V$, $R_G = 6\Omega$
Turn-Off Fall Time	t _F	_	16.7	_	ns	

Notes:

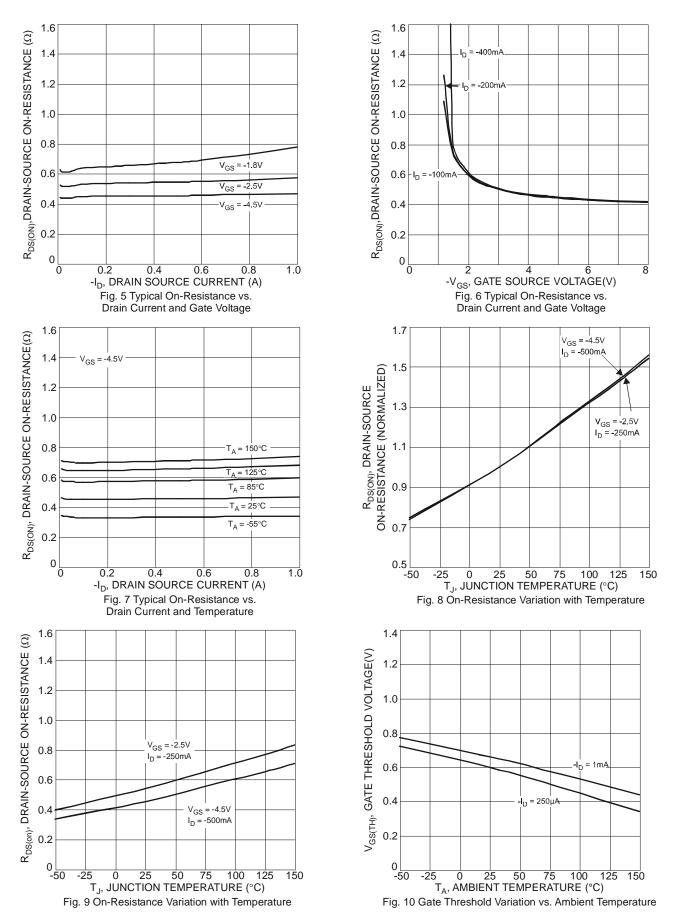
- 8. Short duration pulse test used to minimize self-heating effect. 9. Guaranteed by design. Not subject to product testing.

Typical Electrical Characteristics

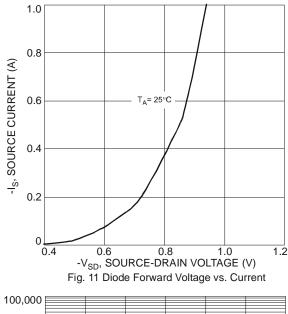












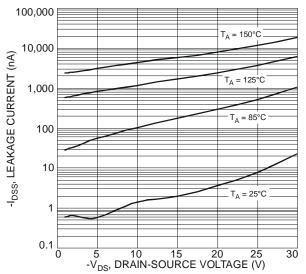
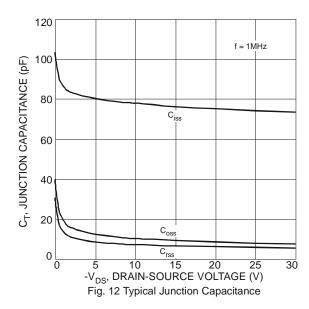
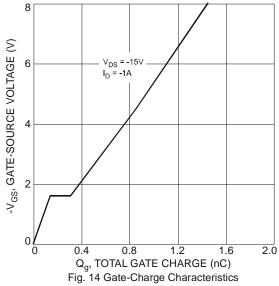


Fig. 13 Typical Drain-Source Leakage Current vs. Voltage



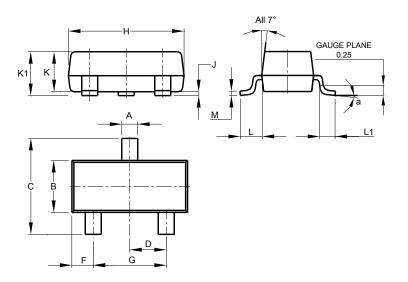




Package Outline Dimensions

Please see http://www.diodes.com/package-outlines.html for the latest version.

SOT23

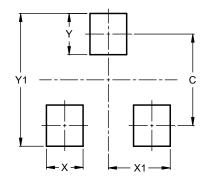


SOT23							
Dim	Min	Max	Тур				
Α	0.37	0.51	0.40				
В	1.20	1.40	1.30				
С	2.30	2.50	2.40				
D	0.89	1.03	0.915				
F	0.45	0.60	0.535				
G	1.78	2.05	1.83				
Н	2.80	3.00	2.90				
7	0.013	0.10	0.05				
K	0.890	1.00	0.975				
K1	0.903	1.10	1.025				
L	0.45	0.61	0.55				
L1	0.25	0.55	0.40				
М	0.085	0.150	0.110				
а	a 0° 8° –						
All Dimensions in mm							

Suggested Pad Layout

Please see http://www.diodes.com/package-outlines.html for the latest version.

SOT23



Dimensions	Value (in mm)			
С	2.0			
Х	0.8			
X1	1.35			
Y	0.9			
Y1	2.9			



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