

SAC57D54HCVLT :Part Detail

[Product Summary Page](#) | [General Information](#) | [Package Information](#) | [Environmental and Compliance Information](#) | [Manufacturing Information](#) | [Ordering Information](#) | [Operating Characteristics](#) |

General Information

Part Number	SAC57D54HCVLT
Description	ARM 32-bit MCU, Triple core, 4MB Flash, 1.3MB gRAM, 1MB sysRAM, -40/+105degC, LQFP 208
Product Line	G9F5756B
PTI	JBGE
Material Type	Tested Packaged Device
Life Cycle Description (code)	PRODUCT RAPID GROWTH
Status	Active
Application/Qualification Tier	AUTO; >=10 YEARS APPLICATION LIFE

Package Information

Package Type and Termination Count	LQFP 208
Package Description and Mechanical Drawing	LQFPEP 208 28SQ P0.5
Device Weight(g)	2.59410
Package Length (nominal)(mm)	28.000
Package Width (nominal)(mm)	28.000
Package Thickness (nominal)(mm)	1.600
Tape & Reel	No

Environmental and Compliance Information

Pb-Free	
RoHS Compliant	
Halogen Free	Yes
Material Composition Declaration (MCD)	Download MCD Report  Download MCD Report 
RoHS Certificate of Analysis (CoA)	Contact Us
2nd Level Interconnect	e3
Moisture Sensitivity Level (MSL)	3
Floor Life	168 HOURS
Peak Package Body Temperature (PBT)(°C)	260

Maximum Time at Peak Temperature (s)	40
Number of Reflow Cycles	3
REACH SVHC	NXP REACH Statement
UL94 (plastics flammability test)	V0;Burning stops within 10 seconds on a vertical specimen;no drips allowed

Manufacturing Information

Micron Size(μm)	.05
-----------------	-----

Ordering Information

Minimum Package Quantity (MPQ)	180
MPQ Container	BRICK
Exempt from Minimum Delivery Value	Yes
Preferred Order Quantity (POQ)	360
POQ Container	BOX
Leadtime (weeks)	20
Export Control Classification Number (US)	5A002A
Harmonized Tariff (US) Disclaimer	8542.31.0000
CCATS Document	-
ENC Status	-
Other Trade Compliance Documents	-
Budgetary Price excluding tax(US\$) Change Currency	-

Order

 Buy Direct

[Check Buy Availability](#)

 Sample

[Check Sample Availability](#)

Operating Characteristics

Additional Features	MLB50 10/100
CAN	3
Core Type	ARM Cortex-A5 ARM Cortex-M0+ IOP ARM Cortex-M4
Core: Number of cores (Spec)	3
eDMA	2x 16ch
External Memory Supported	2x Dual DDR QuadSPI 16-Bit SDR DRAM (160mhz) 32-Bit DDR2 DRAM (320mhz)
I2C	2

Internal Flash (KB)	4000
Internal RAM (KB)	1300
L1 Cache	32/32 on A5
LinFlex D	3
Memory / Peripheral Protection	Yes
Operating Frequency (Max) (MHz)	320
Sample Exception Availability	Y
SPI	5
Timer/PWM	8ch PIT 3x SWT ARTC 4x 8ch Flextimer
Video/Display features	OpenVG 1.1 Segment LCD 2x 2D-ACE HUD Warping Engine Video Input Unit

ABOUT NXP

[Investors](#)

[Partners](#)

[Careers](#)

RESOURCES

[Mobile Apps](#)

[Press, News, Blogs](#)

[Contact Us](#)

FOLLOW US



NEWS

NXP News

[Read More](#)

