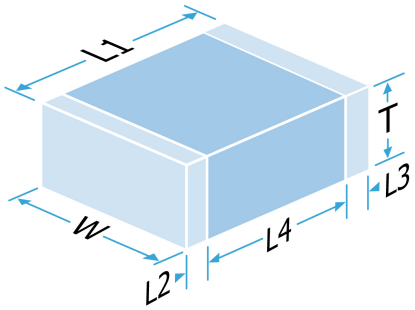
Multilayer Ceramic Chip Capacitor

Part Number: 1808YA250182KSTSYX

Description: 1808 250Vac (Y2), 305Vac (X1), 50/60Hz / 1000Vdc 1.8nF ±10% X7R (2R1) to AEC-Q200

Approval Specifications:	IEC/EN60384-14:2023 UL60384-14, CAN/CSA E60384-14:14
Certification:	TÜV R60186738 / ID1111314114 UL/cUL E228790-20210208
Classification:	IEC/EN 60384-14:2023 Class Y2 / X1 UL/cUL FOWX2, FOWX8



Component Marking and Certification Bodies:

Material Group I : CTI >= 600

Mechanical Specification

Size Code	1808
Length (L1) in mm (")	4.95 ± 0.35 (0.195 ± 0.014)
Width (W) in mm (")	2.0 ± 0.30 (0.08 ± 0.012)
Thickness (T) in mm (")	2.0 Max (0.08 Max)
Minimum Termination Band (L2,L3) in mm (")	0.30 (0.012)
Maximum Termination Band (L2,L3) in mm (")	0.80 (0.030)
Minimum Band Gap (L4) in mm (") (per IEC/EN 60384-14)	4.0 (0.158)
Termination Material	FlexiCap™ Polymer termination, Nickel barrier, Sn Plated Solder (RoHS compliant)
Solderability	IEC-60068-2-58
Packaging	7" Reel Horizontal Orientation, 1500 per reel

General Electrical Specification

Rated Voltage	Class Y2 (250Vac), Class X1 (305Vac), 50/60Hz, 5kV impulse
Humidity Grade	Grade IIIB (IEC/EN60384-14:2023 Annex I)
Maximum DC Working Voltage	1000Vdc to Annex H / (2500Vdc outside scope of any specification)
Nominal Capacitance Value	1.8nF
Capacitance Tolerance	±10%
Tangent of Loss Angle (Tan δ)	≤0.025
Capacitance and Tan δ Test Conditions	1.0Vrms @ 1kHz
Voltage Proof (50mA max charging current for DC tests. 50% Max, RH)	100% test: 4000Vdc 1s min / 5s max AQL test: 4000Vdc / 3000Vac 60s min / 5kV 1.2x50µs impulse
Min Insulation Resistance (IR)	100.00GOhm @ 100Vdc
Dielectric Classification	X7R (2R1) to AEC-Q200
Rated Temperature Range	-55°C / +125°C
Maximum Capacitance Change over Temperature Range	No DC Voltage ±15% Rated DC Voltage -
Climatic Category (IEC)	55/125/56
Ageing Characteristic	<2% per decade (nominal capacitance is 1000 hour value)

Knowles Precision Devices - Sales

Europe: KPD-Europe-sales@knowles.com

Asia: KPD-Asia-sales@knowles.com

USA: KPD-NA-sales@knowles.com

www.knowlescapacitors.com

This datasheet is for a standard item and is confirmed valid on the date generated, the latest published data for this part may differ and is available at http://www.knowlescapacitors.com or by contacting us.

© The information contained on this drawing is confidential and may not be copied in whole or part in any form or disclosed to a third party without the consent of Knowles and any customer mentioned within this specification.

Data is correct to the best of our knowledge, errors and omissions excepted.

Date: Saturday, November 21, 2026

20261121 005057274UTC

Downloaded from Arrow.com.

Multilayer Ceramic Chip Capacitor

Part Number: 1808YA250182KSTSYX	**Description:** 1808 250Vac (Y2), 305Vac (X1), 50/60Hz / 1000Vdc 1.8nF ±10% X7R (2R1) to AEC-Q200

Environmental

RoHS Compliant to 2011/65/EC as amended by 2015/863/EU	Compliant
REACH Compliant	253 compliant
California Proposition 65	No exposure risk

Board Layout

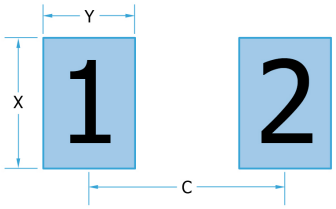
Knowles' conventional 2-terminal chip capacitors can generally be mounted using pad designs in accordance with international specification IPC-7351, Generic Requirements for Surface Mount Design and Land Pattern Standards, but there are some other factors that have been shown to reduce mechanical stress, such as reducing the pad width to less than the chip width. In addition, the position of the chip on the board should be considered.

Some high voltage parts may require modifications to the board layout and/or the addition of a conformal coating to prevent flashover, especially under high humidity conditions. Board cleanliness and environmental conditions can also impact this. Refer to application note AN0043 for further information.

Dimensions given are for guidance. It is ultimately the customers responsibility to confirm that the circuit layout is in accordance with their own product requirements.

IPC-7351 pad design

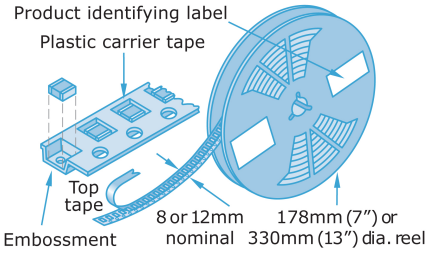
	1808	
C	5.35mm	0.211"
Y	1.25mm	0.049"
X	2.30mm	0.091"



Packaging

Tape packaging information for tape-and-reel parts:

Tape and reel packing of surface mounting chip capacitors for automatic placement are in accordance with IEC60286-3.

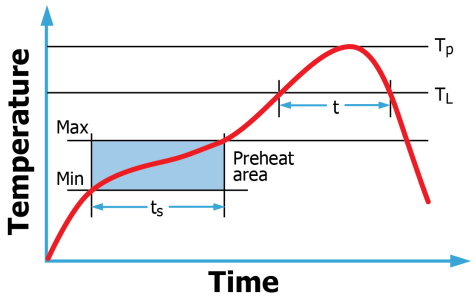


Soldering

Reflow solder in accordance with IPC-A-610. Recommended reflow profile as laid down in IPC/JEDEC J-STD-020.

Wave soldering is also possible, but care must be taken for case sizes 1210 and larger and component thickness >1.0mm. Trials are encouraged.

Hand soldering is not recommended and can lead to component damage through thermal shock.



Application notes with mounting and handling guidance are available on request.

Compex DLI Johanson MFG Novacap Syfer Voltronics

Knowles Precision Devices - Sales

Europe: KPD-Europe-sales@knowles.com

Asia: KPD-Asia-sales@knowles.com

USA: KPD-NA-sales@knowles.com

www.knowlescapacitors.com

This datasheet is for a standard item and is confirmed valid on the date generated, the latest published data for this part may differ and is available at http://www.knowlescapacitors.com or by contacting us.

© The information contained on this drawing is confidential and may not be copied in whole or part in any form or disclosed to a third party without the consent of Knowles and any customer mentioned within this specification.

Data is correct to the best of our knowledge, errors and omissions excepted.

Date: Saturday, November 21, 2026

20261121 005057274UTC

Downloaded from Arrow.com.