

PESD5V0S1UB-Q

ESD protection diode

14 September 2026 **Product data sheet**

1. General description

Unidirectional ElectroStatic Discharge (ESD) protection diode in a SOD523 plastic package designed to protect one transmission or data line from the damage caused by ESD and other transients.

2. Features and benefits

- Unidirectional ESD protection of one line
- Low clamping voltage: V_{CL} = 20 V at I_{PPM} = 15 A
- ESD protection up to 30 kV
- IEC 61000-4-5 (surge); I_{PPM} = 15 A at t_p = 8/20 µs
- Qualified according to AEC-Q101 and recommended for use in automotive applications

3. Application information

- Computers and peripherals
- Communication systems
- Audio and video equipment
- Data lines
- CAN bus protection

4. Quick reference data

Table 1. Quick reference data

Symbol	Parameter	Conditions		Min	Typ	Max	Unit
V_{RWM}	reverse standoff voltage	T_{amb} = 25 °C		-	-	5	V
C_d	diode capacitance	f = 1 MHz; V_R = 0 V; T_{amb} = 25 °C		-	152	200	pF

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5. Pinning information

Table 2. Pinning information

Pin	Symbol	Description	Simplified outline	Graphic symbol
1	K	cathode[1]	 SC-79 (SOD523)	 sym035
2	A	anode		

[1] The marking bar indicates pin 1.

6. Ordering information

Table 3. Ordering information

Type number	Package		
	Name	Description	Version
PESD5V0S1UB-Q	SC-79	plastic, surface-mounted package; 2 leads; 1.2 mm x 0.8 mm x 0.6 mm body	SOD523

7. Marking

Table 4. Marking codes

Type number	Marking code
PESD5V0S1UB-Q	N2

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8. Limiting values

Table 5. Limiting values

In accordance with the Absolute Maximum Rating System (IEC 60134).

Symbol	Parameter	Conditions		Min	Max	Unit
P_{PPM}	rated peak pulse power	t_p = 8/20 µs	[1]	-	260	W
I_{PPM}	rated peak pulse current	t_p = 8/20 µs	[1]	-	15	A
T_j	junction temperature			-	150	°C
T_{amb}	ambient temperature			-55	150	°C
T_{stg}	storage temperature			-65	150	°C
ESD maximum ratings						
V_{ESD}	electrostatic discharge voltage	IEC 61000-4-2 (contact discharge)	[2]	-	30	kV
		HBM MIL-STD883		-	10	kV

[1] Non-repetitive current pulse 8/20 µs exponentially decay waveform.
[2] Device stressed with ten non-repetitive ESD pulses.

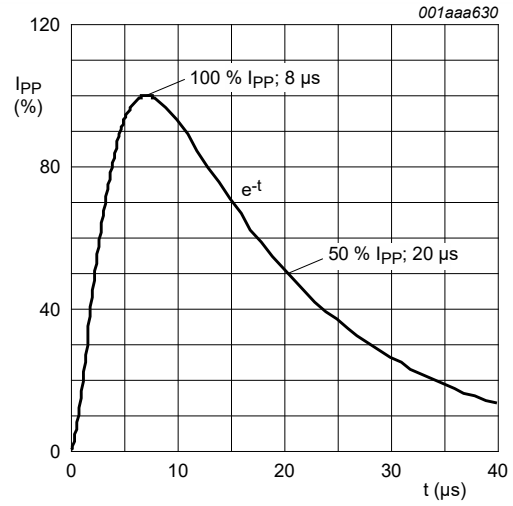


Fig. 1. 8/20 µs pulse waveform according to IEC 61000-4-5

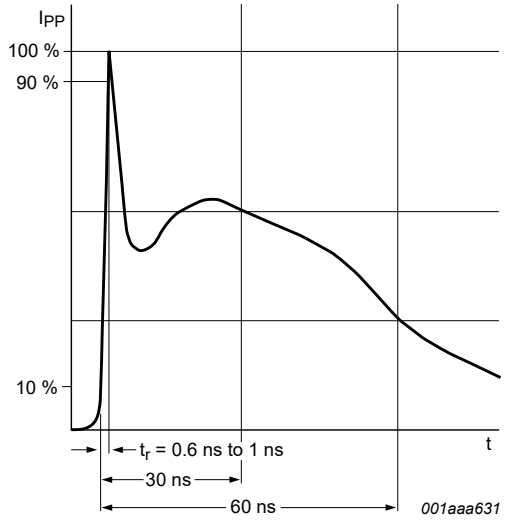


Fig. 2. ESD pulse waveform according to IEC 61000-4-2

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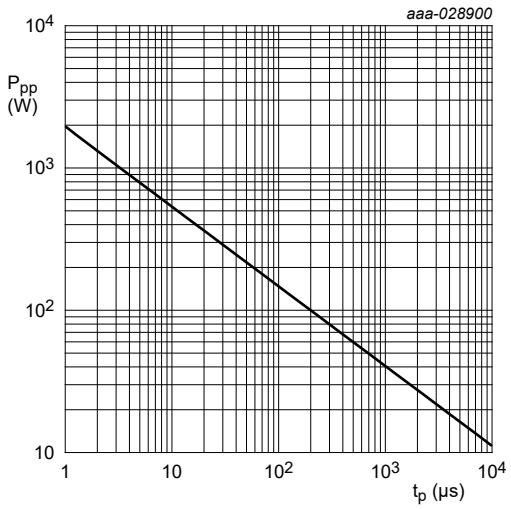
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9. Characteristics

Table 6. Characteristics

Symbol	Parameter	Conditions		Min	Typ	Max	Unit
V_{RWM}	reverse standoff voltage	T_{amb} = 25 °C		-	-	5	V
V_{BR}	breakdown voltage	I_R = 5 mA; T_{amb} = 25 °C		6.4	6.8	7.2	V
I_{RM}	reverse leakage current	V_{RWM} = 5 V; T_{amb} = 25 °C		-	0.1	1	µA
C_d	diode capacitance	f = 1 MHz; V_R = 0 V; T_{amb} = 25 °C		-	152	200	pF
V_{CL}	clamping voltage	I_{PPM} = 1 A; T_{amb} = 25 °C	[1]	-	-	9	V
		I_{PPM} = 15 A; T_{amb} = 25 °C	[1]	-	-	20	V
R_{diff}	differential resistance	I_R = 1 mA; T_{amb} = 25 °C		-	-	80	Ω

[1] Non-repetitive current pulse 8/20 µs exponentially decay waveform.



T_{amb} = 25 °C
t_p = 8/20 µs exponentially decay waveform.

Fig. 3. Peak pulse power dissipation as a function of pulse time; typical values

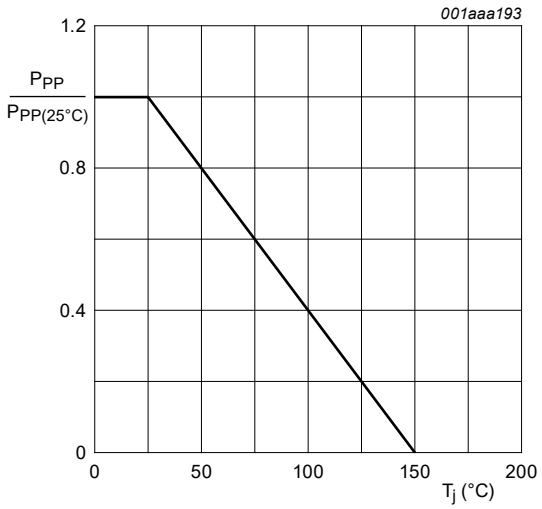
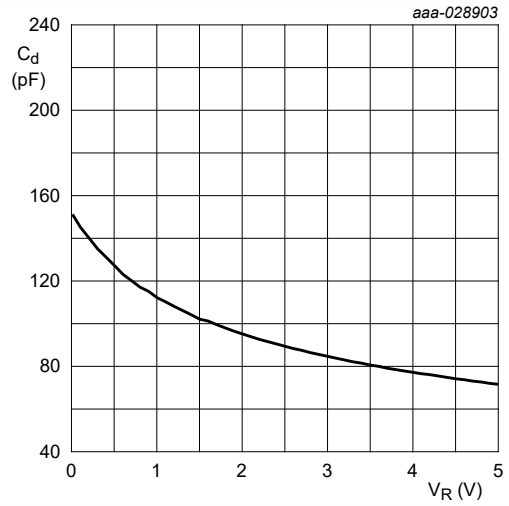


Fig. 4. Relative variation of peak pulse power as a function of junction temperature; typical values

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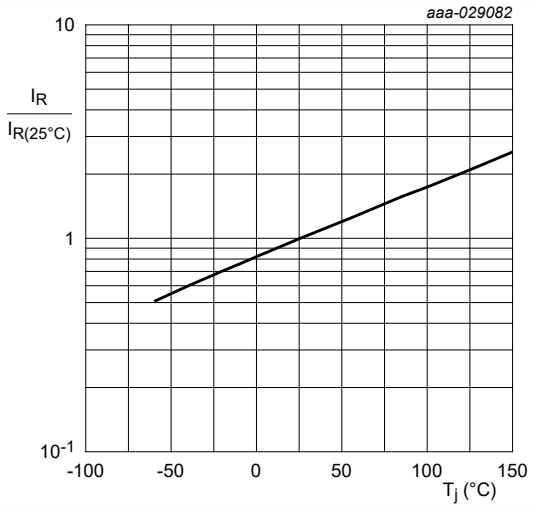
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f = 1 MHz; T_{amb} = 25 °C

Fig. 5. Diode capacitance as a function of reverse voltage; typical values



V_{RWM} = 5 V

Fig. 6. Relative variation of reverse leakage current as a function of junction temperature; typical values

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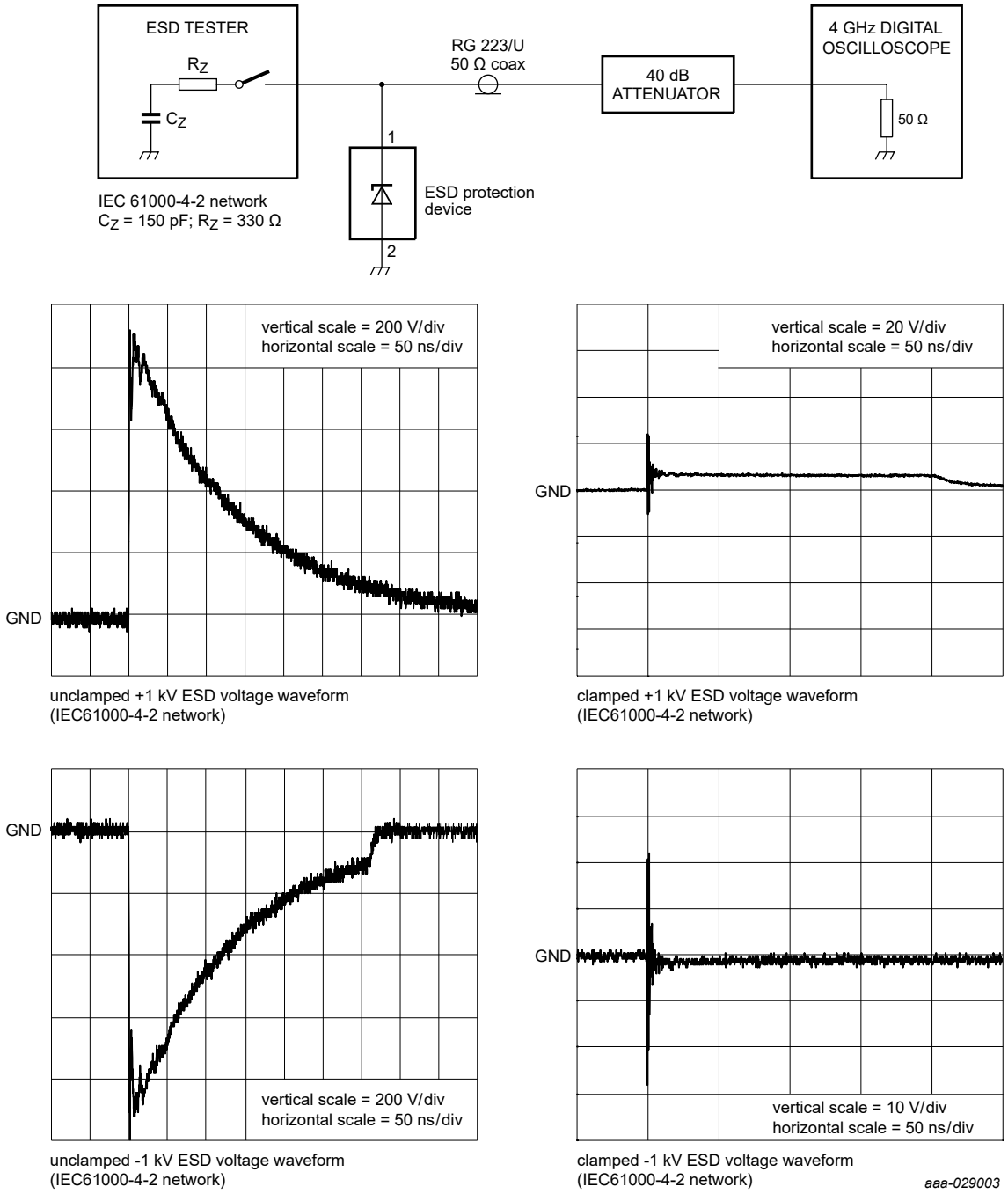


Fig. 7. ESD clamping test setup and waveforms

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10. Application information

The device is designed for protection of one unidirectional data or signal line from surge pulses and ESD damage. The device is suitable on lines where the signal polarities are either positive or negative with respect to ground.

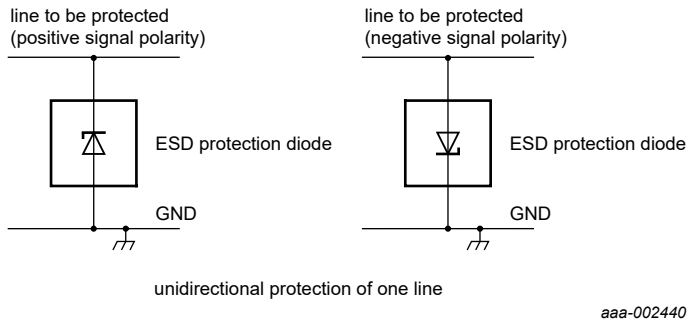


Fig. 8. Application diagram

Circuit board layout and protection device placement

Circuit board layout is critical for the suppression of ESD, Electrical Fast Transient (EFT) and surge transients. The following guidelines are recommended:

1. Place the device as close to the input terminal or connector as possible.
2. Minimize the path length between the device and the protected line.
3. Keep parallel signal paths to a minimum.
4. Avoid running protected conductors in parallel with unprotected conductors.
5. Minimize all Printed-Circuit Board (PCB) conductive loops including power and ground loops.
6. Minimize the length of the transient return path to ground.
7. Avoid using shared transient return paths to a common ground point.
8. Use ground planes whenever possible. For multilayer PCBs, use ground vias.

11. Test information

Quality information

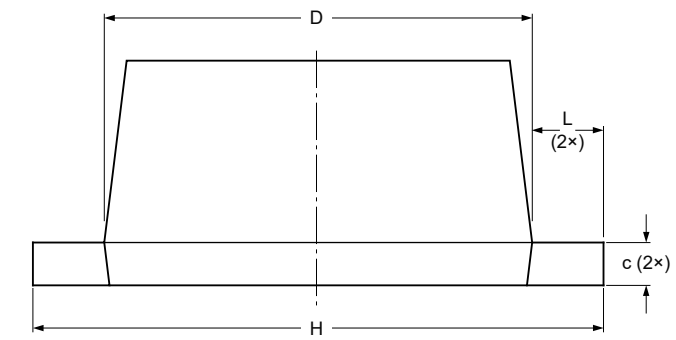
This product has been qualified in accordance with the Automotive Electronics Council (AEC) standard *Q101 - Stress test qualification for discrete semiconductors*, and is suitable for use in automotive applications.

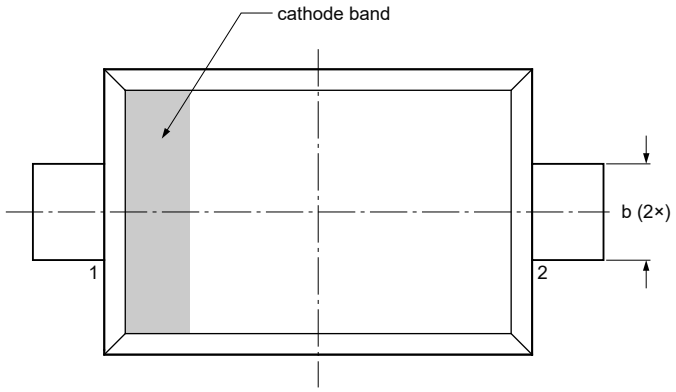
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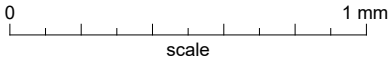
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12. Package outline

Plastic surface-mounted package; 2 leads **SOD523**







Dimensions (mm are the original dimensions)

Unit		A	b	c	D	E	H	L
mm	max	0.65	0.34	0.17	1.25	0.85	1.70	0.30
	nom							
	min	0.58	0.26	0.11	1.15	0.75	1.55	0.10

sod523_po

Outline version	References				European projection	Issue date
	IEC	JEDEC	EIAJ			
SOD523			SC-79 (except Dim-L)			~~26-06-04~~ 26-08-25

Fig. 9. Package outline SC-79 (SOD523)

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13. Soldering

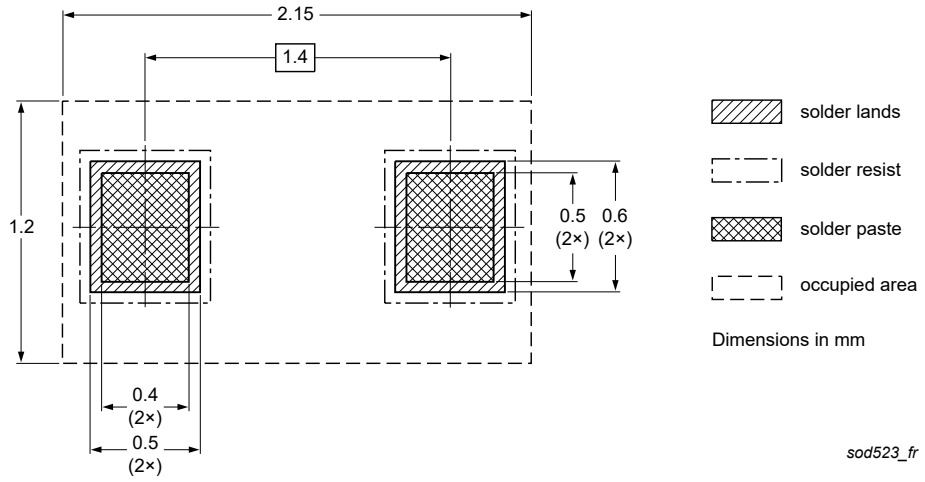


Fig. 10. Reflow soldering footprint for SC-79 (SOD523)

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14. Revision history

Table 7. Revision history

Data sheet ID	Release date	Data sheet status	Change notice	Supersedes
PESD5V0S1UB-Q v.3	20260914	Product data sheet	-	PESD5V0S1UB-Q v.2
Modifications:	• Package outline: Typo corrected			
PESD5V0S1UB-Q v.2	20260806	Product data sheet	-	PESD5V0S1UB-Q v.1
PESD5V0S1UB-Q v.1	20220531	Product data sheet	-	-

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Data sheet status

Document status [1][2]	Product status [3]	Definition
Objective [short] data sheet	Development	This document contains data from the objective specification for product development.
Preliminary [short] data sheet	Qualification	This document contains data from the preliminary specification.
Product [short] data sheet	Production	This document contains the product specification.

[1] Please consult the most recently issued document before initiating or completing a design.
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