Unit: mm

TOSHIBA Field Effect Transistor Silicon N Channel MOS Type (π -MOSVII)

TK2Q60D

Switching Regulator Applications

• Low drain-source ON-resistance: $RDS(ON) = 3.2 \Omega(typ.)$

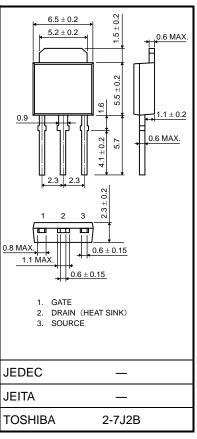
• High forward transfer admittance: $|Y_{fs}| = 1.0 \text{ S (typ.)}$

• Low leakage current: $IDSS = 10 \mu A (max) (VDS = 600 V)$

• Enhancement mode: $V_{th} = 2.4 \text{ to } 4.4 \text{ V (V}_{DS} = 10 \text{ V, I}_{D} = 1 \text{ mA})$

Absolute Maximum Ratings (Ta = 25°C)

Characteristics		Symbol	Rating	Unit	
Drain-source voltage		V _{DSS}	600	V	
Gate-source voltage		V _{GSS}	±30	V	
Drain current	DC (Note) I _D	2	А	
	Pulse (Note) IDP	8	A	
Drain power dissipati	on (Tc = 25°C)	PD	60	W	
Single pulse avalance	ne energy (Note 2	EAS	101	mJ	
Avalanche current		I _{AR}	2	А	
Repetitive avalanche	energy (Note 3	B) EAR	6.0	mJ	
Channel temperature		T _{ch}	150	°C	
Storage temperature range		T _{stg}	−55 to 150	°C	



Weight: 0.36 g (typ.)

Note: Using continuously under heavy loads (e.g. the application of high temperature/current/voltage and the significant change in temperature, etc.) may cause this product to decrease in the reliability significantly even if the operating conditions (i.e. operating temperature/current/voltage, etc.) are within the absolute maximum ratings. Please design the appropriate reliability upon reviewing the Toshiba Semiconductor Reliability Handbook ("Handling Precautions"/"Derating Concept and Methods") and individual reliability data (i.e. reliability test report and estimated failure rate, etc).

Thermal Characteristics

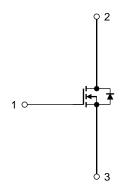
Characteristics	Symbol	Max	Unit
Thermal resistance, channel to case	R _{th (ch-c)}	2.08	°C/W
Thermal resistance, channel to ambient	Rth (ch-a)	125	°C/W

Note 1: Ensure that the channel temperature does not exceed 150°C.

Note 2: VDD = 90 V, Tch = 25°C(initial), L = 44.1 mH, RG = 25 Ω , IAR = 2 A

Note 3: Repetitive rating: pulse width limited by maximum channel temperature

This transistor is an electrostatic-sensitive device. Handle with care.



Start of commercial production 2009-03

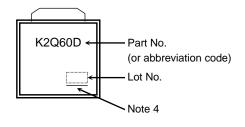
Electrical Characteristics (Ta = 25°C)

Char	acteristics	Symbol	Test Condition	Min	Тур.	Max	Unit
Gate leakage current		IGSS	VGS = ±30 V, VDS = 0 V	_	_	±1	μА
Drain cut-off curr	ent	I _{DSS}	V _{DS} = 600 V, V _{GS} = 0 V	_	_	10	μА
Drain-source bre	akdown voltage	V (BR) DSS	I _D = 10 mA, V _{GS} = 0 V	600	_	_	V
Gate threshold v	oltage	V _{th}	V _{DS} = 10 V, I _D = 1 mA	2.4	_	4.4	V
Drain-source ON	resistance	R _{DS} (ON)	V _{GS} = 10 V, I _D =1 A	_	3.2	4.3	Ω
Forward transfer	admittance	Y _{fs}	V _{DS} = 10 V, I _D =1 A	0.3	1.0	_	S
Input capacitance		C _{iss}		_	280	_	pF
Reverse transfer capacitance		C _{rss}	V _{DS} = 25 V, V _{GS} = 0 V, f = 1 MHz	_	1.5	_	
Output capacitance		Coss		_	30	_	
Switching time	Rise time	t _r	V_{QS} $V_{DD} \approx 200 \text{ V}$	_	15	_	
	Turn-on time	ton		_	35	_	
	Fall time	t _f		_	7	_	ns
	Turn-off time	t _{off}	Duty ≤ 1%, t _W = 10 μs	_	55	_	
Total gate charge		Qg		_	7	_	
Gate-source charge		Qgs	$V_{DD} \approx 400 \text{ V}, \text{ VGS} = 10 \text{ V}, \text{ I}_D = 2 \text{ A}$	_	4	_	nC
Gate-drain charge		Qgd		_	3	_	

Source-Drain Ratings and Characteristics (Ta = 25°C)

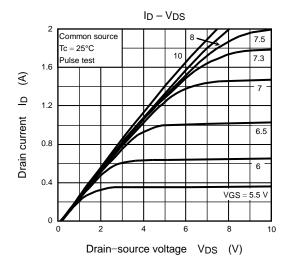
Characteristics	Symbol	Test Condition	Min	Тур.	Max	Unit
Continuous drain reverse current (Note 1)	I _{DR}	_	_	_	2	Α
Pulse drain reverse current (Note 1)	IDRP	_	_	_	8	Α
Forward voltage (diode)	VDSF	IDR = 2 A, VGS = 0 V	_	_	-1.7	V
Reverse recovery time	t _{rr}	$I_{DR} = 2 A$, $V_{GS} = 0 V$,	_	550	_	ns
Reverse recovery charge	Q _{rr}	dI _{DR} /dt = 100 A/μs	_	2.2	_	μС

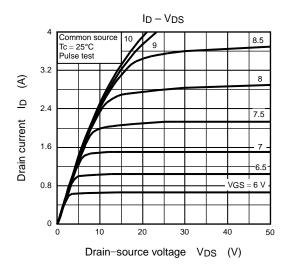
Marking

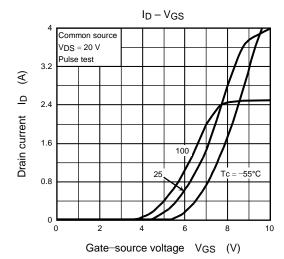


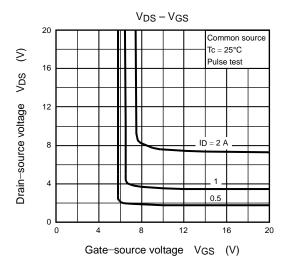
Note 4 : A line under a Lot No. identifies the indication of product Labels [[G]]/RoHS COMPATIBLE or [[G]]/RoHS [[Pb]]

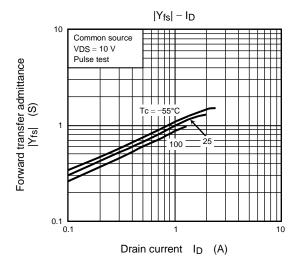
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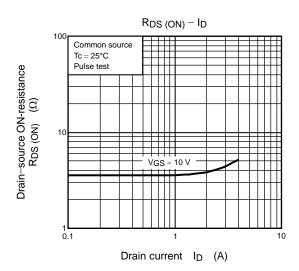


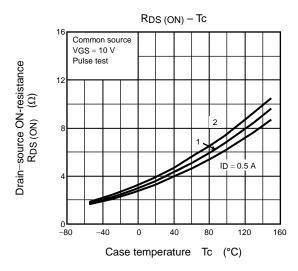


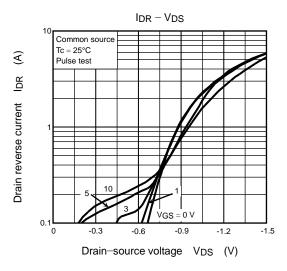


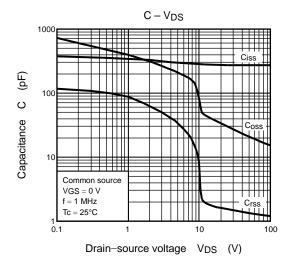


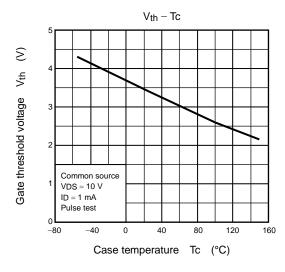


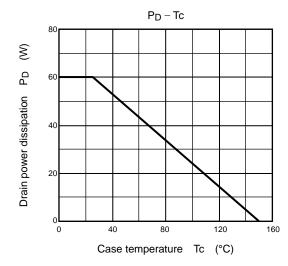


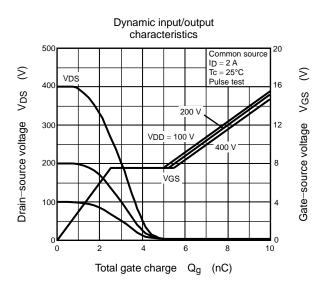


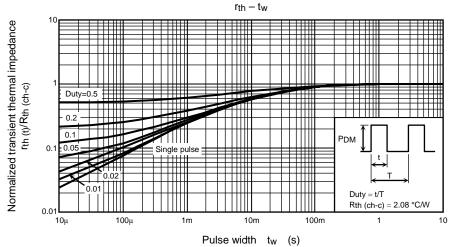




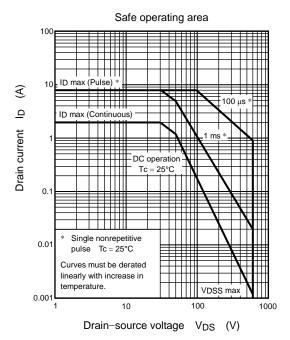


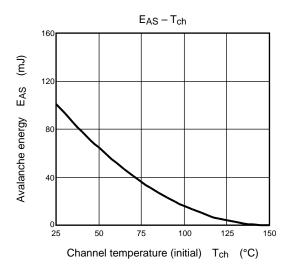


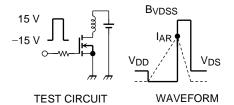












$$R_{G} = 25 \Omega$$

$$\begin{aligned} &R_G = 25 \ \Omega \\ &V_{DD} = 90 \ V, \ L = 44.1 \ mH \end{aligned} \qquad EAS = \frac{1}{2} \cdot L \cdot I^2 \cdot \left(\frac{BVDSS}{BVDSS - V_{DD}} \right)$$

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