You are here

CY8C3445PVI-094T

RoHS   Pb

Status: **In Production**

Datasheet

📄 **English**   📄 **Chinese**

File Type: PDF   Updated: Jun 30, 2017

Product Specs	**Packaging/Ordering**	**Quality and RoHS**	**Technical Documents**

CY8C3445PVI-094T

Development Kit	CY8CKIT-001
Automotive Qualified	N
Boost Converter (Volts)	0.5
CPU Core	8051
CapSense	Y
Dedicated ADC (#_ Max Resolution @ Sample Rate)	DelSig (1, 12-bit @ 192 ksps)
Dedicated DAC (#_ Max Resolution @ Sample Rate)	(2, 8-bit @ 8 msps)
EEPROM (KB)	1
Flash (KB)	32
LCD Direct Drive (Yes/No)	Y
Max. Operating Frequency (MHz)	50
Max. Operating Temp. (°C)	85
Max. Operating Voltage (V)	5.50
Min. Operating Temp. (°C)	-40
Min. Operating Voltage (V)	1.71
No. of CAN Controllers	0
No. of DMA Channels	24
No. of Dedicated Comparators	4
No. of Dedicated Digital Filter Blocks	0
No. of Dedicated I2C	1
No. of Dedicated OpAmps	2
No. of Dedicated SPI	0
No. of Dedicated Timer/Counter/PWM Blocks	4
No. of Dedicated UART	0
No. of GPIOs	25
No. of Programmable Analog Blocks	2
No. of Programmable Universal Digital Blocks	20
No. of SIO	4
No. of USB IO	0
Part Family	Standard Sync
SRAM (KB)	4
Tape & Reel	Y
USB (Type)	None

DESIGN SUPPORT

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Third Party Network (CYPros)

TRAINING

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Webinars

Availability	Quantity	Ships In	Buy from Cypress	Buy from Distributors
Out of Stock	0	Please click here to check lead times	Contact Sales	Contact Sales

PACKAGING/ORDERING

Package	SSOP
No. of Pins	48
Package Dimensions	625 L x 2.3 H x 295 W (Mils)
Package Weight	650.23 (mgs)
Package Cross Section Drawing	Download
Package Carrier	REEL
Package Carrier Drawing / Orientation	Reel Drawing , Tape Drawing , Package Orientation
Standard Pack Quantity	1000
Minimum Order Quantity (MOQ)	1000
Order Increment	1000
Estimated Lead Time (days)	91
HTS Code	8542.31.0001
ECCN	(A.3)
ECCN Suball	3A991

QUALITY AND ROHS

Moisture Sensitivity Level (MSL)	3
Peak Reflow Temp. (°C)	260 (Cypress Reflow Profile)
RoHS Compliant	Y Print RoHS Certificate of Compliance
PB Free	Y
Lead/Ball Finish	Ni/Pd/Au
Marking	Cypress Marking Format

Package Material Declaration

48L SSOP PB-FREE PACKAGE MATERIAL DECLARATION DATASHEET *Last Update: Nov 24, 2016*

IPC 1752 Material Declaration

IPC1752 MATERIAL DECLARATION SSOP 48 (SP48) - JCET CHINA GREEN MC - AU WIRE *Last Update: Nov 10, 2016*
IPC1752 MATERIAL DECLARATION SSOP 48 (SP48) - CML PHILIPPINES GREEN MC - AU WIRE *Last Update: Oct 13, 2016*

RoHS Analysis Certificates (CoA) for Direct Materials

Please click here

Device Qualification Reports

FIT/MTBF, ESD (HBM/CDM) and Latch-up data available in the Device Qualification Report.

QTP#101101: PSOC3 CY8C38/36/34/32 AT FAB-4 CMI PRODUCT QUALIFICATION REPORT *Last Update: Sep 21, 2016*

TECHNICAL DOCUMENTS

Application Notes (24)

AN54181 - Getting Started with PSoC® 3 Sep 16, 2018
AN73854 - PSoC® 3, PSoC 4, and PSoC 5LP Introduction to Bootloaders Apr 05, 2018
AN2155 - PSoC® EMI Design Considerations Feb 18, 2018
AN54460 - PSoC® 3 and PSoC 5LP Interrupts Dec 14, 2017
AN81623 - PSoC® 3, PSoC 4, and PSoC 5LP Digital Design Best Practices Dec 14, 2017
AN70698 - PSoC® 3, PSoC 4 and PSoC 5LP Temperature Measurement with an RTD Oct 09, 2017
AN61290 - PSoC® 3 and PSoC 5LP Hardware Design Considerations Sep 21, 2017
AN54439 - PSoC® 3 and PSoC 5LP External Crystal Oscillators Aug 16, 2017
AN82156 - PSoC® 3, PSoC 4, and PSoC 5LP - Designing PSoC Creator™ Components with UDB Datapaths Jul 31, 2017
AN60317 - PSoC® 3 and PSoC 5LP I2C Bootloader Jul 06, 2017
AN2099 - PSoC® 1, PSoC 3, PSoC 4, and PSoC 5LP - Single-Pole Infinite Impulse Response (IIR) Filters Jul 03, 2017
AN75511 - PSoC® 3 / PSoC 5LP - Temperature Measurement With Thermocouples May 26, 2017
AN60616 - PSoC® 3 and PSoC 5LP Startup Procedure May 25, 2017
AN60631 - PSoC® 3 and PSoC 5LP Clocking Resources May 25, 2017
AN60594 - PSoC® 3 and PSoC 5LP: Low-Frequency FSK Modulation and Demodulation May 14, 2017
AN60024 - PSoC® 3, PSoC 4, PSoC 5LP Switch Debouncer and Glitch Filter May 11, 2017
AN66477 - PSoC® 3, PSoC 4, and PSoC 5LP - Temperature Measurement with a Thermistor May 10, 2017
AN66627 - PSoC® 3 and PSoC 5LP Intelligent Fan Controller May 10, 2017

AN73054 - PSoC® 3 and PSoC 5LP Programming Using an External Microcontroller (HSSP) May 09, 2017
AN77835 - PSoC® 3 to PSoC 5LP Migration Guide May 04, 2017
AN60630 - PSoC® 3 8051 Code and Memory Optimization May 04, 2017
AN89611 - PSoC® 3 and PSoC 5LP – Getting Started With Chip Scale Packages (CSP) Apr 28, 2017
AN65977 - PSoC® 3, PSoC 4, and PSoC 5LP - Temperature Measurement with a TMP05/TMP06 Digital Sensor Apr 28, 2017
AN68403 - PSoC® 3 and PSoC 5LP Analog Signal Chain Calibration Apr 27, 2017
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Product Change Notice (PCN) (2)

PCN125175 Oct 31, 2017
Q2, 2012 - Q4, 2013 Horizon Report
PCN125157 Oct 30, 2017
Q1, 2012 - Q2, 2013 Horizon Report

Product Information Notice (PIN) (6)

PIN145304 Nov 07, 2017
PSoC Programmer Upgrade for PSoC 3 Family of Products

PIN145281 Nov 06, 2017
Qualification of New Carrier Tape Supplier at Cypress Philippines

PIN145280 Nov 06, 2017
Qualification of Manufacturing and Design Changes for PSoC 3 Products

PIN145277 Nov 06, 2017
Changes to Cypress Address Labels

PIN145276 Nov 06, 2017
Addendum to PIN 135258 - Qualification of JCET as an additional Test and Finish Location for Cypress Products

PIN145273 Nov 06, 2017
Improvement of Cypress Minnesota Back-End-of-Line Integration for 130nm SONOS Product Families

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CORPORATE HEADQUARTERS

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CUSTOMER SERVICE SUPPORT

+1-800-541-4736
Hours:
8:00AM - 5:00PM (local time)
Create a MyCase
Cypress Developer Community

INTERNATIONAL SUPPORT

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