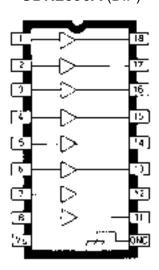
UDN2595A (DIP)



Dwg. No. A-11,407

ABSOLUTE MAXIMUM RATINGS

at 25°C Free-Air Temperature for any one driver (unless otherwise noted)

Output Voltage, V _{CE} 20 V
Supply Voltage, V _S 20 V
Input Voltage, V _{IN} 20 V
Output Current, 1 _C 200 mA
Ground Terminal Current, I _{GND} 1.6 A
Package Power Dissipation, P _D See Graph
Operating Temperature Range, $T_A \ \ \textbf{-20}^{\circ}\textbf{C} \ \textbf{to} \ \textbf{+85}^{\circ}\textbf{C}$
Storage Temperature Range, T _S 55°C to +150°C

Developed for use with low-voltage LED and incandescent displays requiring low output saturation voltage, the UDN2595A and A2595SLW meet many interface needs, including those exceeding the capabilities of standard logic buffers. The eight non-Darlington outputs of each driver can continuously and simultaneously sink load currents of 100 mA at ambient temperatures of up to +75°C.

The eight-channel driver's active-low inputs can be driven directly from TTL, Schottky TTL, DTL, 5 to 16 V CMOS, and NMOS logic. All input connections are on one side of the package, output connections on the other, for simplified printed wiring board layouts.

These drivers are packaged in plastic DIPs (suffix A) or surface-mountable wide-body SOICs (suffix LW), and are rated for operation over the temperature range of -20°C to +85°C. The A2595SLW is also available for operation to -40°C. To order, change the suffix from 'SLW' to 'ELW'.

FEATURES

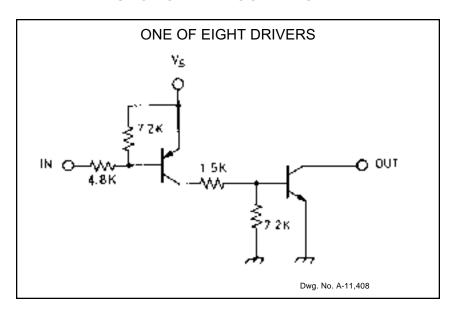
- Non-Inverting Function
 (Input Low = Output ON)
- 200 mA Current Rating
- 100 mA Continuous and Simultaneous (All outputs) to +85°C
- Low Saturation Voltage
- TTL, CMOS, NMOS Compatible
- Efficient Input/Output Pin Format
- DIP or SOIC Packaging

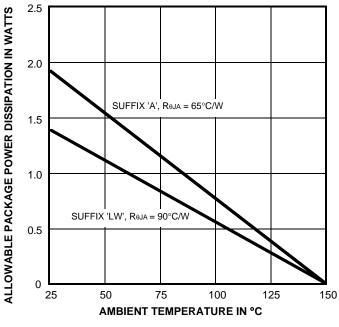
Always order by complete part number:

Part Number	Package
UDN2595A	18-Pin DIP
A2595SLW	20-Lead Wide-Body SOIC

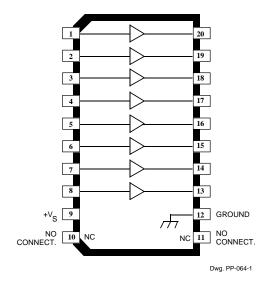


FUNCTIONAL BLOCK DIAGRAM





A2595SLW (SOIC)



Dwg. GS-009-1B



ELECTRICAL CHARACTERISTICS at $T_A = +25^{\circ}C$, $V_S = 5.0 \text{ V}$ (unless otherwise noted).

			Limits		
Characteristic	Symbol	Test Conditions	Min.	Max.	Units
Output Leakage Current	I _{CEX}	V _{IN} ≥ 4.5 V, V _{OUT} = 20 V, T _A = 25°C	_	50	μΑ
		V _{IN} ≥ 4.6 V, V _{OUT} = 20 V, T _A = 70°C	_	100	μΑ
Output Saturation Voltage	V _{CE(SAT)}	V _{IN} = 0.4 V, I _{OUT} = 50 mA	_	0.5	V
		V _{IN} = 0.4 V, I _{OUT} = 100 mA	_	0.6	V
Input Current	I _{IN(ON)}	V _{IN} = 0.4 V, I _{OUT} = 100 mA	_	-1.6	mA
		V_{IN} = 0.4 V, I_{OUT} = 100 mA, V_{S} = 15 V	_	-5.0	mA
Input Voltage	V _{IN(ON)}	I _{OUT} = 100 mA, V _{OUT} ≤ 0.6 V	_	0.4	٧
	V _{IN(OFF)}	I _{OUT} = 100 μA, T _A = 70°C	4.6	_	V
Input Capacitance	C _{IN}		_	25	pF
Supply Current	I _S	V _{IN} = 0.4 V, I _{OUT} = 100 mA	_	6.0	mA
		V _{IN} = 0.4 V, I _{OUT} = 100 mA, V _S = 15 V	_	20	mA

NOTES: 1. Negative current is defined as coming out of the specified device pin.

2. The $V_{IN(ON)}$ voltage limit guarantees a minimum output sink current per the specified conditions.

3. l_S is measured with any one of eight drivers turned ON.

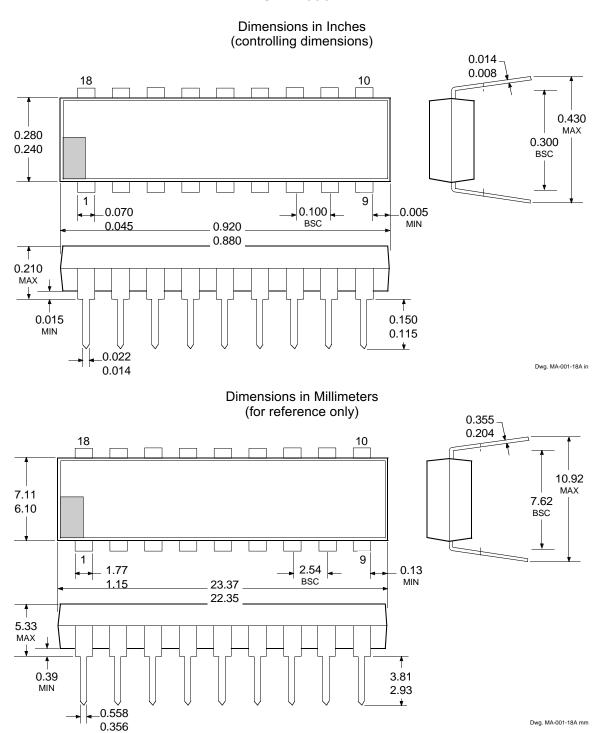
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Allegro products are not authorized for use as critical components in life-support devices or systems without express written approval.

The information included herein is believed to be accurate and reliable. However, Allegro MicroSystems, Inc. assumes no responsibility for its use; nor for any infringement of patents or other rights of third parties which may result from its use.

UDN2595A



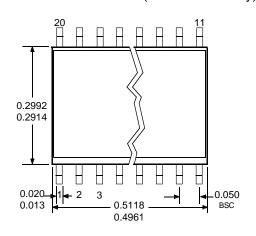
- NOTES: 1. Exact body and lead configuration at vendor's option within limits shown.
 - 2. Lead spacing tolerance is non-cumulative.
 - 3. Lead thickness is measured at seating plane or below.

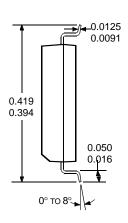


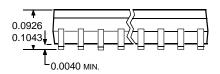
2595 8-CHANNEL SATURATED SINK DRIVER

A2595SLW

Dimensions in Inches (for reference only)

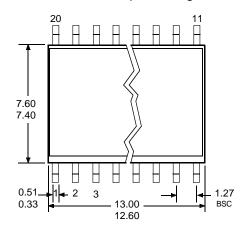


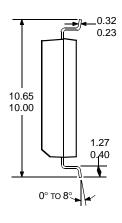


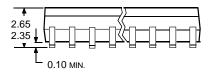


Dwg. MA-008-20 in

Dimensions in Millimeters (controlling dimensions)







Dwg. MA-008-20 mm

NOTES: 1. Exact body and lead configuration at vendor's option within limits shown.

2. Lead spacing tolerance is non-cumulative.