

AUTOMOTIVE AND HOUSEHOLD SECURITY SIREN DRIVER

ZSD100

ISSUE 8 – JUNE 2007

INTRODUCTION

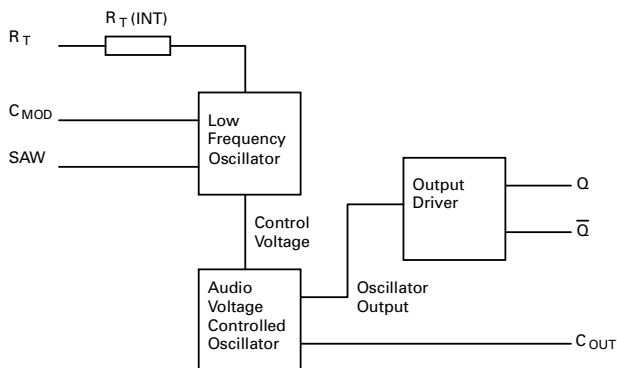
The ZSD100 is a frequency swept alarm signal generator designed specifically for static and automotive security alarm systems. One ZSD100, two timing capacitors, an in-expensive TO92 darlington, piezo transducer and coupling transformer is all that is necessary to produce an ear piercing 120 dB alarm siren.

Including an audio frequency signal generator, low frequency sweep generator, disable circuitry and output driver stages, the ZSD100 provides every function necessary to produce a standard alarm signal. Available in an 8 pin SO package, the IC gives a low cost compact solution to siren signal generation. The device operates from supplies of 4V up to 18V and is ideal for security alarms in battery powered applications, burglar alarms and automotive anti theft systems.

FEATURES

- 4-18V Operating voltage range.
- Small external component count.
- Direct drive of darlington, mosfet and IGBT output stages.
- Single ended or push-pull output stages.
- Suitable for automotive and static alarm systems.
- Low supply current - 10mA operating
- 1µA sleep mode
- Low cost 8 pin SO package.
- User selected audio and sweep frequencies.
- -40°C to 125°C operating temperature range.
- Choice of modulation waveform.

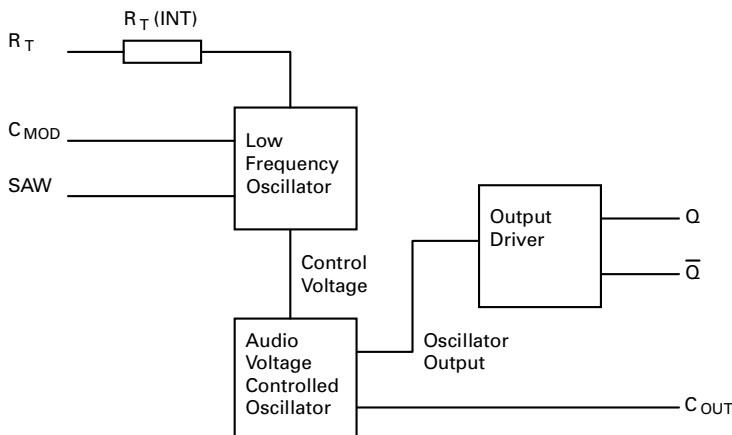
SCHEMATIC DIAGRAM



ZSD100

FUNCTIONAL DESCRIPTION

The audio signal of the ZSD100 is generated using a squarewave oscillator whose output is capable of directly driving a wide range of output circuits. To produce a characteristic alarm siren sound, the frequency of the audio oscillator is swept over a fixed 2:1 range by a second, low frequency oscillator. The frequencies of both oscillators are controlled by $R_T(INT)$ and capacitors C_{MOD} and C_{OUT} .



PIN DESCRIPTIONS

1. R_T Optional external resistor for improved frequency control. An external resistor improves the control of both the modulating and output oscillators. The R_T pin is also used to power the device down. Either connecting R_T to V_{CC} or an open circuit will result in the device being disabled.
2. SAW Selection of modulation waveform is made using the SAW pin. An open circuit produces a triangle wave, sawtooth is achieved by connecting SAW to the C_{MOD} pin.
3. C_{MOD} An external capacitor is used to program the low frequency modulating oscillator. The value of C_{MOD} recommended is between 0.1 μ F and 100 μ F.
4. GND
5. C_{OUT} An external capacitor is used to program the output oscillator. The value of C_{OUT} recommended is between 1nF and 100nF.
6. Q Non inverted output driver
7. $\bar{Q}$ Inverted output driver
8. V_{CC}

ZSD100

ABSOLUTE MAXIMUM RATING

V _{CC}	-0.5V to +20V
Operating Temperature	-40 to 125°C
Storage Temperature	-65 to 150°C

Power Dissipation (T_{amb}=25°C)

SO8	625mW
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ELECTRICAL CHARACTERISTICS

TEST CONDITIONS (Unless otherwise stated): T_{amb}=25°C, V_{CC}=12V

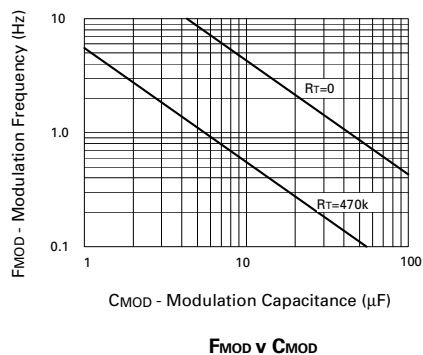
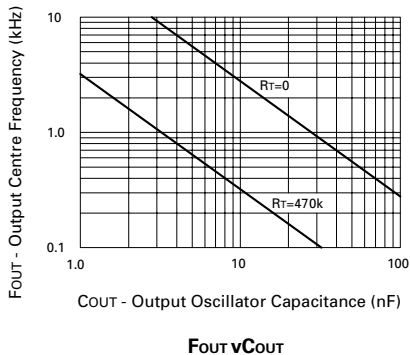
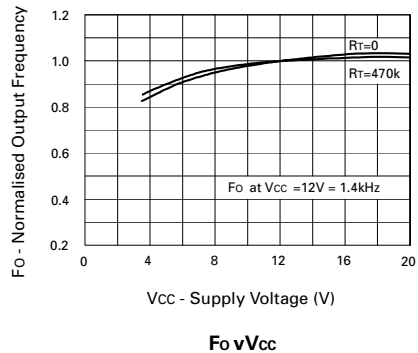
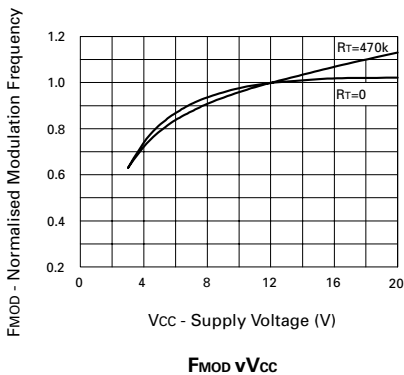
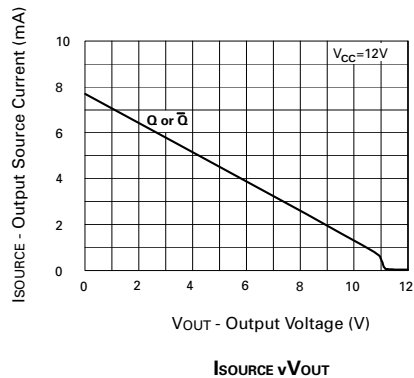
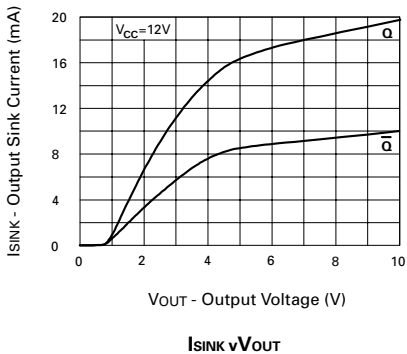
PARAMETER	SYMBOL	MIN.	TYP.	MAX.	CONDITIONS.
Supply Voltage Operating Range	V _{CC}	4V		18V	
Supply Current Disabled Enabled	I _{CC}			1μA 25mA	R _T O/C, V _{CC} =18V R _T , Q, $\bar{Q}$ at Gnd, V _{CC} =18V
Modulating Oscillator Frequency Range	F _{MOD}	0.1Hz		10Hz	V _{CC} =4 to 18V
Frequency Value	F _{MOD}	3.3Hz 5.1Hz	4.3Hz 5.5Hz	5.4Hz 5.9Hz	R _T =0, C _{MOD} =10μF R _T =470kΩ, C _{MOD} =1μF
See Note 1					
Output Oscillator Frequency Range	F _{OUT}	100Hz		10kHz	V _{CC} =4 to 18V
Centre Frequency (Note 2) Deviation Duty Cycle	F _{OUT} ΔF _{OUT}	1.01kHz	1.26kHz ±33% 49%	1.58kHz 55%	R _T =0, C _{OUT} =22nF
Output Source Current Sink Current Open Circuit Voltage	I _{SOURCE} I _{SINK} V _{OUT(HIGH)} V _{OUT(LOW)}	5mA 0.5mA V _{CC} -1.5V		0.5V	V _{OUT} =1.4V V _{OUT} =1.4V I _{OUT} =100μA I _{SINK} =0.1μA
Frequency Control Components					
Internal Resistor	R _T (INT)		61.5kΩ		
External Resistor	R _T (EXT)	0		1MΩ	
Modulation Capacitor	C _{MOD}	0.1μF		100μF	
Output Capacitor	C _{OUT}	1nF		100nF	

Note 1.
$$F_{MOD} = \frac{2850}{C_{MOD}(61.5 + R_T (EXT))} \text{ Hz}$$

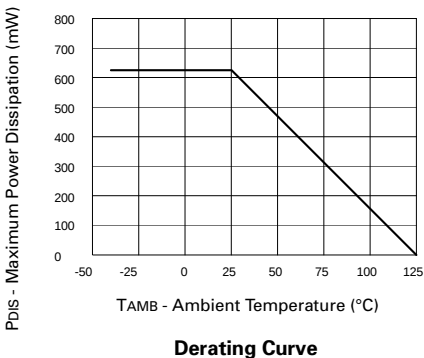
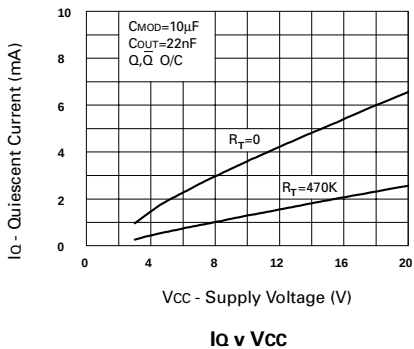
C_{MOD} in μF, R_T (EXT) in kΩ

Note 2.
$$F_{OUT} = \frac{1710}{C_{OUT}(61.5 + R_T (EXT))} \text{ Hz}$$

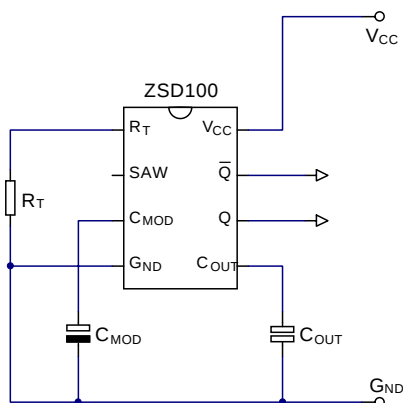
C_{OUT} in μF, R_T (EXT) in kΩ

ZSD100**TYPICAL CHARACTERISTICS**

TYPICAL CHARACTERISTICS



Improved Frequency Control

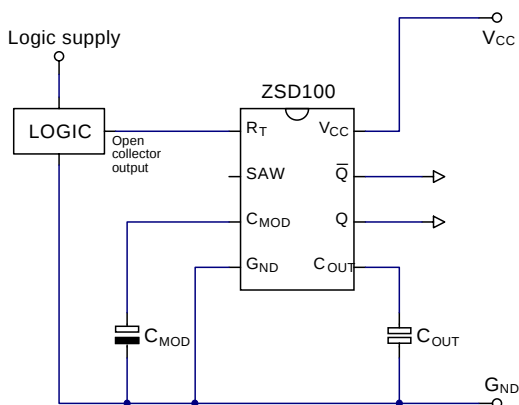


Improved frequency control can be achieved with an external resistor between the R_T pin and GND. An external resistor reduces the value of the timing capacitors required for the selected operating frequency and also reduces the ZSD100's power consumption.

Note: The outputs of the ZSD100 have been designed to avoid cross-conduction in full bridge output circuits. Large value external timing resistors can lead to a small level of cross-conduction. It is recommended that external timing resistors are not used with bridge output circuits.

Remote Switching

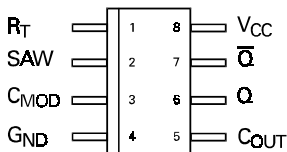
If the R_T pin is switched open-circuit, all internal functions of the IC are shut down.



Note: A small leakage current in the open collector logic controller will enable the IC. If such leakage cannot be eliminated, its effect can be easily negated by adding a high value resistor (eg. 100k) between pin R_T and V_{CC} . This resistor will have no effect on frequency control.

ZSD100

SO8 Package Suffix – N8



Top View

Part Number	Package	Part Mark
ZSD100N8	SO8	ZSD100

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