

Video Development Kit, Cyclone II Edition

from *Altera Corporation*



The Video Development Kit, Cyclone® II Edition, featuring the Texas Instruments (TI) TVP5146 family of video analog-to-digital (A/D) converters, delivers a complete development environment for digital video and image processing. The kit facilitates the entire design process from design conception through hardware implementation.

The Video Development Kit, Cyclone II Edition includes the Cyclone II DSP development board, Altera's video input daughtercard, DSP Builder development tool, Quartus® II development software, evaluation intellectual property (IP) cores, system reference designs and labs, as well as power supplies, cables, and documentation.

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Ordering Information

Table 1. Video Development Kit, Cyclone II Edition Ordering Code and Pricing Information

Ordering Code	Price	Ordering Information
DK-Video-2C70N		In North America, call 1-888-800-0631. For international sales, contact your local distributor.

Notes:

Buyer represents that it is a product developer, software developer or system integrator and acknowledges that this product is an evaluation kit that is not FCC authorized, is made available solely for evaluation and software development, and may not be resold.

Development Kit Contents

The Video Development Kit, Cyclone II Edition features:

- Cyclone II EP2C70 DSP development board
 - Cyclone II EP2C70F672C6 device
- Video input daughtercard
 - Composite video input channels— TI TVP5146
 - Supports NTSC/PAL
 - 10-bit BT.656 output
- Analog I/O
 - Two 14-bit, 125-million samples per second (MSPS) analog-to-digital (A/D) converters— TI ADS5500
 - Two 14-bit, 165-MSPS digital-to-analog (D/A) converters— TI DAC904
 - VGA digital-to-analog converter (DAC)
 - Stereo audio coder/decoder (CODEC), 96 KHz
- Digital I/O
 - EMIF connector for TI's C6000 DSP Starter Kit (DSK) to enable peripheral expansion and FPGA coprocessing
 - MICTOR connector for Agilent and Tektronix logic analyzers
 - Expansion prototype connector
- Memory
 - 256-Mbyte DDR2 DIMM
 - 1-Mbyte synchronous SRAM
 - Two EPCS64 devices
- MATLAB/Simulink evaluation software
- DSP Builder development tool
- Quartus II development kit edition (DKE)
- Evaluation IP cores
- System reference designs and labs
 - Video and image processing reference design
 - DSP Builder filtering design
 - DSP Builder/SOPC Builder image processing reference design
 - Fast Fourier transform (FFT) coprocessor reference design for TI's TMS320C6416 DSK
 - Nios®II reference designs
 - DDR2 DIMM reference design
- Cables and accessories
 - USB-Blastertm download cable
 - Power supply
 - International power cords

Keep up to date with Altera's newest video and image processing offerings by signing up today for the Code:DSP e-newsletter.