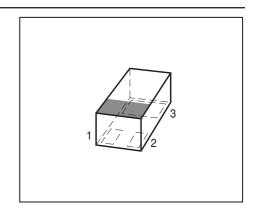


NPN Bipolar RF Transistor

- For low noise, high-gain amplifiers up to 2 GHz
- For linear broadband amplifiers
- f_T = 8 GHz, NF_{min} = 1 dB at 900 MHz
- Pb-free (RoHS compliant) package
- Qualification report according to AEC-Q101 available





ESD (Electrostatic discharge) sensitive device, observe handling precaution!

Type	Marking	Pin	Package		
BFR193L3	RC	1 = B	2 = E	3 = C	TSLP-3-1

Maximum Ratings at T_A = 25 °C, unless otherwise specified

Parameter	Symbol	Value	Unit
Collector-emitter voltage	$V_{\sf CEO}$	12	V
Collector-emitter voltage	V_{CES}	20	
Collector-base voltage	V_{CBO}	20	
Emitter-base voltage	V_{EBO}	2	
Collector current	I _C	80	mA
Base current	l _B	10	
Total power dissipation ¹⁾	P _{tot}	580	mW
<i>T</i> _S ≤ 89°C			
Junction temperature	T_{J}	150	°C
Storage temperature	T_{Stq}	-55 150	

Thermal Resistance

Parameter	Symbol	Value	Unit
Junction - soldering point ²⁾	R _{thJS}	105	K/W

¹T_S is measured on the collector lead at the soldering point to the pcb

 $^{^2}$ For calculation of R_{thJA} please refer to Application Note AN077 (Thermal Resistance Calculation)



Electrical Characteristics at $T_A = 25$ °C, unless otherwise specified

Parameter	Symbol	Values			Unit
		min.	typ.	max.	
DC Characteristics	•			•	•
Collector-emitter breakdown voltage	V _{(BR)CEO}	12	-	-	V
$I_{\rm C} = 1 \text{ mA}, I_{\rm B} = 0$, ,				
Collector-emitter cutoff current	I _{CES}	-	_	100	μA
$V_{CE} = 20 \text{ V}, V_{BE} = 0$					
Collector-base cutoff current	I _{CBO}	-	-	100	nA
$V_{\rm CB} = 10 \text{ V}, I_{\rm E} = 0$					
Emitter-base cutoff current	I _{EBO}	-	-	1	μA
$V_{\rm EB} = 1 \text{ V}, I_{\rm C} = 0$					
DC current gain	h _{FE}	70	100	140	-
$I_{\rm C}$ = 30 mA, $V_{\rm CE}$ = 8 V, pulse measured					

2



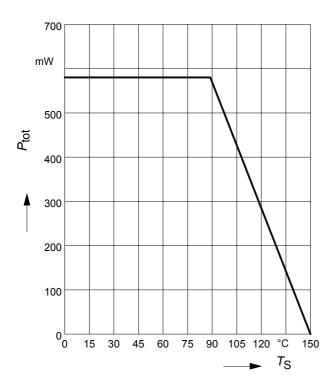
Electrical Characteristics at $T_A = 25$ °C, unless otherwise specified

Parameter	Symbol	Values			Unit
		min.	typ.	max.	
AC Characteristics (verified by random sam	pling)				
Transition frequency	f _T	6	8	-	GHz
$I_{\rm C}$ = 50 mA, $V_{\rm CE}$ = 8 V, f = 500 MHz					
Collector-base capacitance	C_{cb}	-	0.63	0.9	pF
$V_{CB} = 10 \text{ V}, f = 1 \text{ MHz}, V_{BE} = 0$,					
emitter grounded					
Collector emitter capacitance	C _{ce}	-	0.22	-	
$V_{CE} = 10 \text{ V}, f = 1 \text{ MHz}, V_{BE} = 0$,					
base grounded					
Emitter-base capacitance	C _{eb}	-	2.25	-	
$V_{\text{EB}} = 0.5 \text{ V}, f = 1 \text{ MHz}, V_{\text{CB}} = 0$					
collector grounded					
Minimum noise figure	NF _{min}				dB
$I_{\rm C}$ = 10 mA, $V_{\rm CE}$ = 8 V, $Z_{\rm S}$ = $Z_{\rm Sopt}$,					
f = 900 MHz		-	1	-	
$I_{\rm C}$ = 10 mA, $V_{\rm CE}$ = 8 V, $Z_{\rm S}$ = $Z_{\rm Sopt}$,					
f = 1.8 GHz		-	1.6	-	
Power gain, maximum available ¹⁾	G _{ma}				
$I_{\rm C}$ = 30 mA, $V_{\rm CE}$ = 8 V, $Z_{\rm S}$ = $Z_{\rm Sopt}$,					
$Z_{L} = Z_{Lopt}$, $f = 900 \text{ MHz}$		-	19	_	
$I_{\rm C}$ = 30 mA, $V_{\rm CE}$ = 8 V, $Z_{\rm S}$ = $Z_{\rm Sopt}$,					
$Z_{L} = Z_{Lopt}$, $f = 1.8 \text{ GHz}$		-	12.5	-	
Transducer gain	$ S_{21e} ^2$				dB
$I_{\rm C}$ = 30 mA, $V_{\rm CE}$ = 8 V, $Z_{\rm S}$ = $Z_{\rm L}$ = 50 Ω ,					
f = 900 MHz		_	14.5	_	
$I_{\rm C}$ = 30 mA, $V_{\rm CE}$ = 8 V, $Z_{\rm S}$ = $Z_{\rm L}$ = 50 Ω ,					
f = 1.8 GHz		_	9	_	

 $^{{}^{1}}G_{\text{ma}} = |S_{21} / S_{12}| (k-(k^2-1)^{1/2})$

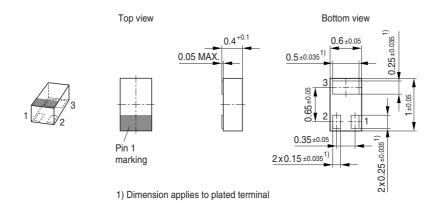


Total power dissipation $P_{tot} = f(T_S)$



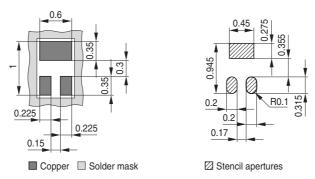


Package Outline

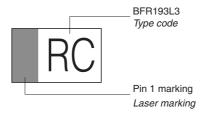


Foot Print

For board assembly information please refer to Infineon website "Packages"

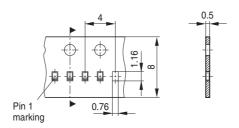


Marking Layout (Example)



Standard Packing

Reel ø180 mm = 15.000 Pieces/Reel



5



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6