



#### 400V DUAL NPN MEDIUM POWER TRANSISTORS IN PowerDI3333-8

#### **Features**

- Dual NPN with Common-Collector
- BV<sub>CEO</sub> > 400V
- I<sub>C</sub> = 0.5A Continuous Collector Current
- Configurable as NPN Darlington Pair
- Low Saturation Voltage V<sub>CE(SAT)</sub> < 175mV @ 500mA</li>
- Totally Lead-Free & Fully RoHS Compliant (Notes 1 & 2)
- Halogen and Antimony Free. "Green" Device (Note 3)

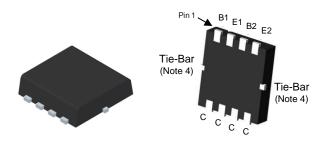
### **Mechanical Data**

- Case: PowerDI<sup>®</sup>3333-8
- Case Material: Molded Plastic, "Green" Molding Compound.
   UL Flammability Classification Rating 94V-0
- Moisture Sensitivity: Level 1 per J-STD-020
- Terminals: Finish Matte Tin Plated Leads. Solderable per MIL-STD-202, Method 208 ©3
- Weight: 0.072 grams (Approximate)

### **Applications**

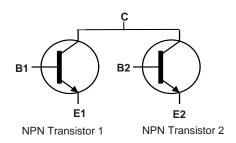
- Power Management
- High Voltage Start-Up Switch
- DC-DC Converters

### PowerDI3333-8 (Type UXB)



Top View Bottom View

#### **Dual NPN with Common-Collector**



**Equivalent Circuit** 

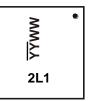
### **Ordering Information** (Note 5)

Ī	Part Number	Compliance	Marking	Reel Size (inches)	Tape Width (mm)	Quantity per Reel
	ZXTN08400BNS-7	Standard	2L1	7	12	2,000

Notes:

- 1. No purposely added lead. Fully EU Directive 2002/95/EC (RoHS), 2011/65/EU (RoHS 2) & 2015/863/EU (RoHS 3) compliant.
- See https://www.diodes.com/quality/lead-free/ for more information about Diodes Incorporated's definitions of Halogen- and Antimony-free, "Green" and Lead-free.
- 3. Halogen- and Antimony-free "Green" products are defined as those which contain <900ppm bromine, <900ppm chlorine (<1500ppm total Br + Cl) and <1000ppm antimony compounds.
- 4. Tie-bars are internally connected to the Common-Collector. They do not need to be externally connected.
- 5. For packaging details, go to our website at https://www.diodes.com/design/support/packaging/diodes-packaging/.

### **Marking Information**



2L1 = Product Type Marking Code

YYWW = Date Code Marking

YY = Last Two Digits of Year (ex: 19 for 2019)

WW = Week Code (01 to 53)



## Absolute Maximum Ratings (@T<sub>A</sub> = +25°C, unless otherwise specified.)

Characteristic	Symbol	Value	Unit
Collector-Base Voltage	V <sub>CBO</sub>	450	V
Collector-Emitter Voltage (Forward Blocking)	V <sub>CEX</sub>	450	V
Collector-Emitter Voltage	V <sub>CEO</sub>	400	V
Emitter-Collector Voltage (Reverse Blocking)	V <sub>ECO</sub>	6	V
Emitter-Base Voltage	V <sub>EBO</sub>	7	V
Continuous Collector Current	Ic	0.5	Α
Peak Pulse Current	I <sub>CM</sub>	1	А
Base Current	I <sub>B</sub>	0.2	Α

## Thermal Characteristics (@TA = +25°C, unless otherwise specified.)

Characteristic		Symbol	Value	Units
Total Power Dissipation (Note 6)		$P_D$	0.83	W
Thermal Resistance, Junction to Ambient (Note 6)	Steady State	$R_{\theta JA}$	150	°C/W
Total Power Dissipation (Note 7)		$P_D$	1.83	W
Thermal Resistance, Junction to Ambient (Note 7)	Steady State	$R_{\theta JA}$	68	°C/W
Thermal Resistance, Junction to Lead (Note 8)		$R_{\theta JL}$	19	C/VV
Operating and Storage Temperature Range		$T_{J_i}T_{STG}$	-55 to +150	°C

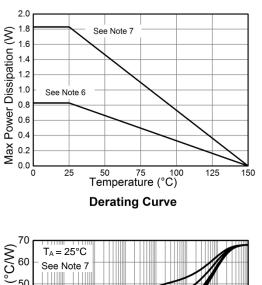
# ESD Ratings (Note 9)

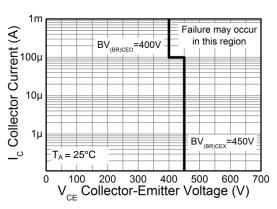
Characteristic	Symbol	Value	Unit	JEDEC Class
Electrostatic Discharge - Human Body Model	ESD HBM	4,000	V	3A
Electrostatic Discharge - Machine Model	ESD MM	400	V	С

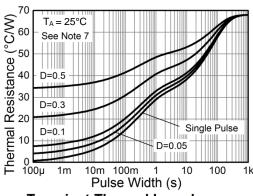
- Device mounted on FR-4 PCB board, with minimum recommended pad layout, single sided.
   Device mounted on FR-4 substrate PCB board, 2oz copper, with thermal bias to bottom layer 1-inch square copper plate.
   Thermal resistance from junction to soldering point (on the collector pads).
   Refer to JEDEC specification JESD22-A114 and JESD22-A115.

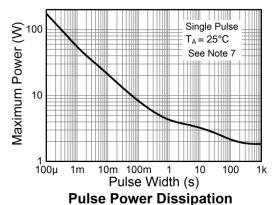


# **Thermal Characteristics and Derating Information**









**Transient Thermal Impedance** 



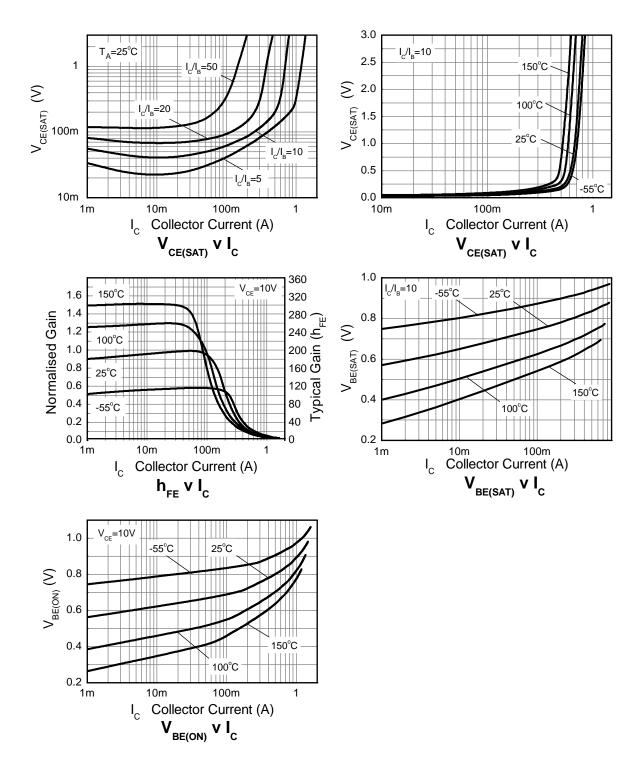
## Electrical Characteristics (@TA = +25°C, unless otherwise specified.)

Characteristic	Symbol	Min	Тур	Max	Unit	Test Condition
OFF CHARACTERISTICS	•					•
Collector-Base Breakdown Voltage	BV <sub>CBO</sub>	450	550	_	V	$I_{C} = 100 \mu A$
Collector-Emitter Breakdown Voltage (Forward Blocking)	BV <sub>CEX</sub>	450	550	_	V	$I_C = 100\mu A$ , $R_{BE} \le 1k\Omega$ or -1V < V <sub>BE</sub> < 0.25V
Collector-Emitter Breakdown Voltage (Base Open) (Note 10)	$BV_CEO$	400	500	_	V	I <sub>C</sub> = 10mA
Emitter-Base Breakdown Voltage	$BV_{EBO}$	7	8.1		V	$I_E = 100\mu A$
Emitter-Collector Breakdown Voltage (Reverse Blocking)	$BV_{ECX}$	6	8	_	V	$I_E = 100\mu A$ , $R_{BC} \le 1k\Omega$ or -0.25V < $V_{BC} < 0.25V$
Emitter-Collector Breakdown Voltage (Base Open)	BV <sub>ECO</sub>	6	8.5	_	V	I <sub>E</sub> = 100μA
Collector-Base Cutoff Current			<1	50	nA	V <sub>CB</sub> = 360V
Collector-Base Cutoff Current	I <sub>CBO</sub>	_	_	20	μA	V <sub>CB</sub> = 360V, T <sub>A</sub> = +100°C
Collector-Emitter Cutoff Current	I <sub>CEX</sub>	_	<1	100	nA	V <sub>CE</sub> = 360V, R <sub>BE</sub> ≤1kΩ -1V < V <sub>BE</sub> < 0.25V
Emitter-Base Cutoff Current	I <sub>EBO</sub>	_	<1	50	nA	V <sub>EB</sub> = 5.6V
ON CHARACTERISTICS (Note 10)						
	h <sub>FE</sub>	90	165			$I_C = 1$ mA, $V_{CE} = 5$ V
Static Forward Current Transfer Ratio		100	180	300	_	$I_{C} = 50 \text{mA}, V_{CE} = 5 \text{V}$
		10	20			I <sub>C</sub> = 500mA, V <sub>CE</sub> = 5V
	VCE(SAT)		70	85	mV	$I_C = 20$ mA, $I_B = 1$ mA
Collector-Emitter Saturation Voltage			50	70		$I_{C} = 50 \text{mA}, I_{B} = 5 \text{mA}$
Collector-Emitter Saturation Voltage		_	120	170		$I_C = 300 \text{mA}, I_B = 30 \text{mA}$
			125	175		IC = 500mA, IB = 100mA
Base-Emitter Saturation Voltage	V <sub>BE(SAT)</sub>	_	865	950	mV	I <sub>C</sub> = 500mA, I <sub>B</sub> = 100mA
Base-Emitter On Voltage	$V_{BE(ON)}$	_	800	900	mV	$I_C = 500 \text{mA}, V_{CE} = 10 \text{V}$
SMALL SIGNAL CHARACTERISTICS (Note 10)						
Transition Frequency	f⊤	_	40	_	MHz	$I_C = 10$ mA, $V_{CE} = 20$ V, f = 20MHz
Output Capacitance	Сово	_	8	10	pF	$V_{CB} = 20V, f = 1MHz$
Delay Time	t <sub>D</sub>	_	100	_	ns	V <sub>CC</sub> = 100V,
Rise Time	t <sub>R</sub>	_	52	_	ns	$I_C = 100 \text{mA},$
Storage Time	t <sub>S</sub>	_	3122		ns	$I_{B1} = 10 \text{mA}$
Fall Time	t <sub>F</sub>	_	240	_	ns	$I_{B2} = -20 \text{mA}$

Note: 10. Measured under pulsed conditions. Pulse width  $\leq$  300 $\mu$ s. Duty cycle  $\leq$  2%.



# Typical Electrical Characteristics (@T<sub>A</sub> = +25°C, unless otherwise specified.)

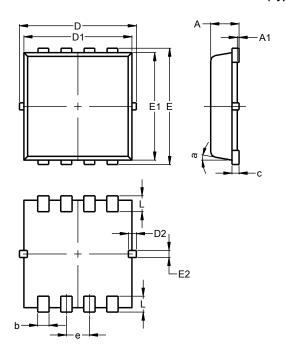




# **Package Outline Dimensions**

Please see http://www.diodes.com/package-outlines.html for the latest version.

### PowerDI3333-8 (Type UXB)

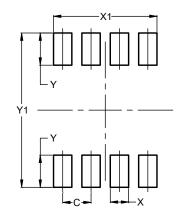


PowerDI3333-8							
(Type UXB)							
Dim	Min	Max	Тур				
Α	0.75	0.85	0.80				
<b>A</b> 1	0.00	0.05					
b	0.25	0.40	0.32				
С	0.10	0.25	0.15				
D	3.20	3.40	3.30				
D1	2.95	3.15	3.05				
D2	0.10	0.35	0.23				
Е	3.20	3.40	3.30				
E1	2.95	3.15	3.05				
E2	0.10	0.30	0.20				
е	_	_	0.65				
L	0.35	0.55	0.45				
а	0°	12°	10°				
All Dimensions in mm							

# **Suggested Pad Layout**

Please see http://www.diodes.com/package-outlines.html for the latest version.

### PowerDI3333-8 (Type UXB)



Dimensions	Value (in mm)
C	0.650
Х	0.420
X1	2.370
Y	0.730
Y1	3.500

Note: For high voltage applications, the appropriate industry sector guidelines should be considered with regards to creepage and clearance distances between device Terminals and PCB tracking.



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