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## NCP1593AGEVB: 3 A Synchronous Buck Regulator Evaluation Board

The NCP1593 is a fixed 1 MHz, high output current, synchronous PWM converter that integrates a low-resistance, high-side P-channel MOSFET and a low-side N-channel MOSFET. The NCP1593 utilizes internally compensated current mode control to provide good transient response, ease of implementation and excellent loop stability. It regulates input voltages from 4.0 V to 5.5 V down to an output voltage as low as 0.6 V and is able to supply up to 3 A of load current. The NCP1593 includes an internally fixed switching frequency (FSW), and an internal soft-start to limit inrush current. Other features include cycle-by-cycle current limiting, 100% duty cycle operation, short-circuit protection, power saving mode and thermal



|              |        |            | ON Semis  | onductor CND CND LX |                           |  |
|--------------|--------|------------|---|---------------------|---------------------------|--|
| Evaluation/  | Develo | pment Tool | Information                                     |                     |                           |  |
| Product      | Status | Compliance | Short Description                               | Parts Used          | Action                    |  |
| NCP1593AGEVB | Active | Pb-free    | 3 A Synchronous Buck Regulator Evaluation Board | NCP1593AMNTWG       | ** Contact<br>Local Sales |  |

| Technical Documents           |  |   |     |  |  |  |  |
|-------------------------------|--|---|-----|--|--|--|--|
| Туре                          | Document Title                                   | Document ID/Size                              | Rev |  |  |  |  |
| Eval Board: BOM               | NCP1593AGEVB Bill of Materials ROHS<br>Compliant | NCP1593AGEVB_BOM_ROHS.PDF - 48.0 KB           | 0   |  |  |  |  |
| Eval Board: Gerber            | NCP1593AGEVB Gerber Layout Files (Zip Format)    | NCP1593AGEVB_GERBER.ZIP - 228.0 KB            | 0   |  |  |  |  |
| Eval Board: Schematic         | NCP1593AGEVB Schematic                           | NCP1593AGEVB_SCHEMATIC.PDF - 120.0 KB         | 0   |  |  |  |  |
| Eval Board: Test<br>Procedure | NCP1593AGEVB Test Procedure                      | NCP1593AGEVB_TEST_PROCEDURE.PDF - 107.0<br>KB | 0   |  |  |  |  |
| Video                         | 3 A Synchronous Buck Regulator                   | WVD17507/D                                    |     |  |  |  |  |

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