TOSHIBA CMOS DIGITAL INTEGRATED CIRCUIT SILICON MONOLITHIC

TC4SU69F

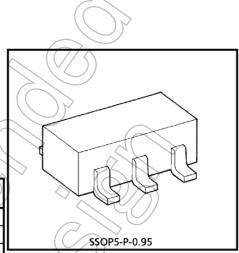
INVERTER GATE

The TC4SU69F is single inverter.

Therefore, this is suitable for the applications of C, R oscillator circuits, crystal oscillator circuits and linear amplifiers in addition to its application as inverters.

ABSOLUTE MAXIMUM RATINGS (Ta = 25°C)

CHARACTERISTIC	SYMBOL	RATING	TINU
DC Supply Voltage	V_{DD}	Vss - 0.5~Vss + 20	V
Input Voltage	VIN	$V_{SS} = 0.5 \sim V_{DD} + 0.5$	A
Output Voltage	Vout	$V_{SS} = 0.5 \sim V_{DD} + 0.5$	∨
DC Input Current	IN	±10	mA
Power Dissipation	PD	200	mW
Operating Temperature Range	T _{opr}	40~85	℃ (°C
Storage Temperature Range	T _{stg}	-65~150	°¢/
Lead Temperature (10s)	T _L	260	√ °C

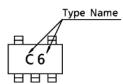


Weight: 0,016g (Typ.)

LOGIC DIAGRAM







Start of commercial production 1987-02

PIN CONFIGURATION (TOP VIEW)

VSS 3

4 OUT X

OPERATING RANGES (V_{SS} = 0V)

CHARACTERISTIC	SYMBOL		MIN.	TYP.	MAX.	UNIT
DC Supply Voltage	V_{DD}	_	⟨3	_	18	V
Input Voltage	v_{IN}	_	0		V_{DD}	V

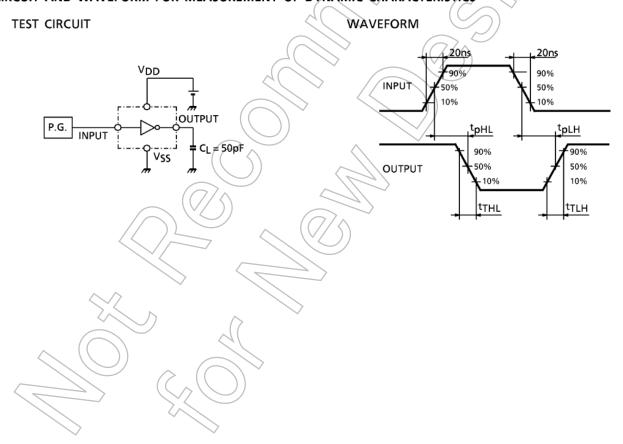
STATIC ELECTRICAL CHARACTERISTICS $(V_{SS} = 0V)$

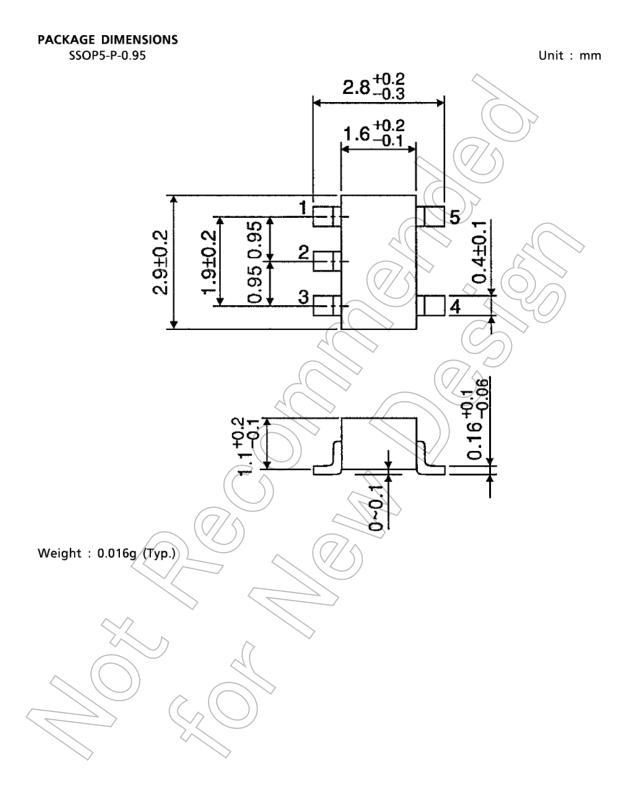
				– 40°C		25°C		85°C			
CHARACTERISTIC	BOL TEST CONDITION		V _{DD} = 40 C (V) MIN. MA			MHN.	\sim	MAX.			UNIT
			5	4.95		4.95	$\overline{}$		4.95		
High-Level	$ I_{OUT} < 1\mu A$	10	9.95	ı	9.95	/ /	I	9.95			
Output Voltage	output Voltage	$V_{IN} = V_{SS}$	15	14.95	/ /	14.95		I	14.95		.,
1 - 11		1	5	_	0.05	-	0.00	0.05		0.05	V
Low-Level	VOL	lout <1μA	10	-6	0.05	\rightarrow	0.00	0.05		0.05	
Output Voltage	"-	$V_{IN} = V_{DD}$	15	_((0.05) —	@,00	0.05		0.05	
		V _{OH} = 4.6V	5	-0.61		- 0.51	- 1.0	7	-0,42	_	
Outmont Himb		V _{OH} = 2.5V	5	2.5	\rightarrow	- 2.1	-4.0	1	<u>-</u> 1.7	_	
Output High Current	ІОН	V _{OH} = 9.5V	1,0 (-1.5	_	- 1.3	(-(2.2)	7	- 1.1	_	
Current		V _{OH} = 13.5V	15	- 4.0	—	- 3.4	- 9.0	V	- 2.8	_	
		$V_{IN} = V_{SS}$					770				^
		V _{OL} = 0.4V	5	[→] 0.61		0.51	<u>1)2</u>	_	0.42		mA
Output Low		V _{OL} = 0.5V	10	1.5		1.3	3.2	—	1.1	_	
Current	lOL	V _{OL} = 1.5V	15	4.0	<u></u>	3.4	12.0	—	2.8	_	
		$V_{IN} = V_{DD}$] `			k //					
		V _{OUT} = 0.5V	5	4.0	_	4.0	_	_	4.0	_	
Janua High Valtaga	\ ,	V _{OUT} = 1,0 V	10	0,8	_	8.0	_	 	8.0	_	
Input High Voltage	VIH	V _{OUT} = 1.5V	15	12.0	/	12.0	_	—	12.0	_	
		louτ ≤1μA		7/	<i>?</i> ?						l v l
		V _{OUT} ≠ 4.5V	5	1/	1.0	_	_	1.0	—	1.0	
Input Low Voltage VIL	λιί.	V _{OUT} = 9.0V	107	Z \	2.0	—	_	2.0	l —	2.0	
	KIL)	V _{OUT} = 13.5V	15/))	3.0	—	_	3.0	—	3.0	
		I _{OUT} <1μA									
Input H Level	ΊΗ	V _{IH} = 18V	18	_	0.1	_	10-5		_	1.0	
Current L Level	կլ	V _{IL} = 0V	18	-	- 0.1	_	- 10 ⁻⁵	- 0.1	—	- 1.0	μ A
Quiescent			5	_	0.25	_	0.001	ı	_	7.5	
Device Current	1DD	$V_{IN} = V_{SS}$, V_{DD}	10	–	0.5	-	0.001	0.5	—	15	μ A
January Current			15		1.0		0.002	1.0		30	

DYNAMIC ELECTRICAL CHARACTERISTICS (Ta = 25°C, $V_{SS} = 0V$, $C_L = 50pF$)

CHARACTERISTIC	SYMBOL	TEST CONDITION	V _{DD} (V)	MIN.	TYP.	MAX.	UNIT
Output Transition Time			5	-/	70	200	
(Low to High)	tTLH	_	10	– /	35	100	
(LOW to High)			15	_ \	30	80	
Output Transition Time			5		70	200	ns
(High to Low)	tTHL	_	10	(+//	35	100	
(High to Low)			15	/ Fi	30	80	
			5 (7	55	110	
Propagation Delay Time	t _{pLH}	_	10) Y	30	60	
			15	_	25	50	
			(5)	\supset $-$	55	110	ns
Propagation Delay Time	t _{pHL}		10	_	30	60	
			//15	_	25	50	
Input Capacitance	CIN	_(4	7.5	15	pF

CIRCUIT AND WAVEFORM FOR MEASUREMENT OF DYNAMIC CHARACTERISTICS





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