

Products

Timing over Packet Networks (ToPSync)

Other products to view

Timing over Packet Networks (ToPSync)

- > **ACS1790T**
PLL Frequency Synthesizer with Integrated VCO
- > **ACS9510**
ToPSync® Timing-over-packet synchronization technology
- > **ACS9522T**
ToPSync® Timing-over-packet synchronization technology
- > **ACS9550**
ToPSync® Timing-over-packet synchronization technology slave 1588 device
- > **ACS9593T**
ToPSync® Timing-over-packet synchronization technology slave 1588 device

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Semtech ACS9520T

ToPSync® Timing-over-packet synchronization technology

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Overview



Diagrams & Specs

Docs & Resources

Sample & Buy

Contact your local Sales Representative for full product specifications

 Datasheet

 Pb Free/ROHS Status

 Check distributor inventory

Features

PTP timing features

- > PTP Grandmaster selection - automatic or manual PTP master/slave mode selection.
- > Powerful network delay analysis - full time-alignment in the slave over hostile networks (Layer 2 or layer 3 networks).
- > Dynamic adaptation - to network delay variations. Network loading change tolerant (e.g., ramps and steps).
- > Time alignment - better than ±1 ?s on a managed 10-switch GbE network under G.826120 test conditions.*
- > Frequency alignment - better than ±10 ppb on a managed 10-switch GbE network under G.826120 test conditions.*

TDM timing features

- > Programmable TDM timing bandwidth - for wander and jitter tracking/attenuation, 0.1 Hz to 70 Hz in 10 steps
- > Automatic hit-less source switchover - on loss of input.
- > Output clock phase adjustment - in 6 ps steps to ±200 ns

Applications

- > Stratum 3, 3E, 4E, 4, SONET Minimum Clock (SMC) or SONET/SDH Equipment Clock (SEC) or Ethernet, IEEE158813 PTP and Synchronous Ethernet

Packaging

- > BGA - 256 pin, 14 mm x 14 mm

Order Codes

- > ACS95201FAFBGT - Lead (Pb)-free package, RoHS and WEEE compliant

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