

Product Summary

V _{RRM} (V)	I _o (A)	V _{F(MAX)} (V) @ +25°C	I _{R(MAX)} (mA) @ +25°C
50	25	0.55	0.12

Description and Applications

Packaged in the compact thermally efficient PowerDI[®]5060-8 package, the DIODES[™] SBRT25M50SLP provides low V_F and excellent reverse leakage stability at high temperatures. It is ideal for use as a rectifier, freewheel diode or blocking diode in:

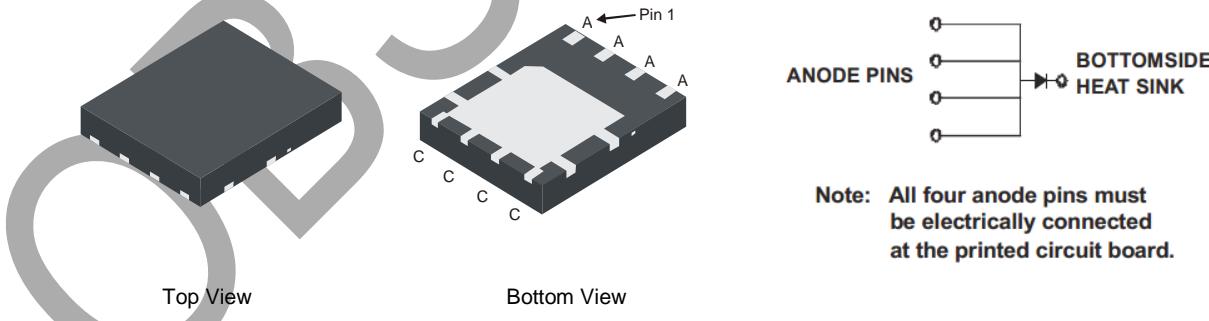
- DC-DC converters
- AC-DC adaptors

Features and Benefits

- Reduced low forward voltage drop (V_F); Better efficiency and cooler operation.
- Reduced high temperature reverse leakage; Increased reliability against thermal runaway failure in high temperature operation.
- Less than 1.1mm package profile – ideal for thin applications.
- Patented Super Barrier Rectifier Technology (SBR[®])
- Lead-Free Finish; RoHS Compliant (Notes 1 & 2)
- Halogen and Antimony Free. "Green" Device (Note 3)
- For automotive applications requiring specific change control (i.e.: parts qualified to AEC-Q100/101/104/200, PPAP capable, and manufactured in IATF 16949 certified facilities), please refer to the related automotive grade (Q-suffix) part. A listing can be found at <https://www.diodes.com/products/automotive/automotive-products/>.
- This part is qualified to JEDEC standards (as references in AEC-Q) for High Reliability. <https://www.diodes.com/quality/product-definitions/>

Mechanical Data

- Package: PowerDI5060-8
- Package Material: Molded Plastic, "Green" Molding Compound; UL Flammability Classification Rating 94V-0
- Moisture Sensitivity: Level 1 per J-STD-020
- Terminals: Finish - Matte Tin Annealed over Copper Leadframe. Solderable per MIL-STD-202, Method 208 (e3)
- Polarity: See Below
- Weight: 0.097 grams (Approximate)



Note: All four anode pins must be electrically connected at the printed circuit board.

Ordering Information (Note 4)

Part Number	Package	Packing	
		Qty.	Carrier
SBRT25M50SLP-13	PowerDI5060-8	2,500	Tape & Reel

Notes:

1. EU Directive 2002/95/EC (RoHS), 2011/65/EU (RoHS 2) & 2015/863/EU (RoHS 3) compliant. All applicable RoHS exemptions applied.
2. See <https://www.diodes.com/quality/lead-free/> for more information about Diodes Incorporated's definitions of Halogen- and Antimony-free, "Green" and Lead-free.
3. Halogen- and Antimony-free "Green" products are defined as those which contain <900ppm bromine, <900ppm chlorine (<1500ppm total Br + Cl) and <1000ppm antimony compounds.
4. For packaging details, go to our website at <https://www.diodes.com/design/support/packaging/diodes-packaging/>.

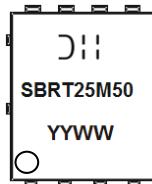
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SBRT25M50SLP

Document number: DS36941 Rev. 4 - 4

Marking Information

PowerDI5060-8



SBRT25M50 = Product Type Marking Code
YYWW = Date Code Marking
YY = Last Two Digits of Year (ex: 22 = 2022)
WW = Week (01-53)

Maximum Ratings (@ $T_A = +25^\circ\text{C}$, unless otherwise specified.)

Single phase, half wave, 60Hz, resistive or inductive load.
For capacitance load, derate current by 20%.

Characteristic	Symbol	Value	Unit
Peak Repetitive Reverse Voltage	V_{RRM}		
Working Peak Reverse Voltage	V_{RWM}	50	V
DC Blocking Voltage	V_{RM}		
Average Rectified Output Current	I_O	25	A
Non-Repetitive Peak Forward Surge Current 8.3ms Single Half Sine-Wave Superimposed on Rated Load	I_{FSM}	220	A

Thermal Characteristics

Characteristic	Symbol	Value	Unit
Typical Thermal Resistance Junction to Ambient (Note 5)	$R_{\theta JA}$	11	°C/W
Typical Thermal Resistance Junction to Case (Note 5)	$R_{\theta JC}$	1	°C/W
Operating and Storage Temperature Range	T_J, T_{STG}	-55 to +150	°C

Electrical Characteristics (@ $T_A = +25^\circ\text{C}$, unless otherwise specified.)

Characteristic	Symbol	Min	Typ	Max	Unit	Test Condition
Forward Voltage Drop	V_F	—	0.42	0.47	V	$I_F = 12.5\text{A}, T_J = +25^\circ\text{C}$ $I_F = 25\text{A}, T_J = +25^\circ\text{C}$
Leakage Current (Note 6)	I_R	—	0.04	0.12	mA	$V_R = 50\text{V}, T_J = +25^\circ\text{C}$ $V_R = 50\text{V}, T_J = +125^\circ\text{C}$

Notes: 5. Test with FR4 substrate 2oz, 2-inch sq. double side copper + additional Aluminum heatsink 50mm*50mm*23mm.
6. Short duration pulse test used to minimize self-heating effect.

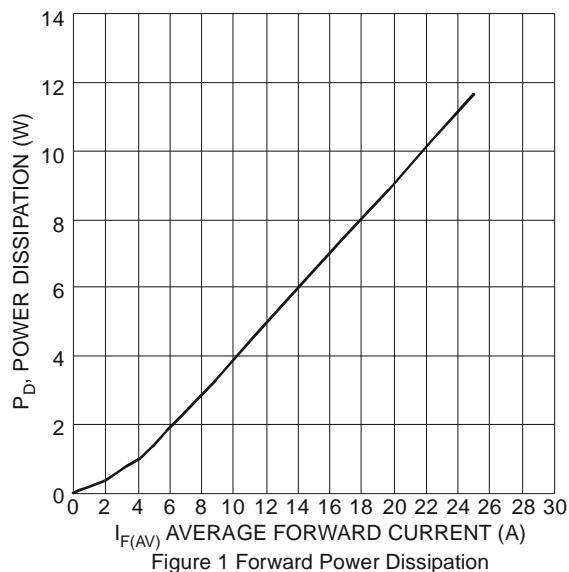


Figure 1 Forward Power Dissipation

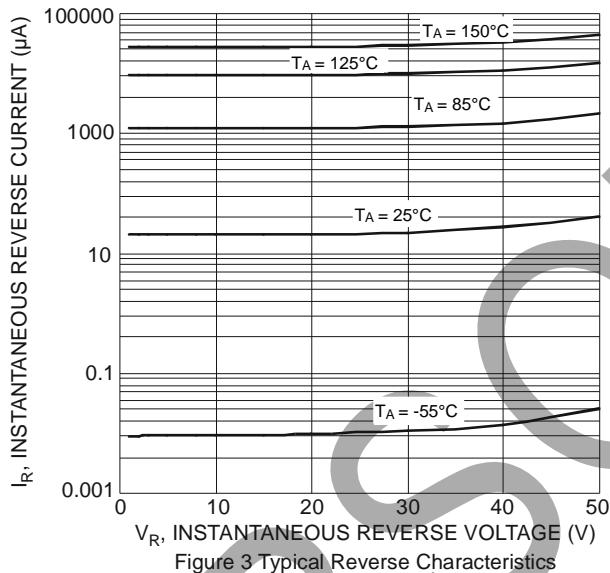


Figure 3 Typical Reverse Characteristics

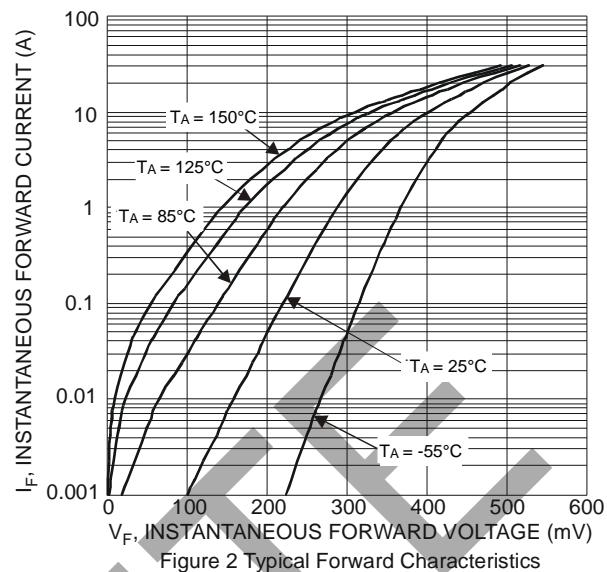


Figure 2 Typical Forward Characteristics

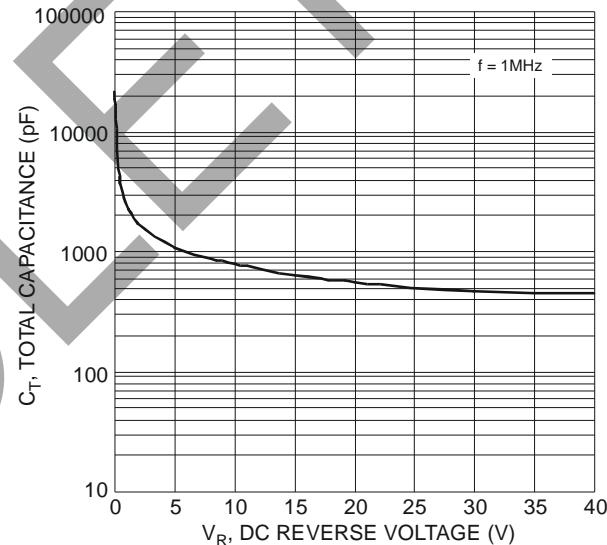


Figure 4 Total Capacitance vs. Reverse Voltage

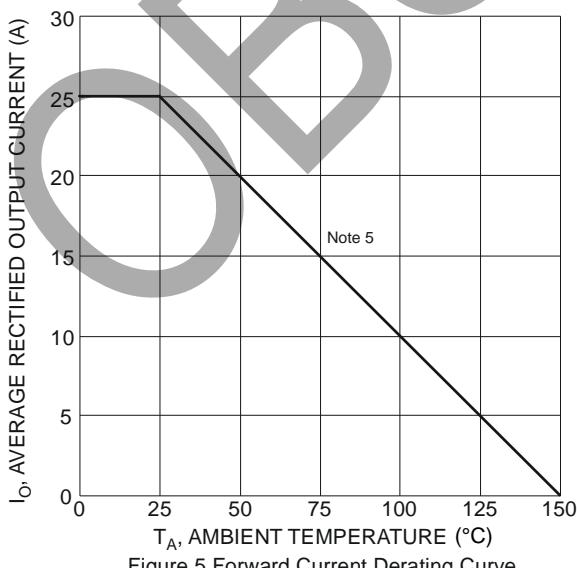
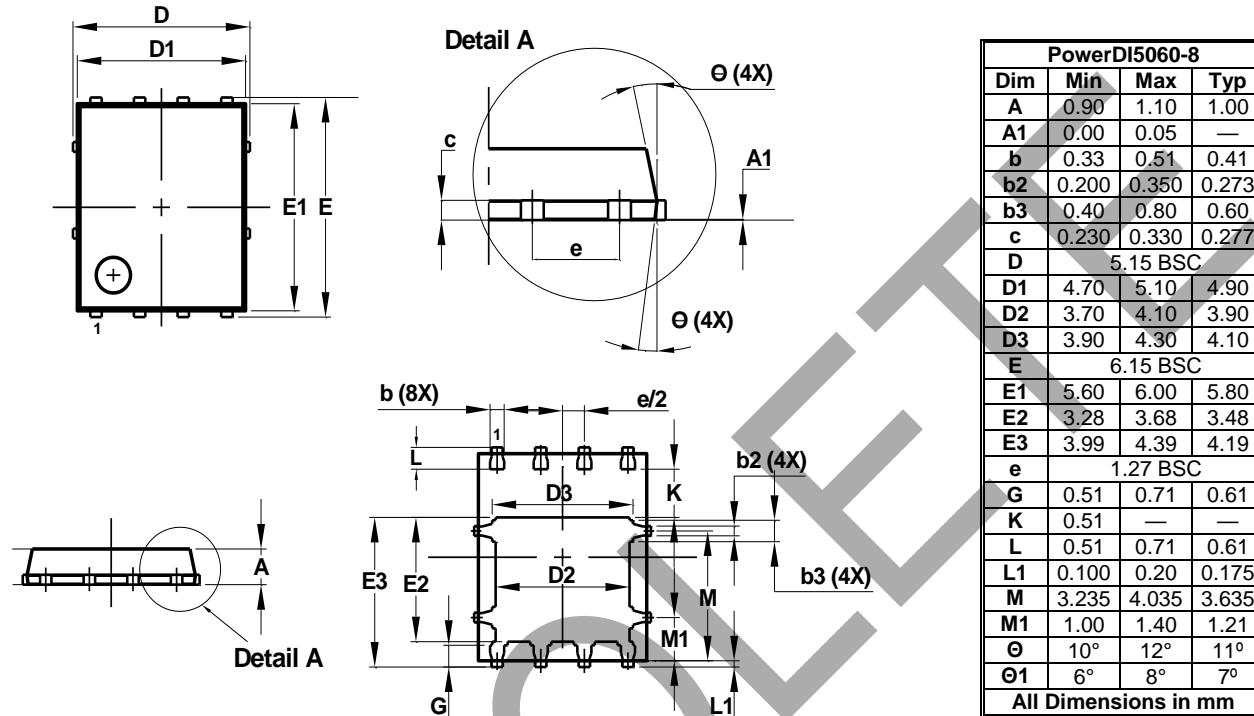


Figure 5 Forward Current Derating Curve

Package Outline Dimensions

Please see <http://www.diodes.com/package-outlines.html> for the latest version.

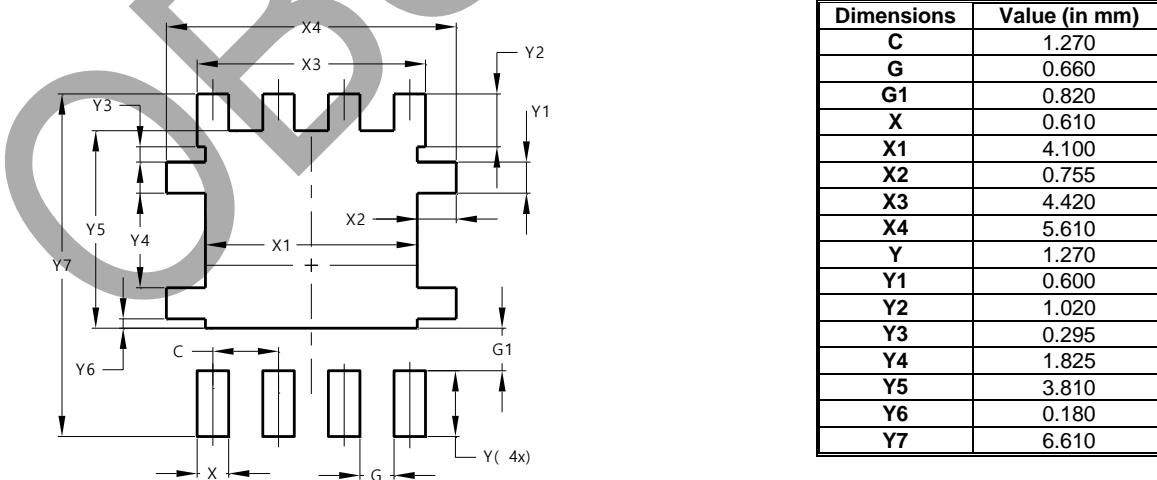
PowerDI5060-8



Suggested Pad Layout

Please see <http://www.diodes.com/package-outlines.html> for the latest version.

PowerDI5060-8



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