NGTG25N120FL2WG

IGBT - Field Stop II

This Insulated Gate Bipolar Transistor (IGBT) features a robust and cost effective Field Stop II Trench construction, and provides superior performance in demanding switching applications, offering both low on state voltage and minimal switching loss. The IGBT is well suited for UPS and solar applications.

Features

- Extremely Efficient Trench with Field Stop Technology
- T_{Jmax} = 175°C
- Optimized for High Speed Switching
- 10 μs Short Circuit Capability
- These are Pb−Free Devices

Typical Applications

- Solar Inverter
- Uninterruptible Power Inverter Supplies (UPS)
- Welding

ABSOLUTE MAXIMUM RATINGS

Rating	Symbol	Value	Unit
Collector−emitter voltage	V_{CES}	1200	V
Collector current @ TC = 25°C @ TC = 100°C	I_C	 50 25	A
Pulsed collector current, T_{pulse} limited by T_{Jmax}	I_{CM}	100	A
Gate−emitter voltage Transient gate−emitter voltage (T_{pulse} = 5 μs, D < 0.10)	V_{GE}	± 20 ±30	V
Power Dissipation @ TC = 25°C @ TC = 100°C	P_D	 385 192	W
Short Circuit Withstand Time V_{GE} = 15 V, V_{CE} = 500 V, $T_J \leq 150$°C	T_{SC}	10	μs
Operating junction temperature range	T_J	−55 to +175	°C
Storage temperature range	T_{stg}	−55 to +175	°C
Lead temperature for soldering, 1/8” from case for 5 seconds	T_{SLD}	260	°C

Stresses exceeding those listed in the Maximum Ratings table may damage the device. If any of these limits are exceeded, device functionality should not be assumed, damage may occur and reliability may be affected.

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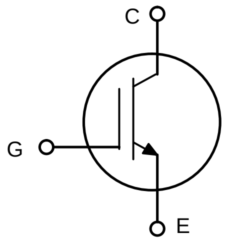
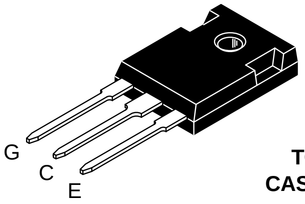
25 A, 1200 V

V_{CEsat} = 2.0 V

E_{off} = 0.60 mJ





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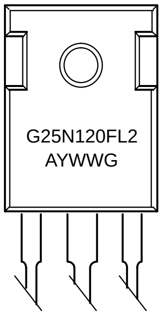
MARKING DIAGRAM



A = Assembly Location
Y = Year
WW = Work Week
G = Pb−Free Package

ORDERING INFORMATION

Device	Package	Shipping
NGTG25N120FL2WG	TO−247 (Pb−Free)	30 Units / Rail

June, 2014 − Rev. 0

THERMAL CHARACTERISTICS

Rating	Symbol	Value	Unit
Thermal resistance junction−to−case, for IGBT	$R_{\theta JC}$	0.39	°C/W
Thermal resistance junction−to−ambient	$R_{\theta JA}$	40	°C/W

ELECTRICAL CHARACTERISTICS (T_J = 25°C unless otherwise specified)

Parameter	Test Conditions	Symbol	Min	Typ	Max	Unit
STATIC CHARACTERISTIC						
Collector−emitter breakdown voltage, gate−emitter short−circuited	V_{GE} = 0 V, I_C = 500 μA	$V_{(BR)CES}$	1200	−	−	V
Collector−emitter saturation voltage	V_{GE} = 15 V, I_C = 25 A V_{GE} = 15 V, I_C = 25 A, T_J = 175°C	V_{CEsat}	− −	2.00 2.40	2.40 −	V
Gate−emitter threshold voltage	V_{GE} = V_{CE}, I_C = 400 μA	$V_{GE(th)}$	4.5	5.5	6.5	V
Collector−emitter cut−off current, gate−emitter short−circuited	V_{GE} = 0 V, V_{CE} = 1200 V V_{GE} = 0 V, V_{CE} = 1200 V, T_J = 175°C	I_{CES}	− −	− −	0.4 2	mA
Gate leakage current, collector−emitter short−circuited	V_{GE} = 20 V , V_{CE} = 0 V	I_{GES}	−	−	200	nA
Input capacitance	V_{CE} = 20 V, V_{GE} = 0 V, f = 1 MHz	C_{ies}	−	4420	−	pF
Output capacitance		C_{oes}	−	151	−	
Reverse transfer capacitance		C_{res}	−	81	−	
Gate charge total	V_{CE} = 600 V, I_C = 25 A, V_{GE} = 15 V	Q_g	−	178	−	nC
Gate to emitter charge		Q_{ge}	−	39	−	
Gate to collector charge		Q_{gc}	−	83	−	
SWITCHING CHARACTERISTIC, INDUCTIVE LOAD						
Turn−on delay time	T_J = 25°C V_{CC} = 600 V, I_C = 25 A R_g = 10 Ω V_{GE} = 0 V/ 15V*	$t_{d(on)}$	−	87	−	ns
Rise time		t_r	−	74	−	
Turn−off delay time		$t_{d(off)}$	−	179	−	
Fall time		t_f	−	136	−	
Turn−on switching loss		E_{on}	−	1.95	−	mJ
Turn−off switching loss		E_{off}	−	0.60	−	
Total switching loss		E_{ts}	−	2.55	−	
Turn−on delay time	T_J = 150°C V_{CC} = 600 V, I_C = 25 A R_g = 10 Ω V_{GE} = 0 V/ 15V*	$t_{d(on)}$	−	84	−	ns
Rise time		t_r	−	94	−	
Turn−off delay time		$t_{d(off)}$	−	185	−	
Fall time		t_f	−	245	−	
Turn−on switching loss		E_{on}	−	2.39	−	mJ
Turn−off switching loss		E_{off}	−	1.26	−	
Total switching loss		E_{ts}	−	3.65	−	

Product parametric performance is indicated in the Electrical Characteristics for the listed test conditions, unless otherwise noted. Product performance may not be indicated by the Electrical Characteristics if operated under different conditions.

*Includes diode reverse recovery loss using NGTB25N120FL2WG.

TYPICAL CHARACTERISTICS

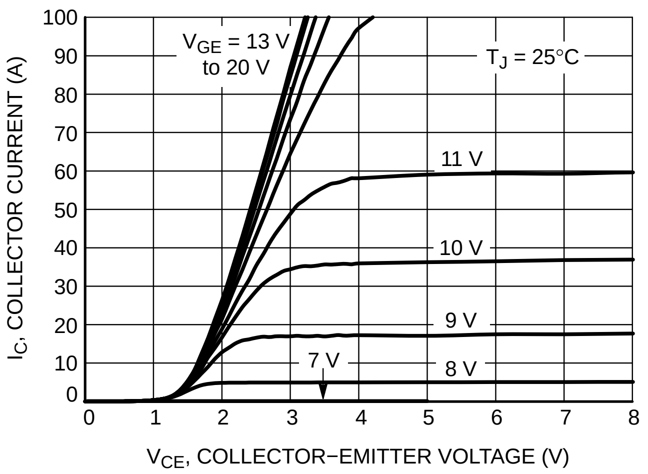


Figure 1. Output Characteristics

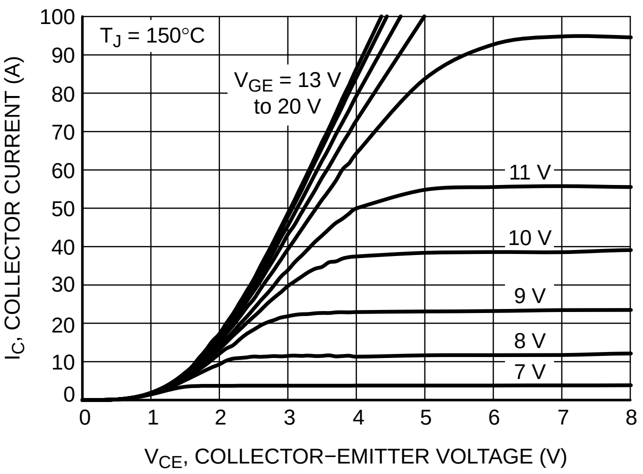


Figure 2. Output Characteristics

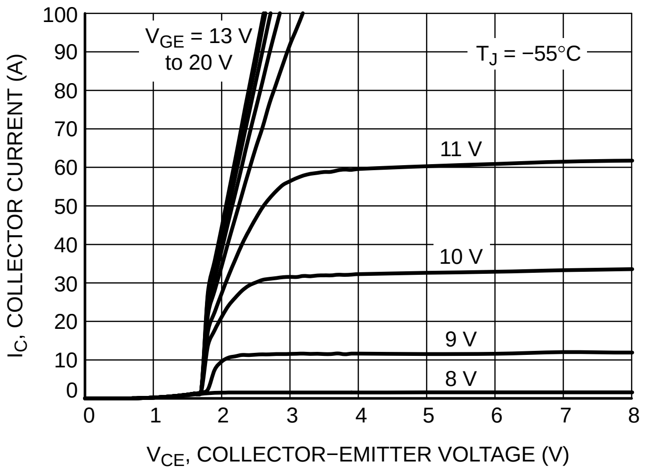


Figure 3. Output Characteristics

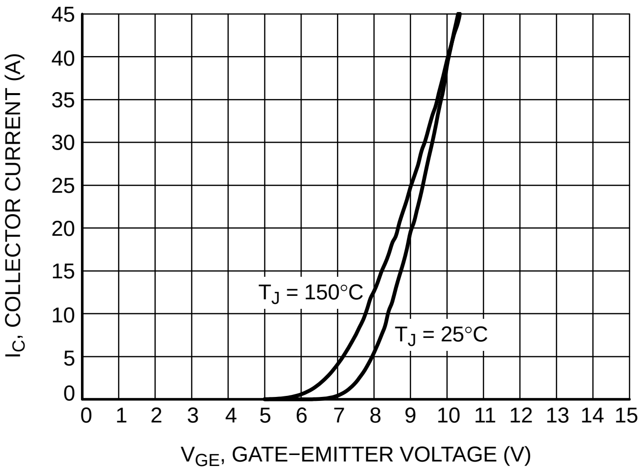


Figure 4. Typical Transfer Characteristics

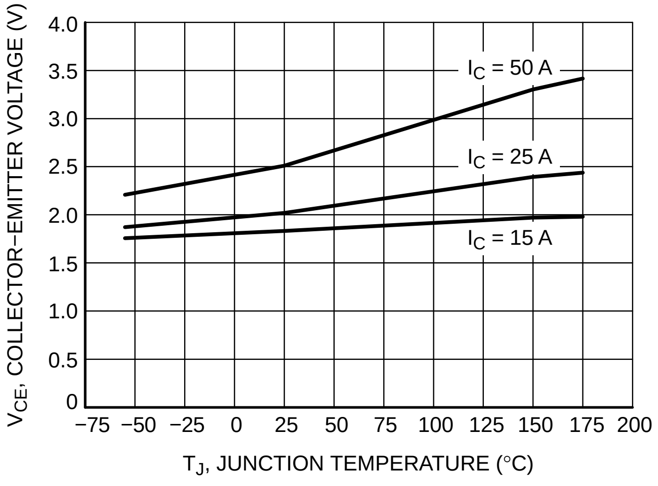


Figure 5. $V_{CE(sat)}$ vs. T_J

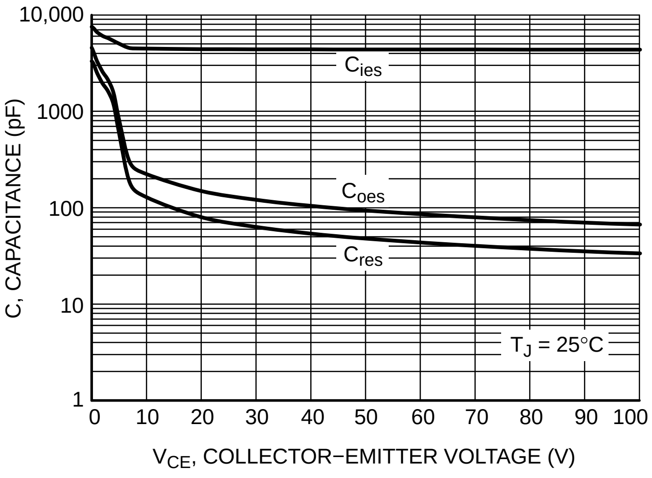


Figure 6. Typical Capacitance

TYPICAL CHARACTERISTICS

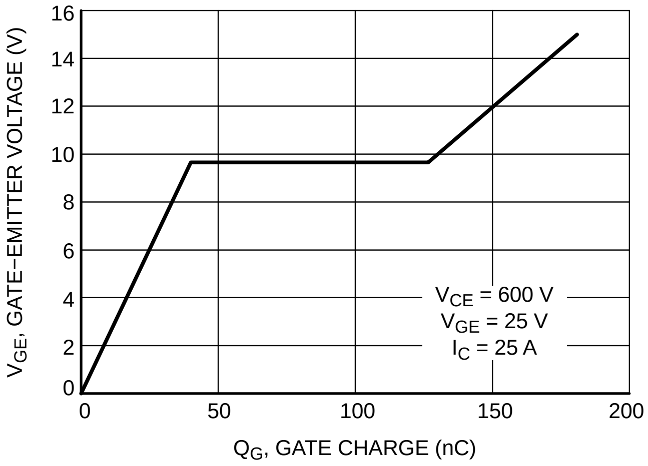


Figure 7. Typical Gate Charge

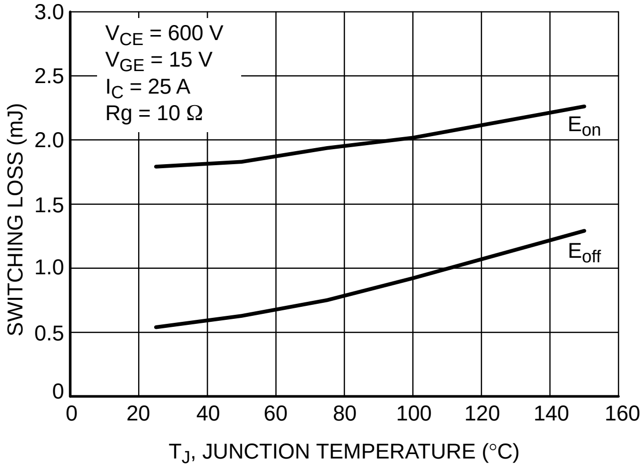


Figure 8. Switching Loss vs. Temperature

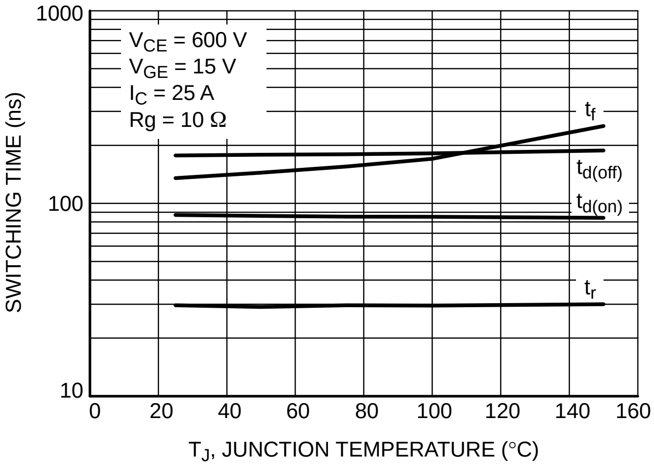


Figure 9. Switching Time vs. Temperature

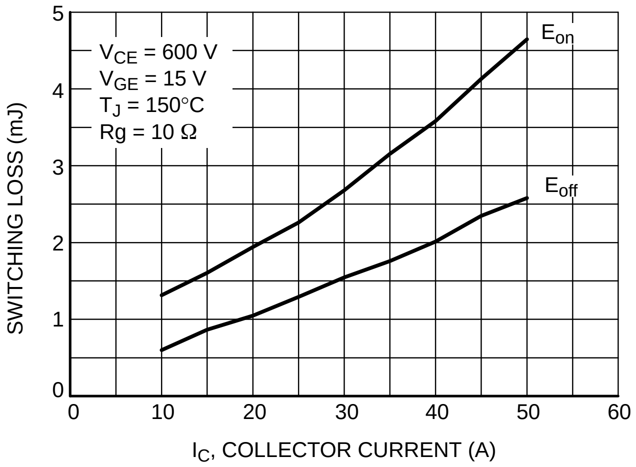


Figure 10. Switching Loss vs. I_C

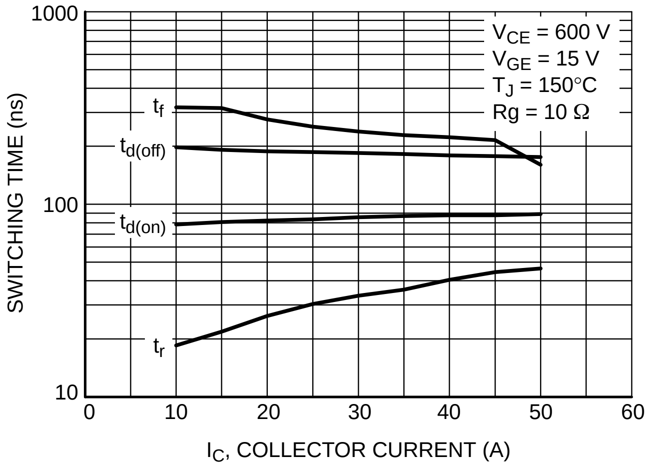


Figure 11. Switching Time vs. I_C

TYPICAL CHARACTERISTICS

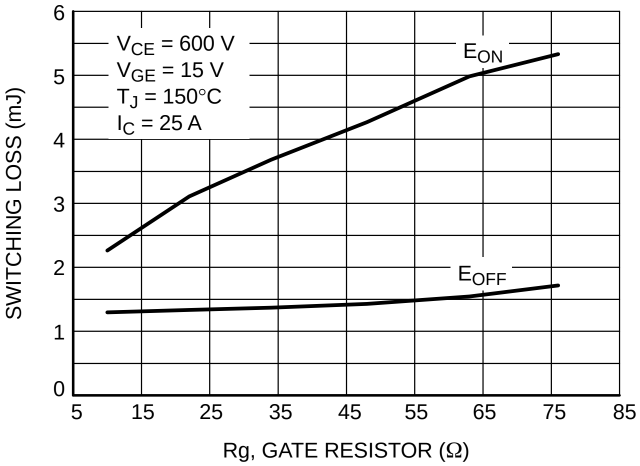


Figure 12. Switching Loss vs. Rg

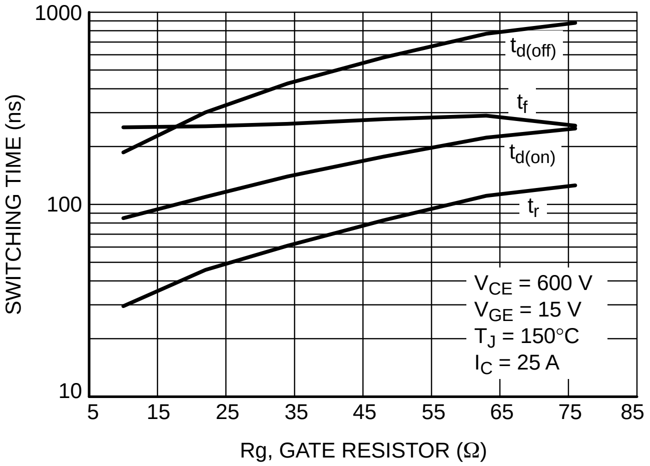


Figure 13. Switching Time vs. Rg

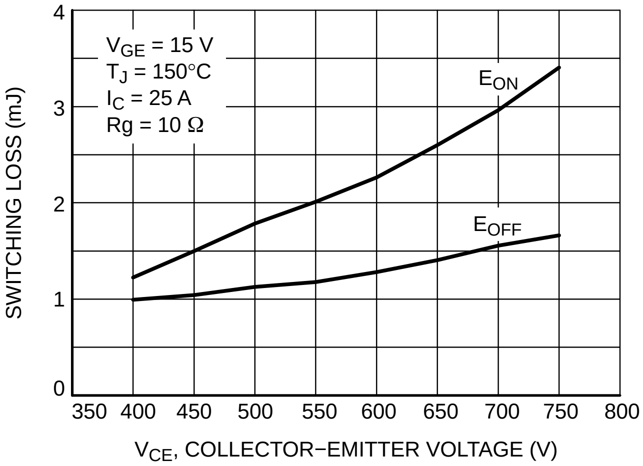


Figure 14. Switching Loss vs. V_{CE}

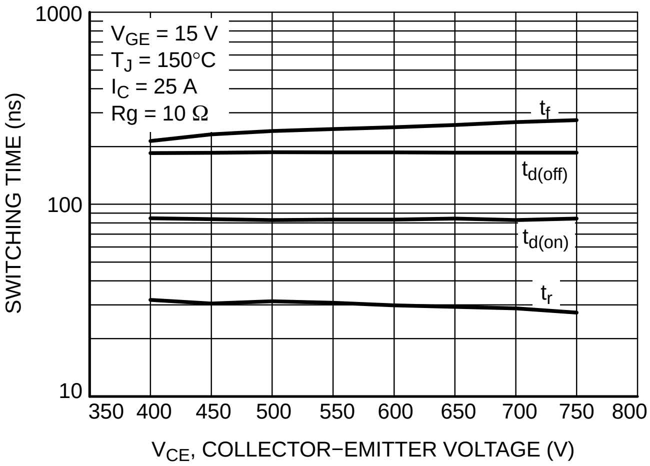


Figure 15. Switching Time vs. V_{CE}

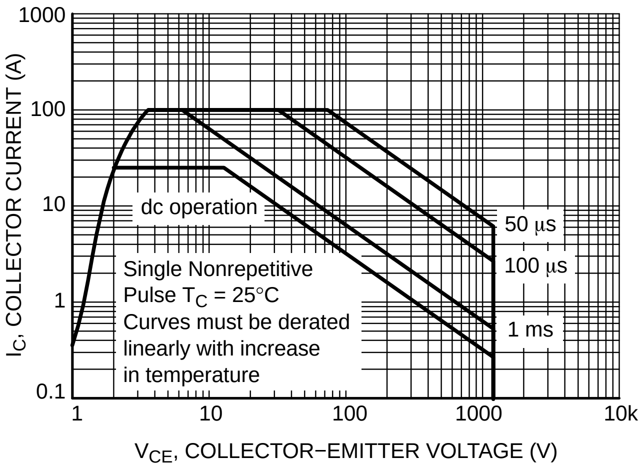


Figure 16. Safe Operating Area

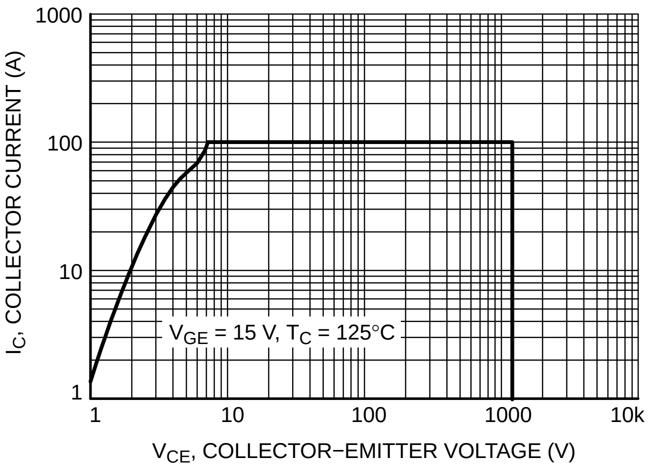
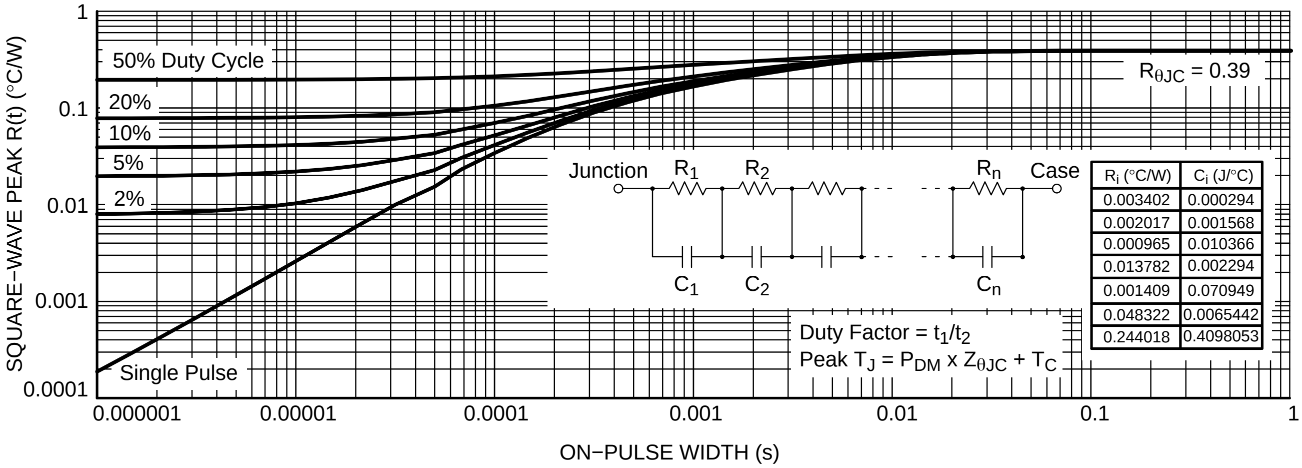


Figure 17. Reverse Bias Safe Operating Area

TYPICAL CHARACTERISTICS



R_i (°C/W)	C_i (J/°C)
0.003402	0.000294
0.002017	0.001568
0.000965	0.010366
0.013782	0.002294
0.001409	0.070949
0.048322	0.0065442
0.244018	0.4098053

Figure 18. IGBT Die Self−heating Square−wave Duty Cycle Transient Thermal Response

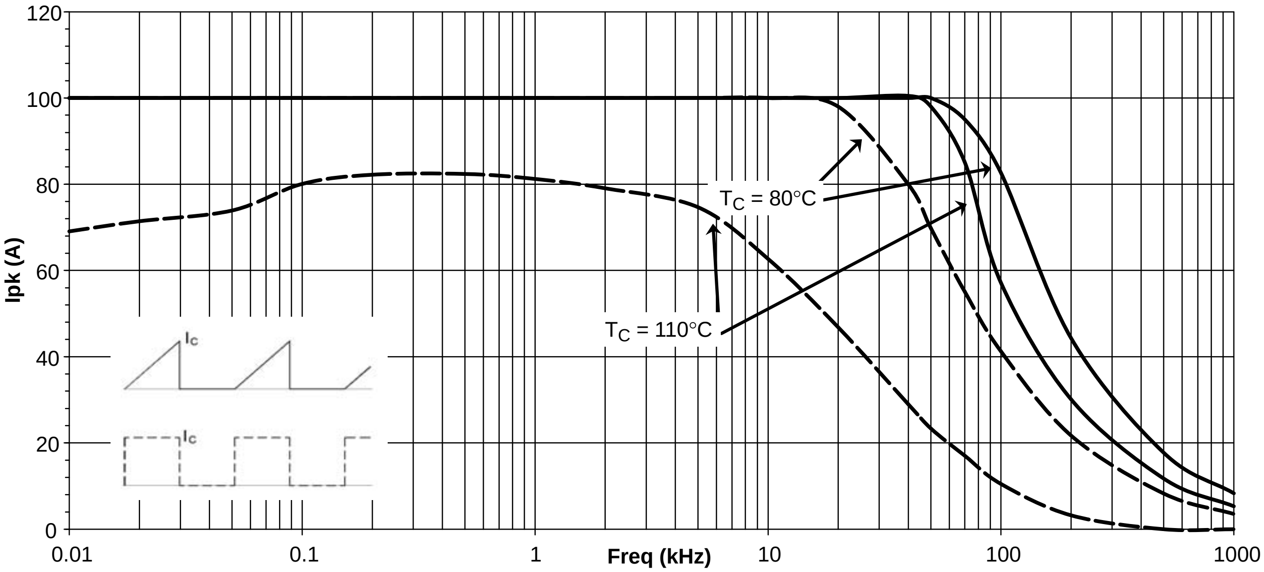


Figure 19. Collector Current vs. Switching Frequency

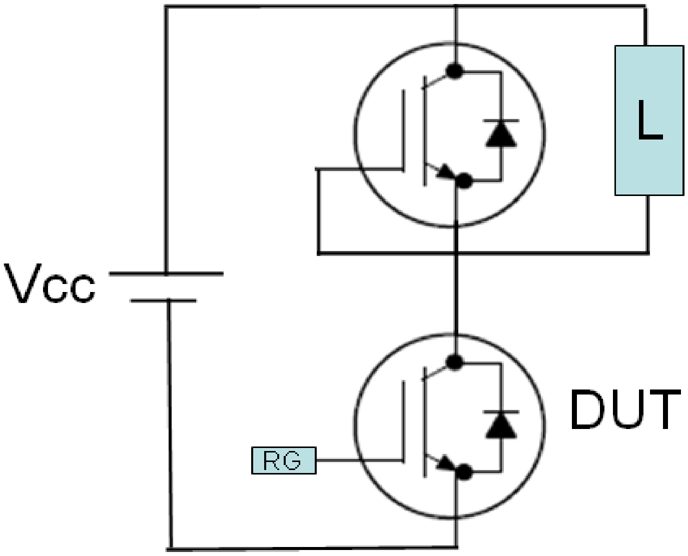


Figure 20. Test Circuit for Switching Characteristics

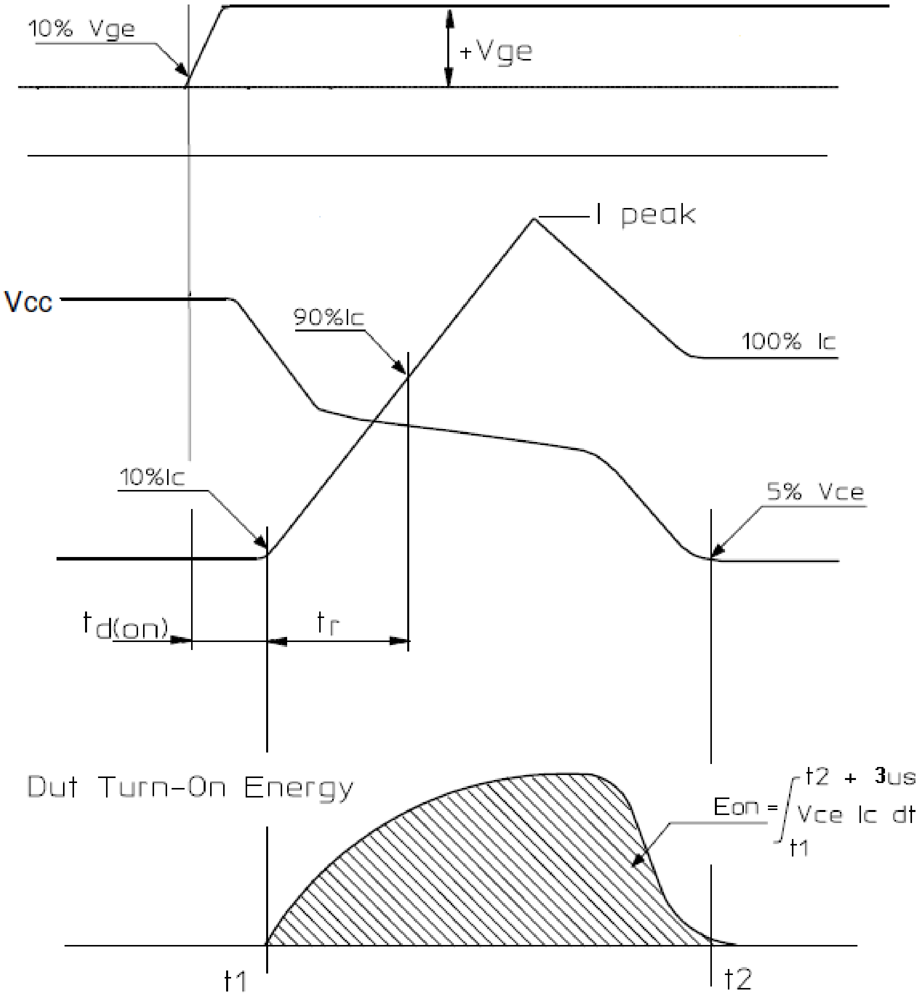


Figure 21. Definition of Turn On Waveform

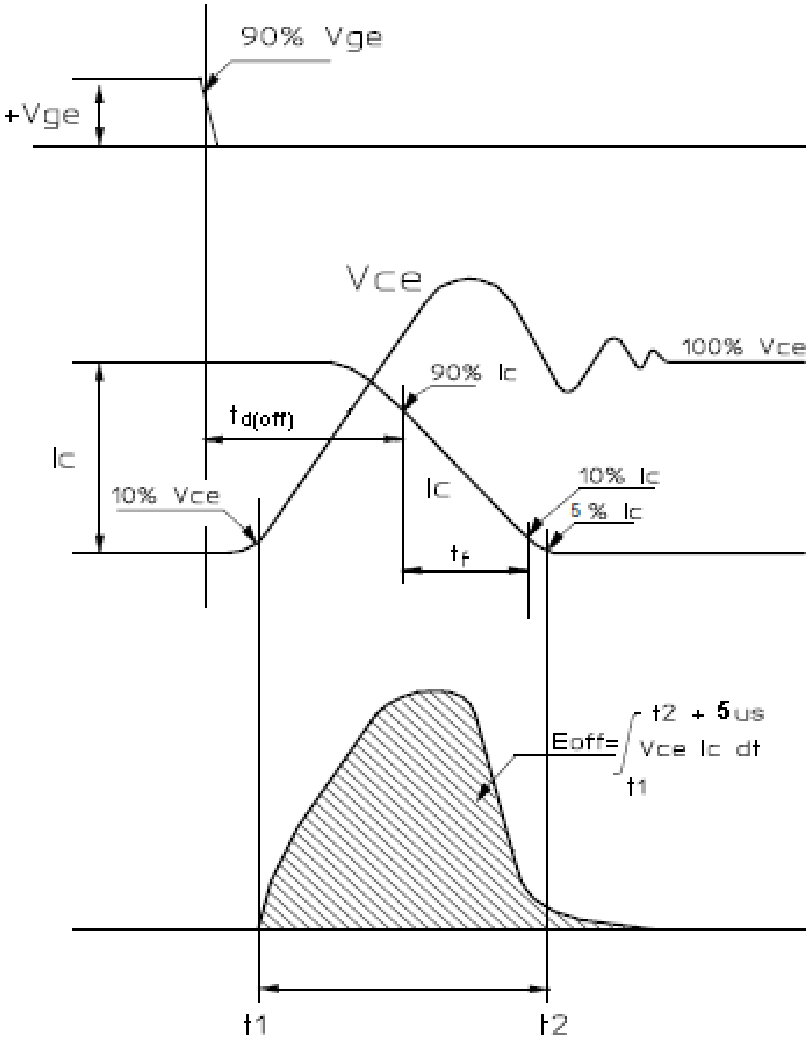
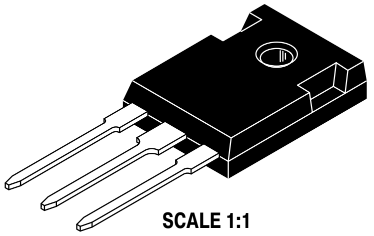


Figure 22. Definition of Turn Off Waveform

MECHANICAL CASE OUTLINE
PACKAGE DIMENSIONS



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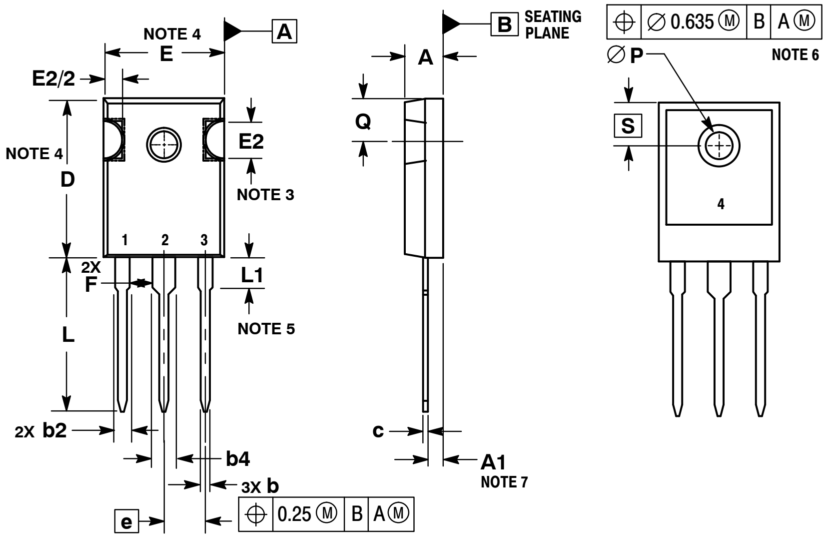
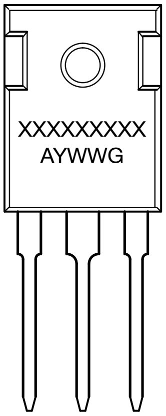


NOTES:
1. DIMENSIONING AND TOLERANCING PER ASME Y14.5M, 1994.
2. CONTROLLING DIMENSION: MILLIMETERS.
3. SLOT REQUIRED, NOTCH MAY BE ROUNDED.
4. DIMENSIONS D AND E DO NOT INCLUDE MOLD FLASH. MOLD FLASH SHALL NOT EXCEED 0.13 PER SIDE. THESE DIMENSIONS ARE MEASURED AT THE OUTERMOST EXTREME OF THE PLASTIC BODY.
5. LEAD FINISH IS UNCONTROLLED IN THE REGION DEFINED BY L1.
6. ∅P SHALL HAVE A MAXIMUM DRAFT ANGLE OF 1.5° TO THE TOP OF THE PART WITH A MAXIMUM DIAMETER OF 3.91.
7. DIMENSION A1 TO BE MEASURED IN THE REGION DEFINED BY L1.

	MILLIMETERS	
DIM	MIN	MAX
A	4.70	5.30
A1	2.20	2.60
b	1.07	1.33
b2	1.65	2.35
b4	2.60	3.40
c	0.45	0.68
D	20.80	21.34
E	15.50	16.25
E2	4.32	5.49
e	5.45 BSC	
F	2.655	---
L	19.80	20.80
L1	3.81	4.32
P	3.55	3.65
Q	5.40	6.20
S	6.15 BSC	

GENERIC MARKING DIAGRAM*



XXXXX	= Specific Device Code
A	= Assembly Location
Y	= Year
WW	= Work Week
G	= Pb−Free Package

*This information is generic. Please refer to device data sheet for actual part marking.
Pb−Free indicator, “G” or microdot “ ▪”, may or may not be present.

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