Programming Software

Control FPWIN GR7

Related Information ■General terms and conditions..... F-3

panasonic.net/id/pidsx/global

Features

- **Configuration, editing programming, searching, monitoring, debugging, security, etc.**

PLC programming demands a lot of time and effort. Many programmers get hung up on trying out different configurations, consulting the manual, and re-writing repetitive code blocks.
The **Control FPWIN GR7** programming software is designed to eliminate these inefficiencies and minimize programming complexity.

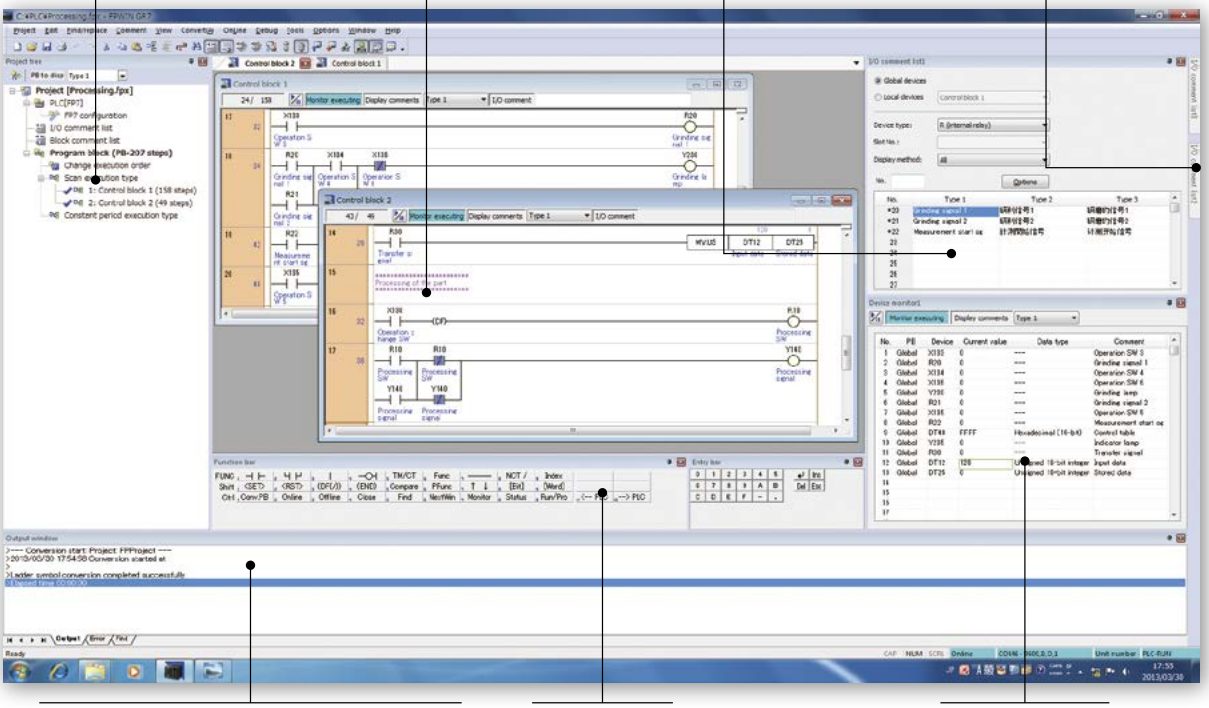
Save time on programming with user-friendly software

Project tree

Program block

I/O comments
Three types of comments can be entered in a column.

Task bar
The display can be scrolled as needed. Effective use of screen.



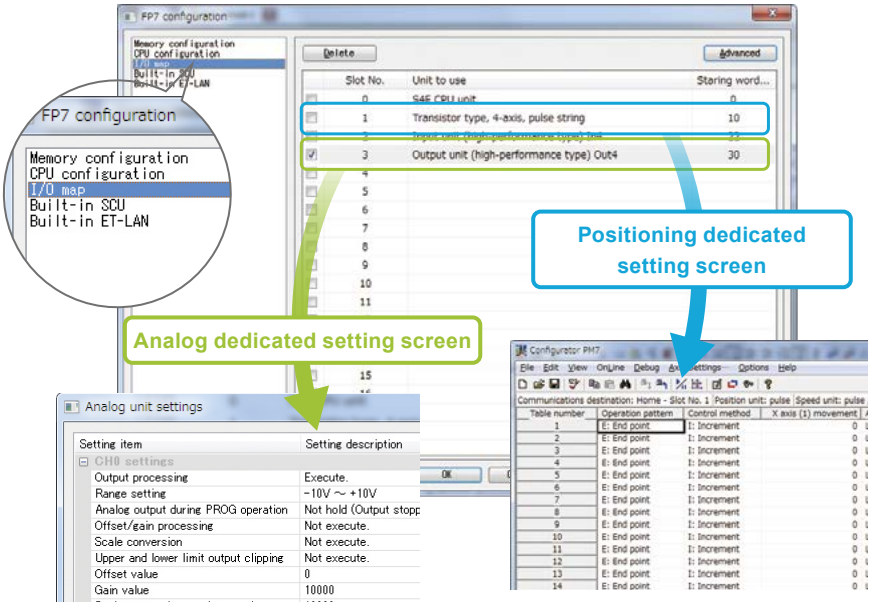
Output Window
Display history (output and errors), search results, etc.

Function bar

Device monitor

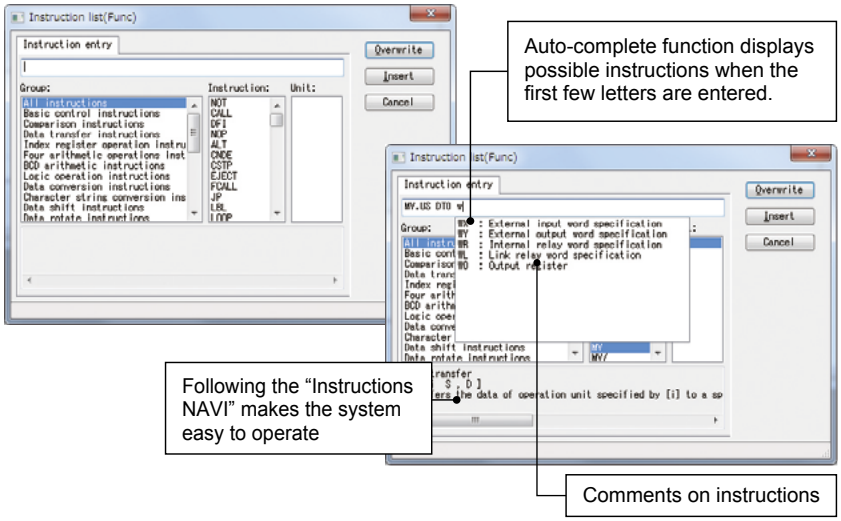
Save time on initial setting

Configuration settings, including those for installed units, can be made directly from the same screen.
This eliminates the need to use other software to accomplish this task.



Save time and effort by using the “Instructions NAVI”

Enter high level instructions by simply selecting the correct order as dictated by the “Instructions NAVI”. The help dialog also supports the selection of high level instructions.



Save time when cross-checking instructions

Comments are directly switchable on the main screen. Various tasks, such as comment rewriting by end users, can be streamlined.
Bulk imported and exported in CSV format comments enables editing of text only in comments.
All languages supported by Windows® are available.

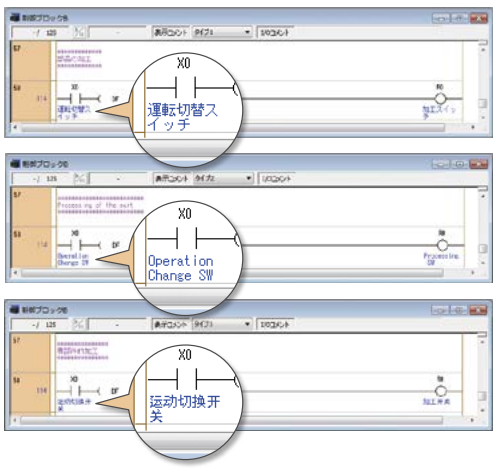
* Windows is a registered trademark or trademark of Microsoft Corporation in the United States and other countries.

	Example 1	Example 2
Type 1	For design	Japanese
Type 2	For production	English
Type 3	For maintenance	Chinese

Program blocks, block comments, I/O comments and annotation comments can be entered in three types.

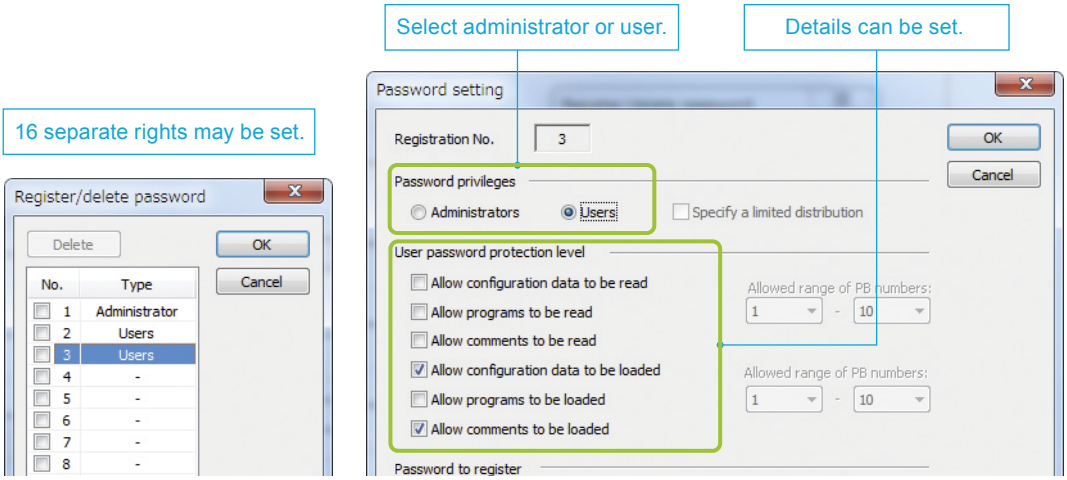
Displayed comments on ladder diagram can be switched.

Three comment types can be registered.



Save time when setting up program security

Access rights to the CPU unit can be made more stringent for settings, to prevent easy access to editing, or program outflow.

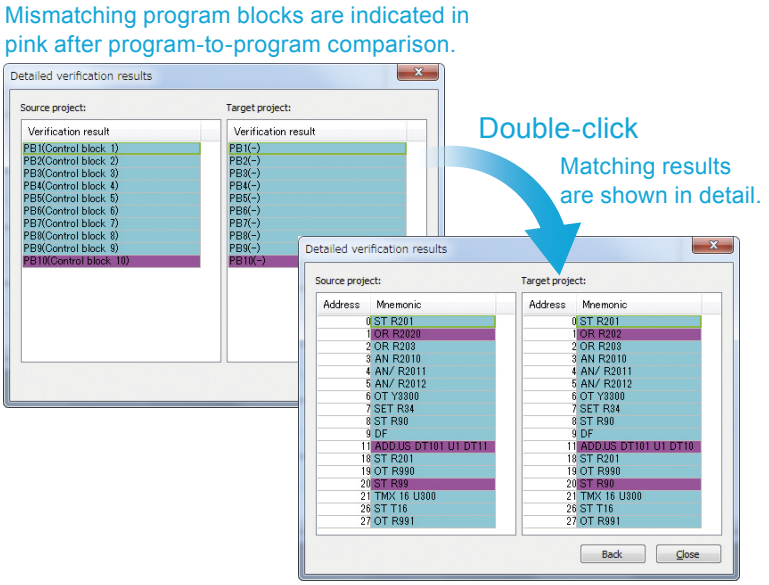


Save time when matching programs

Programs stored in the CPU unit and on the PC can be cross-checked to identify any non-matching portions. This feature is useful for program search and for finding where modifications are needed.

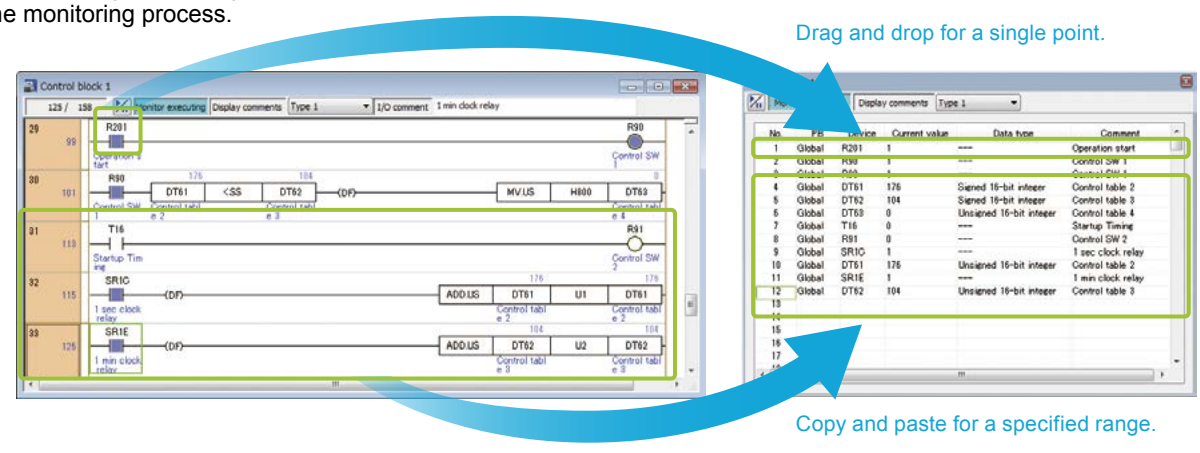
Application example 1
If you want to confirm that programs on the CPU unit and the PC are identical, you can make an instant check.

Application example 2
Content edited by other designers can be checked.



Save time when monitoring operations

Multipoint monitoring devices can be registered easily. It allows you to speed up the monitoring process.



ORDER GUIDE

Product name		Type	Specifications	Part No.
Programming software for Windows® **Control FPWIN GR7**	Japanese version	Supports only CPU unit without encryption function	Windows® 10 (32-bit / 64-bit) / Windows® 8.1 (32-bit / 64-bit) / Windows® 8 (32-bit / 64-bit) / Windows® 7 SP1 or more (32-bit / 64-bit) / Windows® Vista SP2 / Windows® XP SP3	**AFPSGR7JP**
	Security enhanced type	Supports both CPU unit with/without encryption function		**AFPSGR7JPS**
	English version	Supports only CPU unit without encryption function		**AFPSGR7EN**
	Security enhanced type	Supports both CPU unit with/without encryption function		**AFPSGR7ENS**

Note: Windows is a registered trademark or trademark of Microsoft Corporation in the United States and other countries.

SYSTEM REQUIREMENTS

Item	Specifications
OS	Windows® 10 (32-bit / 64-bit) / Windows® 8.1 (32-bit / 64-bit) / Windows® 8 (32-bit / 64-bit) / Windows® 7 SP1 or more (32-bit / 64-bit) / Windows® Vista SP2 / Windows® XP SP3
Available hard disk space	120 MB or more
CPU	Intel® Core™2 Duo 2 GHz or more
System RAM	1 GB or more
Display resolution	1,280 × 800 or more
Applicable PLCs	**FP7** / **FP0R** (Note 2) / **FP-X** (Note 3) / **FP-X0** (Note 3) / **FPΣ** (Note 3) / **FP2** (Note 3) / **FP2SH** (Note 3)

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2) Supported from Ver.2.9 (For creating divided programs, **FP0R** version 1.20 or later is required.)
3) Supported from Ver.2.14.0. However, there are the following restrictions.
• Mnemonic ladder is unsupported.
• IC card operation function of **FP2** is unsupported.
• **FP0** mode of **FP0R** is unsupported.