

CPH6443



Power MOSFET

35V, 37mΩ, 6A, Single N-Channel

This Power MOSFET is produced using ON Semiconductor's trench technology, which is specifically designed to minimize gate charge and low on resistance. This device is suitable for applications with low gate charge driving or low on resistance requirements.

Features

- Low On-Resistance
- 4V drive
- ESD Diode-Protected Gate
- Pb-Free, Halogen Free and RoHS compliance

Typical Applications

- DC/DC Converter
- Current Balance Switch for Backlight

SPECIFICATIONS

ABSOLUTE MAXIMUM RATING at $T_a = 25^\circ\text{C}$ (Note 1)

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Parameter	Symbol	Value	Unit
Drain to Source Voltage	V_{DSS}	35	V
Gate to Source Voltage	V_{GSS}	± 20	V
Drain Current (DC)	I_D	6	A
Drain Current (Pulse) $PW \leq 10\mu\text{s}$, duty cycle $\leq 1\%$	I_{DP}	24	A
Power Dissipation When mounted on ceramic substrate ($1200\text{mm}^2 \times 0.8\text{mm}$)	P_D	1.6	W
Junction Temperature	T_J	150	$^\circ\text{C}$
Storage Temperature	T_{STG}	-55 to +150	$^\circ\text{C}$

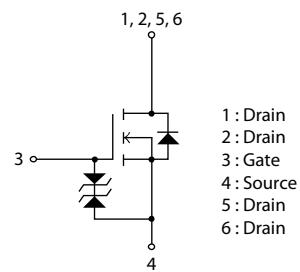
Note 1 : Stresses exceeding those listed in the Maximum Ratings table may damage the device. If any of these limits are exceeded, device functionality should not be assumed, damage may occur and reliability may be affected.

THERMAL RESISTANCE RATINGS

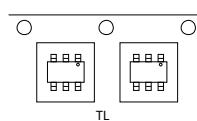
Parameter	Symbol	Value	Unit
Junction to Ambient When mounted on ceramic substrate (1200mm ² × 0.8mm)	R _{θJA}	78.1	°C/W

V_{DSS}	$R_{DS(on)}$ Max	I_D Max
35V	37m Ω @ 10V	6A
	61m Ω @ 4.5V	
	73m Ω @ 4V	

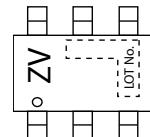
ELECTRICAL CONNECTION N-Channel



PACKING TYPE : TL



MARKING



ORDERING INFORMATION

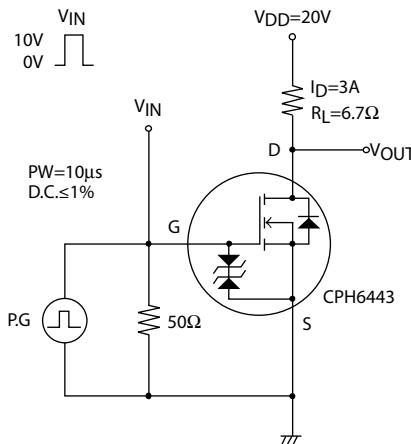
See detailed ordering and shipping information on page 5 of this data sheet.

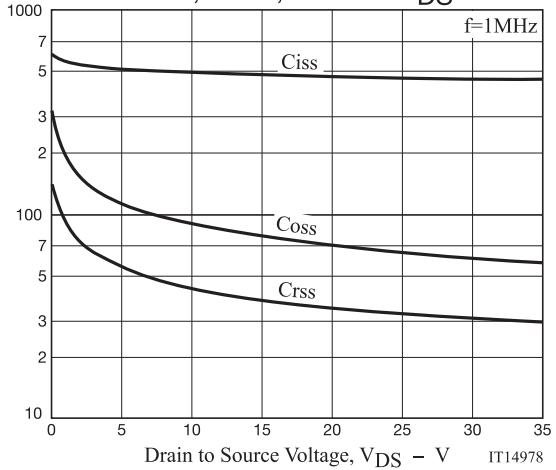
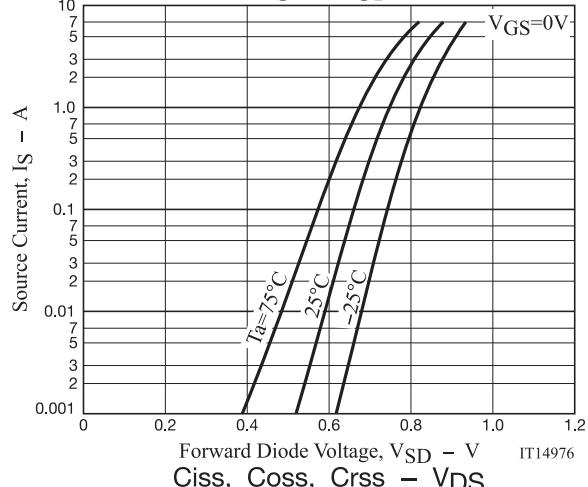
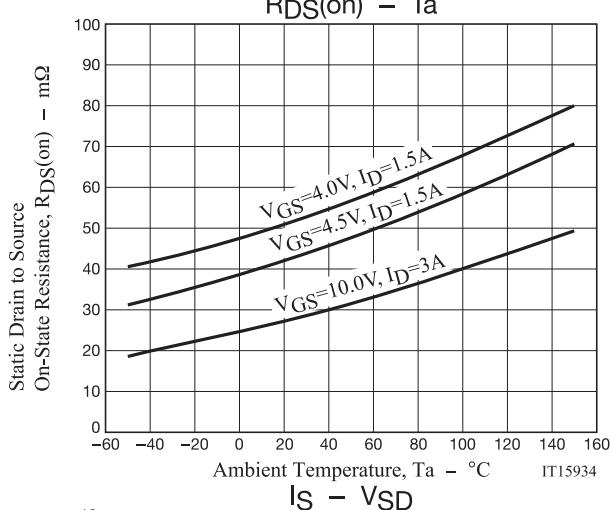
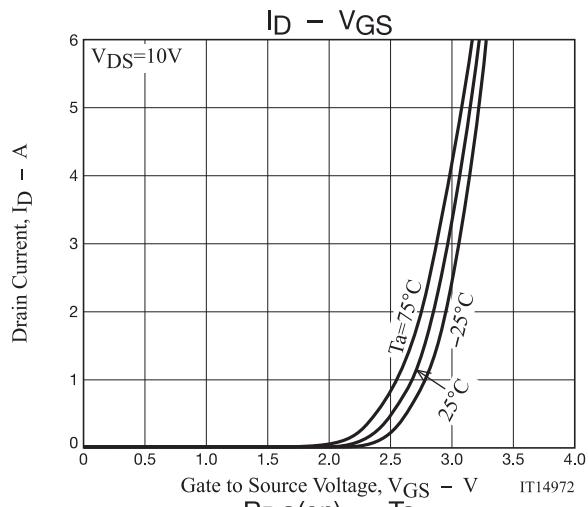
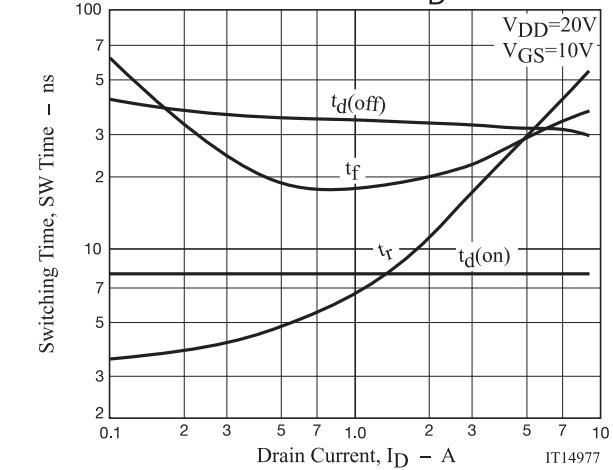
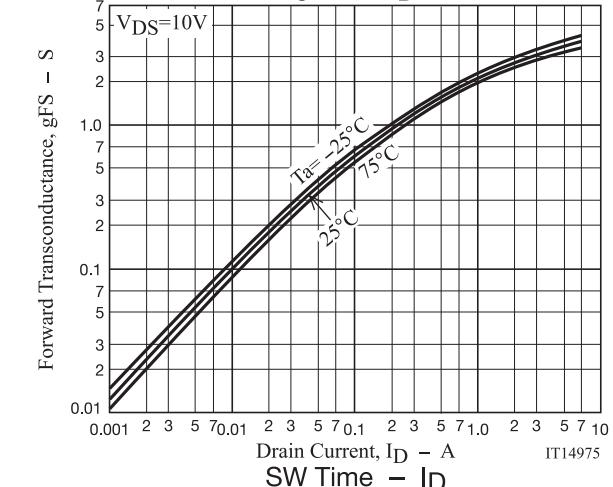
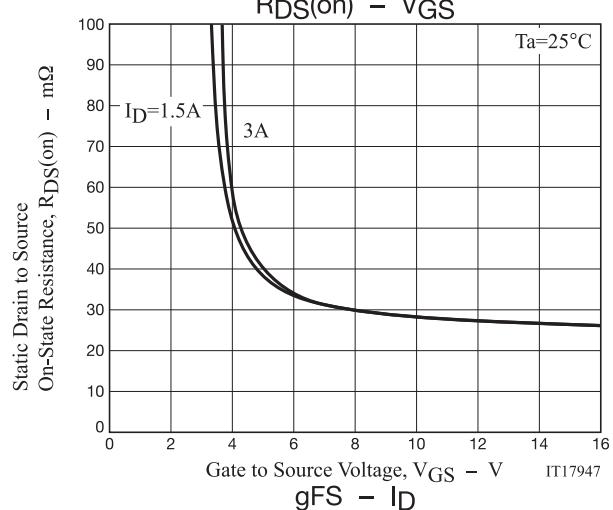
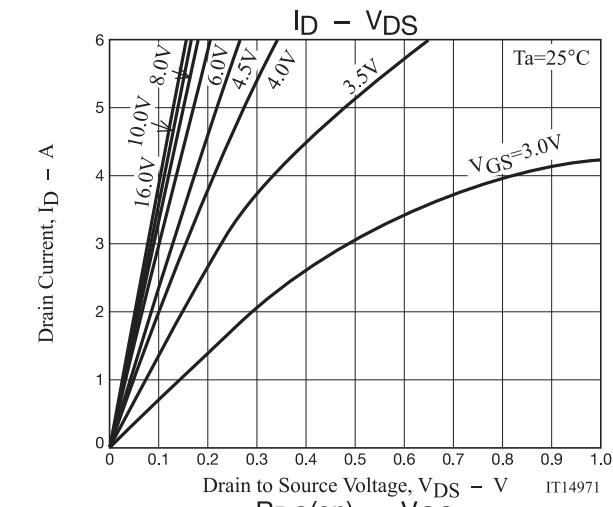
ELECTRICAL CHARACTERISTICS at $T_a = 25^\circ\text{C}$ (Note 2)

Parameter	Symbol	Conditions	Value			Unit
			min	typ	max	
Drain to Source Breakdown Voltage	$V(\text{BR})\text{DSS}$	$I_D=1\text{mA}, V_{GS}=0\text{V}$	35			V
Zero-Gate Voltage Drain Current	I_{DSS}	$V_{DS}=35\text{V}, V_{GS}=0\text{V}$			1	μA
Gate to Source Leakage Current	I_{GSS}	$V_{GS}=\pm 16\text{V}, V_{DS}=0\text{V}$			± 10	μA
Gate Threshold Voltage	$V_{GS(\text{th})}$	$V_{DS}=10\text{V}, I_D=1\text{mA}$	1.2		2.6	V
Forward Transconductance	g_{FS}	$V_{DS}=10\text{V}, I_D=3\text{A}$		2.9		S
Static Drain to Source On-State Resistance	$R_{DS(\text{on})1}$	$I_D=3\text{A}, V_{GS}=10\text{V}$		28	37	$\text{m}\Omega$
	$R_{DS(\text{on})2}$	$I_D=1.5\text{A}, V_{GS}=4.5\text{V}$		43	61	$\text{m}\Omega$
	$R_{DS(\text{on})3}$	$I_D=1.5\text{A}, V_{GS}=4\text{V}$		52	73	$\text{m}\Omega$
Input Capacitance	C_{iss}	$V_{DS}=20\text{V}, f=1\text{MHz}$		470		pF
Output Capacitance	C_{oss}			70		pF
Reverse Transfer Capacitance	C_{rss}			35		pF
Turn-ON Delay Time	$t_{\text{q(on)}}$	See specified Test Circuit		8		ns
Rise Time	t_r			17		ns
Turn-OFF Delay Time	$t_{\text{q(off)}}$			32		ns
Fall Time	t_f			22		ns
Total Gate Charge	Q_g	$V_{DS}=20\text{V}, V_{GS}=10\text{V}, I_D=6\text{A}$		10		nC
Gate to Source Charge	Q_{gs}			2		nC
Gate to Drain "Miller" Charge	Q_{gd}			2		nC
Forward Diode Voltage	V_{SD}	$I_S=6\text{A}, V_{GS}=0\text{V}$		0.84	1.2	V

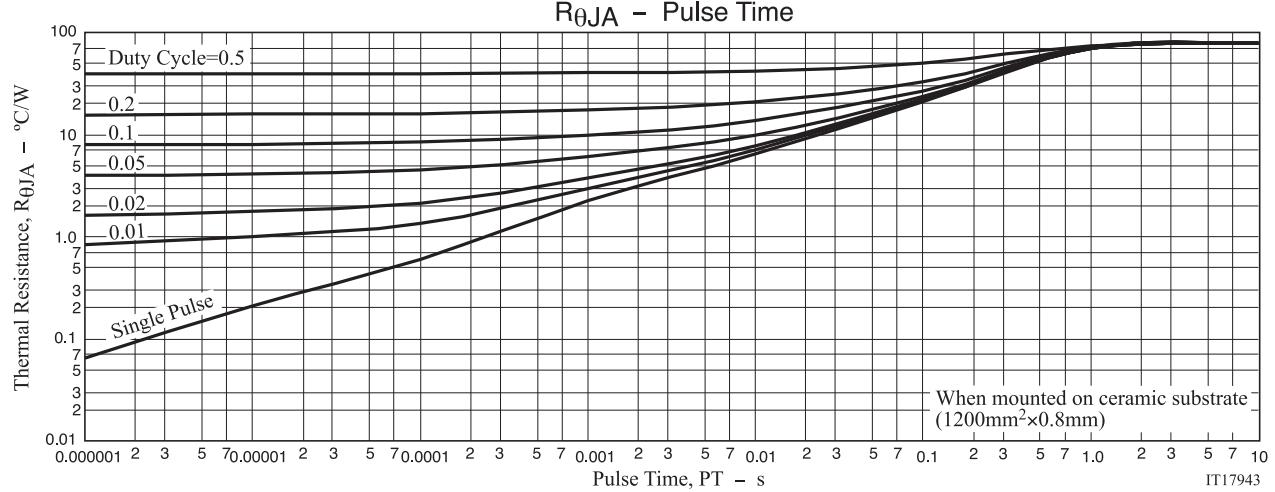
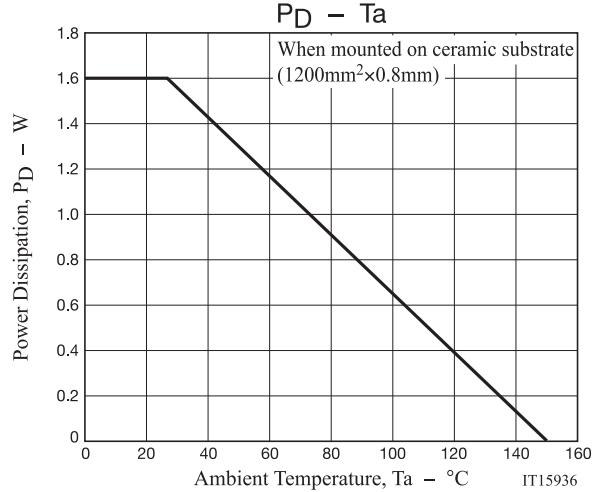
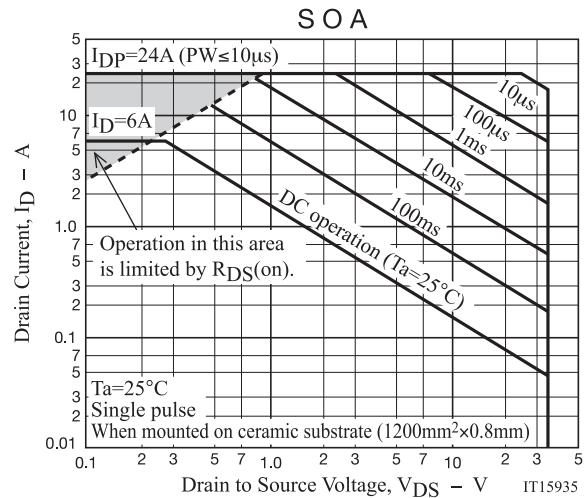
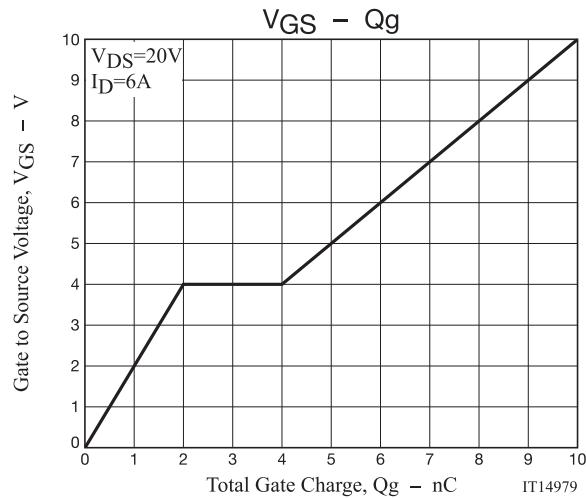
Note 2 : Product parametric performance is indicated in the Electrical Characteristics for the listed test conditions, unless otherwise noted. Product performance may not be indicated by the Electrical Characteristics if operated under different conditions.

Switching Time Test Circuit





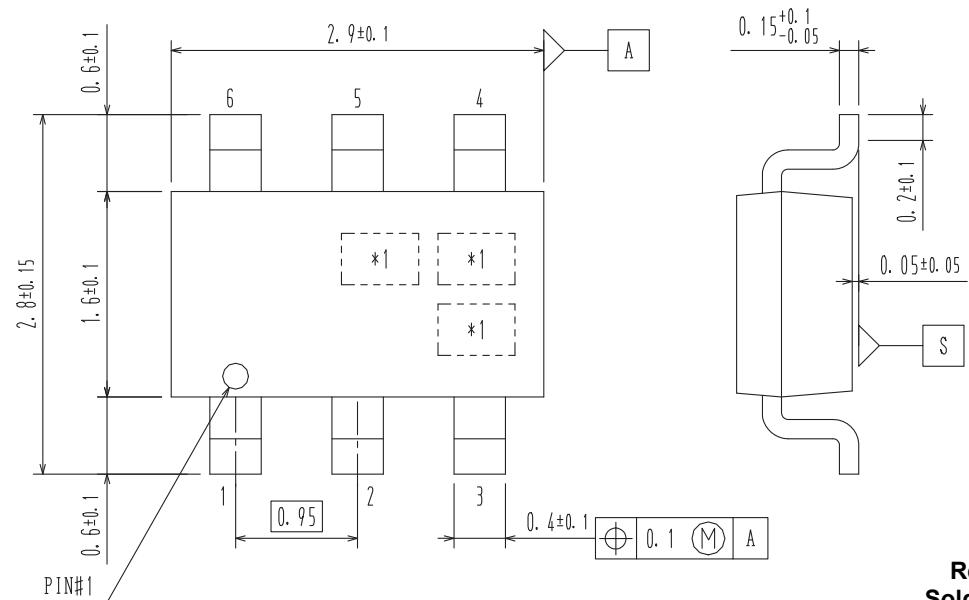
CPH6443



PACKAGE DIMENSIONS

unit : mm

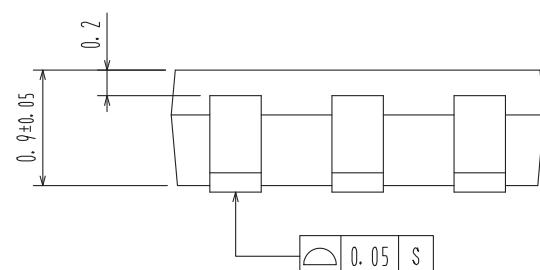
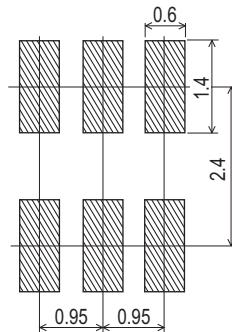
CPH6
CASE 318BD
ISSUE Q



Recommended Soldering Footprint

*1: Lot indication

- 1 : Drain
- 2 : Drain
- 3 : Gate
- 4 : Source
- 5 : Drain
- 6 : Drain



ORDERING INFORMATION

Device	Marking	Package	Shipping (Qty / Packing)
CPH6443-TL-H	ZV	CPH6	3,000 / Tape & Reel
CPH6443-TL-W		(Pb-Free / Halogen Free)	

[†] For information on tape and reel specifications, including part orientation and tape sizes, please refer to our Tape and Reel Packaging Specifications Brochure, BRD8011/D. http://www.onsemi.com/pub_link/Collateral/BRD8011-D.PDF

Note on usage : Since the CPH6443 is a MOSFET product, please avoid using this device in the vicinity of highly charged objects.

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