

SURFACE MOUNT HIGH DENSITY 2 AMP SILICON SCHOTTKY BRIDGE RECTIFIER



APPLICATIONS:Input rectification

• Polarity protection

· Steering diode array

# Central Semiconductor Corp.

www.centralsemi.com

# DESCRIPTION:

The CENTRAL SEMICONDUCTOR CBRLDSH2-40 is a full wave Schottky bridge rectifier mounted in a low profile epoxy surface mount molded case, utilizing glass passivated chips.

## MARKING CODE: CL40

# FEATURES:

- Low leakage current (5.5µA TYP @ V<sub>RRM</sub>)
- High 2.0A current rating
- Low V<sub>F</sub> Schottky diodes (480mV TYP @ I<sub>F</sub>=2.0A)
- Low profile package (1.6mm MAX)

	ATINGS: (T <sub>A</sub> =25°C unless otherwise noted)	SYMBOL		UNITS					
Peak Repetitive Reverse Voltage		V <sub>RRM</sub>	40	V					
DC Blocking Voltage		V <sub>R</sub>	40	V					
RMS Reverse Voltage		V <sub>R(RMS)</sub>	28	V					
Average Forward Current (T <sub>L</sub> =50°C)		Ι <sub>Ο</sub>	2.0	А					
Peak Forward Surge Current		IFSM	50	А					
Operating Junction Temperature		Тј	-55 to +125	°C					
Storage Temperature		T <sub>stg</sub>	-55 to +150	°C					
Thermal Resistance		$\Theta_{JA}$	95	°C/W					
ELECTRICAL CHARACTERISTICS PER DIODE: (TA=25°C unless otherwise noted)									
SYMBOL	TEST CONDITIONS	TYP	MAX	UNITS					
IR	V <sub>R</sub> =40V	5.5	100	μA					
IR	V <sub>R</sub> =40V, T <sub>A</sub> =100°C	2.0	20	mA					
V <sub>F</sub>	I <sub>F</sub> =2.0A	480	500	mV					
Сј	V <sub>R</sub> =4.0V, f=1.0MHz	155	250	pF					

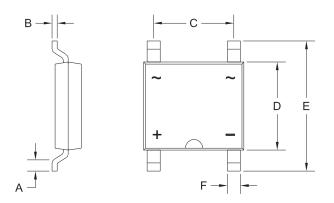
R1 (26-November 2012)

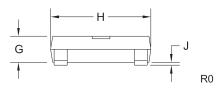




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# LPDIP CASE - MECHANICAL OUTLINE





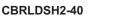
DIMENSIONS							
	INCHES		MILLIMETERS				
SYMBOL	MIN	MAX	MIN	MAX			
A	0.019	0.028	0.50	0.70			
В	0.007	0.012	0.20	0.30			
С	0.153	0.162	3.90	4.10			
D	0.167	0.179	4.25	4.55			
E	0.248	0.264	6.30	6.70			
F	0.023	0.028	0.60	0.70			
G	0.045	0.058	1.15	1.45			
Н	0.190	0.203	4.85	5.15			
J	0.002	0.006	0.05	0.15			

LPDIP (REV: R0)

#### MARKING CODE: CL40

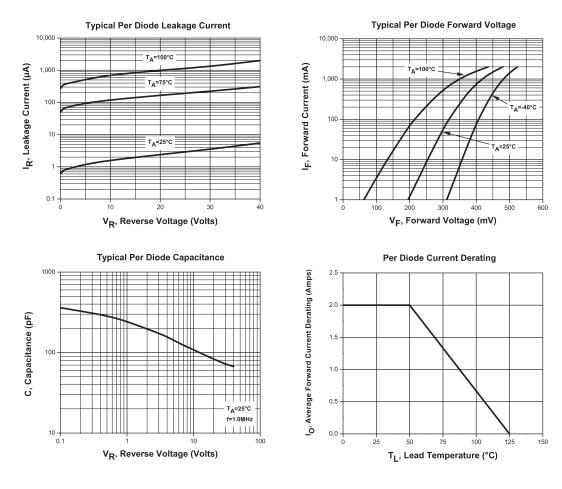
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# TYPICAL ELECTRICAL CHARACTERISTICS

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# OUTSTANDING SUPPORT AND SUPERIOR SERVICES

### PRODUCT SUPPORT

Central's operations team provides the highest level of support to insure product is delivered on-time.

- Supply management (Customer portals)
- Inventory bonding
- Consolidated shipping options

## **DESIGNER SUPPORT/SERVICES**

Central's applications engineering team is ready to discuss your design challenges. Just ask.

- Free quick ship samples (2<sup>nd</sup> day air)
- Online technical data and parametric search
- SPICE models
- Custom electrical curves
- · Environmental regulation compliance
- Customer specific screening
- Up-screening capabilities

· Custom product packing

Custom bar coding for shipments

- Special wafer diffusions
- PbSn plating options
- Package details
- Application notes
- · Application and design sample kits
- · Custom product and package development

# **REQUESTING PRODUCT PLATING**

- If requesting Tin/Lead plated devices, add the suffix "TIN/LEAD" to the part number when 1. ordering (example: 2N2222A TIN/LEAD).
- 2. If requesting Lead (Pb) Free plated devices, add the suffix " PBFREE" to the part number when ordering (example: 2N2222A PBFREE).

## CONTACT US

# **Corporate Headquarters & Customer Support Team**

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