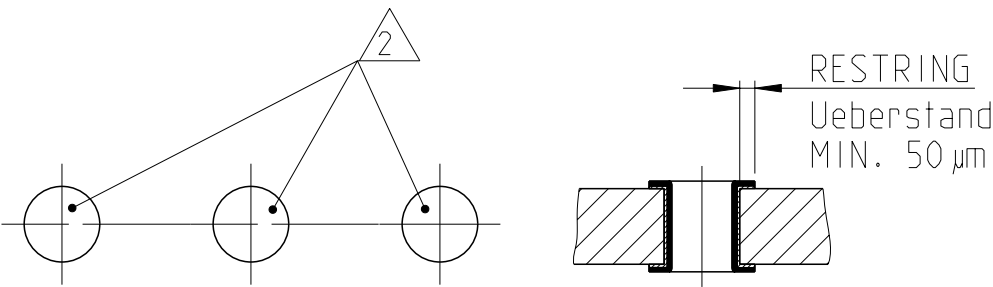


LOC	DIST	REVISIONS ÄNDERUNGEN					
A1	-	P	LTR	DESCRIPTION BESCHREIBUNG	DATE	DWN	APVD
			B1	APPLICATION- AND PRODUCT SPEC NO. ADDED	21NOV2001	PIN	
			C	NEW TOP OF TAP AND DIRECTION OF TOP OF REEL ADDED	06OCT2004	PIN	
			C1	NEW VARIANT 1-929451-4 ADDED	17JAN2019	FRAN	PFIS
			C2	SEE ECR-19-001009	21JAN2019	FRAN	PFIS
PROJEKT NR.: 98-52262							

PCB-LAYOUT
Leiterplatten-Layout



NOTES:
BEMERKUNGEN:

- C2

1

ZONE A: 1-4 µm TIN
ZONE B: 0.8-1.8 µm TIN

Zone A: 1-4 µm Zinn
Zone B: 0.8-1.8 µm Zinn

2

PLATING ACROSS OF HOLE SPEC.	
COPPER COATING	35-50 µm
TIN COATING	0.5-1.5 µm
HOLE DIA.	Ø1.5±0.025
PLATED DIA.	Ø1.44-0.15
HOLES PITCH	2.54±0.05 (2x)

PLATED THROUGH HOLE SPECIFICATION Duchplatierte Bohrungen Spezifikation	
Cu-THICKNESS Kupferschicht	25-50 µm
Sn Pb-THICKNESS Zinn/Blei Schicht	4-15 µm
DRILLED HOLE Ø1.5±0.025	FINISHED HOLE Ø1.44-0.15
Bohr Ø1.5±0.025	Platierter Loch-Ø1.44-0.15

- 3

DIMENSION OF LOOSE PIECE
Mass fuer Einzelteil
- 4

TO BE REELED ON PANCAKEREEL WITH INTERLEAVINGPAPER
Pappspulen mit Zwischenlagenpapier
- 5

TO BE REELED ON PLASTICREEL WITH INTERLEAVINGPAPER
Kunststoffspulen mit Zwischenlagenpapier

- C2

6

ZONE C: 1.5-5 µm SILVER
ZONE B: 0.8-1.8 µm Sn TIN

Zone C: 1.5-5 µm Silber
Zone B: 0.8-1.8 µm Sn Zinn

DIRECTION OFF
TOP OF REEL

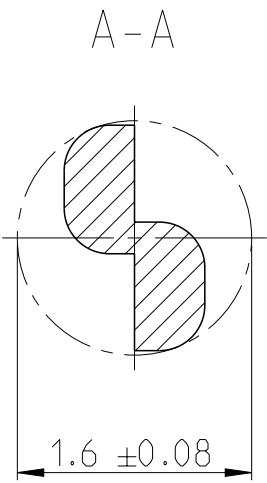
1- -2

1- -3

1- -4

4

5



C2

1-929451-4	A	CuSn4	6	4
1-929451-3	C	CuSn4	1	5
1-929451-2	C	CuSn4	1	4
PART NO.	REV.	MATERIAL	SURFACE	REALPACKING

THIS DRAWING IS A CONTROLLED DOCUMENT.		DWN S. Schaefer CHK J. Ryll	04MAY1999 04MAY1999	TE Connectivity		
DIMENSIONS: mm		TOLERANCES UNLESS OTHERWISE SPECIFIED: n. ISO 8015 n. ISO 2768 - MH -E		NAME TAB 4.8x0.8 WITH ACTION PIN Flachstecker 4.8x0.8 mit ACTION PIN		
		0-PLC ±0.2 1-PLC ±0.2 2-PLC ±0.1 3-PLC ±0.1 4-PLC ±0.1 ANGLES ±1° FINISH		APVD - PRODUCT SPEC 108-18707 APPLICATION SPEC 114-18380 WEIGHT - CUSTOMER DRAWING		
MATERIAL -		-		SIZE A2 CAGE CODE 00779 DRAWING NO C=929451 RESTRICTED TO		
-		-		SCALE 2:1 SHEET 1 OF 1 REV C2		