36V Stepping Motor Driver

BD63610AEFV

●General Description

BD63610AEFV is a bipolar low-consumption driver that driven by PWM current. Rated power supply voltage of the device is 36 V, and rated output current is 0.8A. CLK-IN driving mode is adopted for input interface, and excitation mode is corresponding to FULL STEP mode, HALF STEP mode. In addition, the power supply may be driven by one single system, which simplifies the design.

● Features

- Rated output current（DC）0.8A
- Low ON resistance DMOS output
- CLK-IN drive mode
- PWM constant current (other oscillation)
  Built-in spike noise cancel function (external noise filter is unnecessary)
- Full, half step functionality
- Freely timing excitation mode switch
- Normal rotation & reverse rotation switching function
- Power save function
- Built-in logic input pull-down resistor
- Power-on reset function
- Thermal shutdown circuit (TSD)
- Over-current protection circuit（OCP）
- Under voltage lock out circuit (UVLO)
- Over voltage lock out circuit (OVLO)
- Ghost Supply Prevention (protects against malfunction when power supply is disconnected)
- Microminiature, ultra-thin and high heat-radiation (exposed metal type) package

● Application

- PPC, multi-function printer, laser beam printer, and ink-jet printer
- Monitoring camera and WEB camera
- Sewing machine
- Photo printer, FAX, scanner and mini printer
- Toy and robot

● Major Characteristics

■ Range of power supply voltage	19～28 [V]
■ Rated output current	0.8 [A]
■ Rated output current (peak)	1.0 [A]
■ Range of operating temperature	-25～+85 [℃]
■ Output ON resistance (total of upper and lower resistors)	1.8 [Ω] (Typ.)

●Package

	W(Typ.) x D(Typ.)x H(Max.)
HTSSOP-B20	6.50mm x 6.40mm x 1.00mm



● Basic application circuit

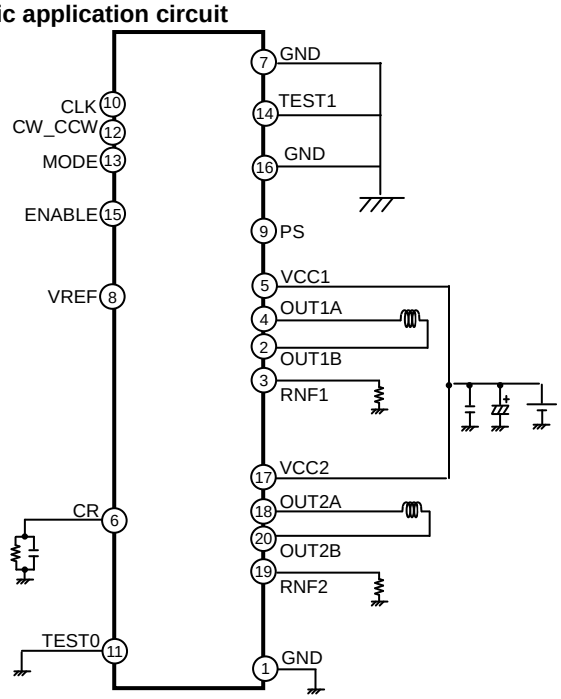


Figure 1. BD63610AEFV application circuit diagram

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● **Terminals Configuration Diagram**

[TOP VIEW]

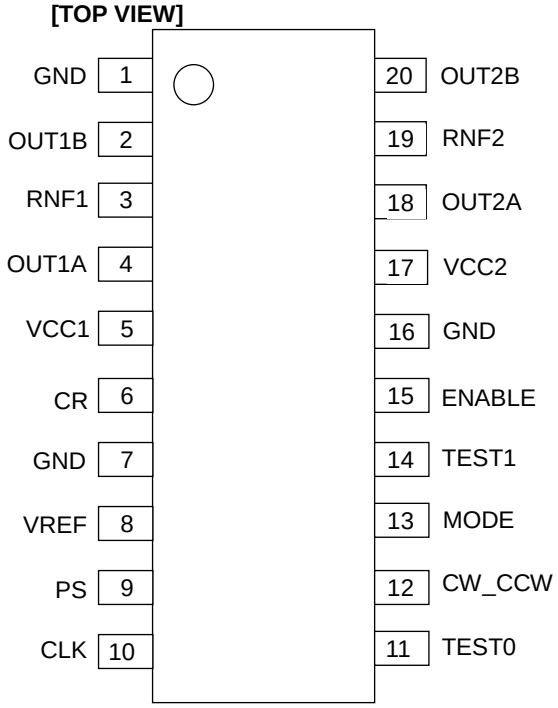


Figure 2. Terminals Configuration Diagram

● **Block Diagram**

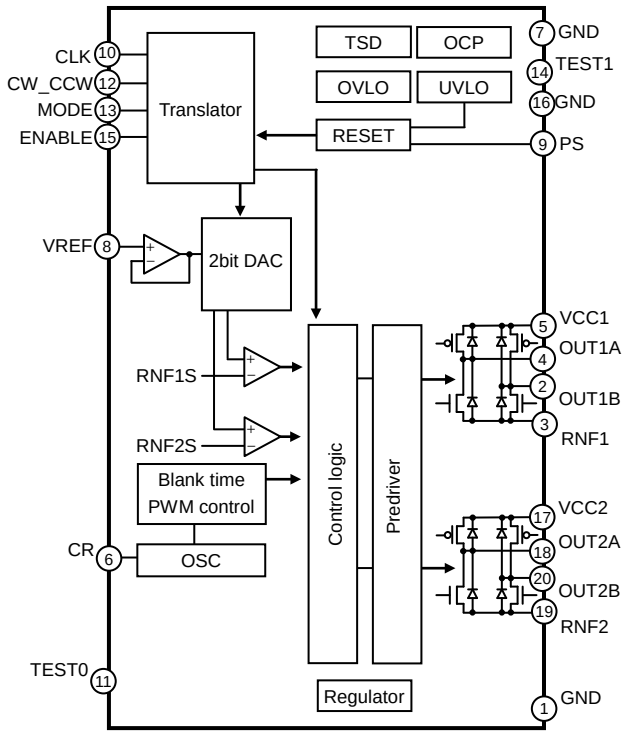


Figure 3. BD63610AEFV Block Diagram

● **Descriptions on Terminals**

NO.	Designation	Function	NO.	Designation	Function
1	GND	Ground terminal	11	TEST0	Terminal for testing (used by connecting with GND)
2	OUT1B	H bridge output terminal	12	CW_CCW	Motor rotating direction setting terminal
3	RNF1	Connection terminal of resistor for output current detection	13	MODE	Motor excitation mode setting terminal
4	OUT1A	H bridge output terminal	14	TEST1	Motor excitation mode setting terminal
5	VCC1	Power supply terminal	15	ENABLE	Terminal for enabling output
6	CR	Connection terminal of CR for setting	16	GND	Grounding terminal
7	GND	Ground terminal	17	VCC2	Non connection
8	VREF	Output current value setting terminal	18	OUT2A	Power supply terminal
9	PS	Power save terminal	19	RNF2	Non connection
10	CLK	Clock input terminal for advancing the	20	OUT2B	H bridge output terminal

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● **Absolute Maximum Rated Values (Ta=25℃)**

Item	Symbol	Rated Value	Unit
Supply voltage	$V_{CC1,2}$	-0.2～+36.0	V
Power dissipation	Pd	1.00※1	W
		3.20※2	W
Input voltage for control pin	V_{IN}	-0.2～+5.5	V
RNF maximum voltage	V_{RNF}	0.7	V
Maximum output current (DC)	I_{OUT}	0.8※3	A/Phase
Maximum output current (peak)	I_{OUT}	1.0※4	A/Phase
Operating temperature range	T_{opr}	-25～+85	℃
Storage temperature range	T_{stg}	-55～+150	℃

※1 70mm×70mm×1.6mm glass epoxy board. Derating in done at 8.8mW/℃ for operating above Ta=25℃.
※2 4-layer recommended board. Derating in done at 32.0mW/℃ for operating above Ta=25℃.
※3 Do not, however exceed Pd, ASO and Tjmax=150℃.
※4 pulth width tw≦1ms,duty20%
Caution: Operating the IC over the absolute maximum ratings may damage the IC. The damage can either be a short circuit between pins or an open circuit between pins and the internal circuitry. Therefore, it is important to consider circuit protection measures, such as adding a fuse, in case the IC is operated over the absolute maximum ratings.

●**Recommended operating range (Ta= -25～+85℃)**

Item	Symbol	Rated Value	Unit
Supply voltage	$V_{CC1,2}$	19～28	V
Maximum Output current (DC)	I_{OUT}	0.6※5	A/ Phase

*5 Not exceeding Pd，ASO or Tj=150℃

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●**Electrical Characteristics (Unless otherwise specified Ta=25℃, $V_{CC1,2}$=24V)**

Item	Symbol	Specification			Unit	Condition
		Minimum	Standard	Maximum		
[Whole]						
Circuit current at standby	I_{CCST}	-	0.4	1.5	mA	PS=L
Circuit current	I_{CC}	-	1.3	4.0	mA	PS=H, VREF=3V
[Control input] (CLK)						
H-level input voltage	V_{IN1H}	2.8	-	-	V	
L-level input voltage	V_{IN1L}	-	-	0.6	V	
Input hysteresis voltage	V_{IN1HYS}	-	0.85	-	V	
H-level input current	I_{IN1H}	35	50	100	μA	V_{IN1}=5V
L-level input current	I_{IN1L}	-10	0	-	μA	V_{IN1}=0V
[Control input] (CW_CCW, MODE, ENABLE, PS)						
H-level input voltage	V_{IN2H}	2.0	-	-	V	
L-level input voltage	V_{IN2L}	-	-	0.8	V	
H-level input current	I_{IN2H}	35	50	100	μA	V_{IN2}=5V
L-level input current	I_{IN2L}	-10	0	-	μA	V_{IN2}=0V
[Output (OUT1A, OUT1B, OUT2A, OUT2B)]						
Output ON resistance	R_{ON}	-	1.8	2.4	Ω	I_{OUT} =±0.5A (Sum of upper and lower)
Output leak current	I_{LEAK}	-	-	10	μA	
[Current control]						
RNFx input current	I_{RNF}	-80	-40	-	μA	RNFx=0V
VREF input current	I_{VREF}	-2.0	-0.1	-	μA	VREF=0V
VREF input voltage range	V_{VREF}	0	-	3.0	V	
MTH input current	I_{MTH}	-2.0	-0.1	-	μA	MTH=0V
MTH input voltage range	V_{MTH}	0	-	3.5	V	
Minimum ON time (Blank time)	t_{ONMIN}	0.3	0.9	1.5	μs	C=1000pF, R=39kΩ
Comparator threshold	V_{CTH}	0.57	0.60	0.63	V	VREF=3V

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●Function explanation

○CLK／Clock input terminal for advancing the electrical angle

CLK is reflected at rising edge. The Electrical angle advances by one for each CLK input.
Motor's misstep will occur if noise is picked up at the CLK terminal, so please design the pattern in such a way that there is no noise plunging.

○MODE／Motor Excitation Mode Setting Terminal

Set the motor excitation mode

MODE0	Excitation Mode
L	FULL STEP
H	HALF STEP

Please refer to the P.12, 13 for the timing chart & motor torque vector of various excitation modes.
Unrelated to CLK, change in setting is forcibly reflected (refer to P.15).

○CW_CCW Terminal／Motor rotating direction setting

Set the motor's rotating direction. Change in setting is reflected at the CLK rising edge immediately after the change in setting (refer to P.14)

CW_CCW	Rotating direction
L	Clockwise (CH2's current is outputted with a phase lag of 90° in regard to CH1's current)
H	Counter Clockwise(CH2's current is outputted with a phase lead of 90° in regard to CH1's current)

○ENABLE Terminal／Output enable terminal

Turn off forcibly all the output transistors (motor output is open).
When ENABLE=L, input to CLK is blocked, and phase advance operation of internal translator circuit is stopped.
However, during excitation modes (MODE0,MODE1) switch within the interval of ENABLE=L, as ENABLE=L→H is reset, the new mode upon switch will be applied for excitation (See P.15).

ENABLE	Motor Output
L	OPEN (electrical angle maintained)
H	ACTIVE

○PS／Power save terminal

PS can make circuit standby state and make motor output OPEN. In standby state, translator circuit is reset (initialized) and electrical angle is initialized.
Please be careful because there is a delay of 40μs(max.) before it is returned from standby state to normal state and the motor output becomes ACTIVE (refer to P.11).

PS	Status
L	Standby state(RESET)
H	ACTIVE

The electrical angle (initial electrical angle) of each excitation mode immediately after RESET is as follows (refer to P.12, 13).

Excitation Mode	Initial Electrical Angle
FULL STEP	45°
HALFSTEP	45°

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○VCC1,VCC2／Power supply terminal

Motor's drive current is flowing in it, so please wire in such a way that the wire is thick & short and has low impedance. Voltage VCC may have great fluctuation, so please arrange the bypass capacitor of about 100μ～470μF as close to the terminal as possible and adjust in such a way that the voltage VCC is stable. Please increase the capacity if needed especially when a large current is used or those motors that have great back electromotive force are used.
In addition, for the purpose of reducing of power supply's impedance in wide frequency bandwidth, parallel connection of multi-layered ceramic capacitor of 0.01μ～0.1μF etc is recommended. Extreme care must be used to make sure that the voltage VCC does not exceed the rating even for a moment.
VCC1 & VCC2 are shorted inside IC, so please be sure to short externally VCC1 & VCC2 when using. If used without shorting, malfunction or destruction may occur because of concentration of current routes etc., so please make sure that they are shorted when in use. Still more, in the power supply terminal, there is built-in clamp component for preventing of electrostatic destruction. If steep pulse or voltage of surge more that maximum absolute rating is applied, this clamp component operates, as a result there is the danger of destruction, so please be sure that the maximum absolute rating must not be exceeded. It is effective to mount a Zener diode of about the maximum absolute rating. Moreover, the diode for preventing of electrostatic destruction is inserted between VCC terminal and GND terminal, as a result there is the danger of IC destruction if reverse voltage is applied between VCC terminal and GND terminal, so please be careful.

○GND／Ground terminal

In order to reduce the noise caused by switching current and to stabilize the internal reference voltage of IC, please wire in such a way that the wiring impedance from this terminal is made as low as possible to achieve the lowest electrical potential no matter what operating state it may be. Moreover, please design patterns not to have any common impedance with other GND patterns.

○OUT1A,OUT1B,OUT2A,OUT2B／H Bridge output terminal

Motor's drive current is flowing in it, so please wire in such a way that the wire is thick & short and has low impedance. It is also effective to add a Schottky diode if output has positive or negative great fluctuation when large current is used etc, for example, if counter electromotive voltage etc. is great. Moreover, in the output terminal, there is built-in clamp component for preventing of electrostatic destruction. If steep pulse or voltage of surge more than maximum absolute rating is applied, this clamp component operates, as a result there is the danger of even destruction, so please be sure that the maximum absolute rating must not be exceeded.

○RNF1,RNF2／Connection terminal of resistor for detecting of output current

Please connect the resistor of 0.1Ω～0.3Ω for current detection between this terminal and GND In view of the power consumption of the current-detecting resistor, please determine the resistor in such a way that $W=I_{OUT2}\cdot R[W]$ does not exceed the power dissipation of the resistor. In addition, please wire in such a way that it has a low impedance and does not have a impedance in common with other GND patterns because motor's drive current flows in the pattern through RNF terminal～current-detecting resistor～GND. Please do not exceed the rating because there is the possibility of circuits' malfunction etc. if RNF voltage has exceeded the maximum rating (0.7V). Moreover, please be careful because if RNF terminal is shorted to GND, large current flows without normal PWM constant current control, then there is the danger that OCP or TSD will operate. If RNF terminal is open, then there is the possibility of such malfunction as output current does not flow either, so please do not let it open.

○VREF／Output current value setting terminal

This is the terminal to set the output current value. The output current value can be set by VREF voltage and current-detecting resistor (RNF resistor).

Output current I_{OUT} [A] = {VREF [V] / 5(division ratio inside IC)} / { RNF[Ω] + β }

β : RNF terminal resistor (reference value), min.25mΩ / typ.30mΩ / max.35mΩ

Please avoid using it with VREF terminal open because if VREF terminal is open, the input is unsettled, and the VREF voltage increases, and then there is the possibility of such malfunctions as the setting current increases and a large current flows etc. Please keep to the input voltage range because if the voltage of over 3V is applied on VREF terminal, then there is also the danger that a large current flows in the output and so OCP or TSD will operate. Besides, please take into consideration the outflow current (max.2μA) if inputted by resistance division when selecting the resistance value. The minimum current, which can be controlled by VREF voltage, is determined by motor coil's L & R values and minimum ON time because there is a minimum ON time in PWM drive.

○CR／Connection terminal of CR for setting chopping frequency

This is the terminal to set the chopping frequency of output. Please connect the external C(470p～1500pF) and R(10k～200kΩ) between this terminal and GND. Please refer to P9. Please interconnect from external components to GND in such a way that the interconnection does not have impedance in common with other GND patterns. In addition, please carry out the pattern design in such ways as keeps such steep pulses as square wave etc. away and that there is no noise plunging. Please mount the two components of C and R if being used by PWM constant current control because normal PWM constant current control becomes impossible if CR terminal is open or it is biased externally.

○TEST 0,TEST1terminal／Terminal for inspection

This terminal is used for delivery inspection on IC, and shall be grounded before use.
In addition, malfunctions may be caused by application without grounding.

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○Thermal Shutdown (TSD)

This IC has a built-in thermal shutdown circuit for thermal protection. When the IC's chip temperature rises above 175℃(Typ.), the motor output becomes OPEN. Also, when the temperature returns to under 150℃ (Typ.), it automatically returns to normal operation. However, even when TSD is in operation, if heat is continued to be added externally, heat overdrive can lead to destruction.

○Over Current Protection (OCP)

This IC has a built in over current protection circuit as a provision against destruction when the motor outputs are shorted each other or VCC-motor output or motor output-GND is shorted. This circuit latches the motor output to OPEN condition when the regulated threshold current flows for 4μs (Typ.). It returns with power reactivation or a reset of the PS terminal. The over current protection circuit's only aim is to prevent the destruction of the IC from irregular situations such as motor output shorts, and is not meant to be used as protection or security for the set. Therefore, sets should not be designed to take into account this circuit's functions. After OCP operating, if irregular situations continues and the return by power reactivation or a reset of the PS terminal is carried out repeatedly, then OCP operates repeatedly and the IC may generate heat or otherwise deteriorate. When the L value of the wiring is great due to the wiring being long, after the over current has flowed and the output terminal voltage jumps up and the absolute maximum values may be exceeded and as a result, there is a possibility of destruction. Also, when current which is over the output current rating and under the OCP detection current flows, the IC can heat up to over T_{jmax}=150℃and can deteriorate, so current which exceeds the output rating should not be applied.

○Under Voltage Lock Out (UVLO)

This IC has a built-in under voltage lock out function to prevent false operation such as IC output during power supply under voltage. When the applied voltage to the VCC terminal goes under 15V (Typ.), the motor output is set to OPEN. This switching voltage has a 1V (Typ.) hysteresis to prevent false operation by noise etc. Please be aware that this circuit does not operate during power save mode. Also, the electrical angle is reset when the UVLO circuit operates during CLK-IN drive mode.

○Over Voltage Lock Out (OVLO)

This IC has a built-in over voltage lock out function to protect the IC output and the motor during power supply over voltage. When the applied voltage to the VCC terminal goes over 32V (Typ.), the motor output is set to OPEN. This switching voltage has a 1V (Typ.) hysteresis and a 4μs (Typ.) mask time to prevent false operation by noise etc. Although this over voltage locked out circuit is built-in, there is a possibility of destruction if the absolute maximum value for power supply voltage is exceeded, therefore the absolute maximum value should not be exceeded. Please be aware that this circuit does not operate during power save mode.

○Ghost Supply Prevention (protects against malfunction when power supply is disconnected)

If a signal (logic input, MTH, VREF) is input when there is no power supplied to this IC, there is a function which prevents the false operation by voltage supplied via the electrostatic destruction prevention diode from these input terminals to the VCC to this IC or to another IC's power supply. Therefore, there is no malfunction of the circuit even when voltage is supplied to these input terminals while there is no power supply.

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○PWM Constant current control

1) Current control operation

When the output transistor is turned on, the output current increases, raising the voltage over the current sense resistor. When the voltage on the RNF pin reaches the voltage value set by the internal 2-bit DAC and the VREF input voltage, the current limit comparator engages and enters current decay mode. The output is then held off for a period of time determined by the RC time constant connected to the CR pin. The process repeats itself constantly for PWM operation.

2) Noise-masking function

In order to avoid misdetection of output current due to RNF spikes that may occur when the output turns ON, the IC employs an automatic current detection-masking period (t_{ONMIN}), during which current detection is disabled immediately after the output transistor is turned on. This allows for constant-current drive without the need for an external filter. This noise-masking period defines the minimum ON-time for the motor output transistor.

3) CR Timer

The CR filter connected to the CR pin is repeatedly charged and discharged between the VCRH and VCRL levels. The output of the internal comparator is masked while charging from VCRL to VCRH in order to cancel noise. (As mentioned above, this period defines the minimum ON-time of the motor output transistor.) The CR terminal begins discharging once the voltage reaches VCRH. When the output current reaches the current limit during this period (i.e. RNF voltage reaches the decay trigger voltage), then the IC enters decay mode. The CR continues to discharge during this period until it reaches VCRL, at which point the IC output is switched back ON. The current output and CR pin begin charging simultaneously. The CR charge time (t_{ONMIN}) and discharge time ($t_{discharge}$) are set by external components, according to the following formulas. The total of t_{ONMIN} and $t_{discharge}$ yield the chopping period, t_{chop}.

$t_{ONMIN}[s] \fallingdotseq C \cdot R' \cdot R / (R'+R) \cdot \ln[(VCR-0.4)/(VCR-1.0)]$

$VCR = V \cdot R/(R'+R)$

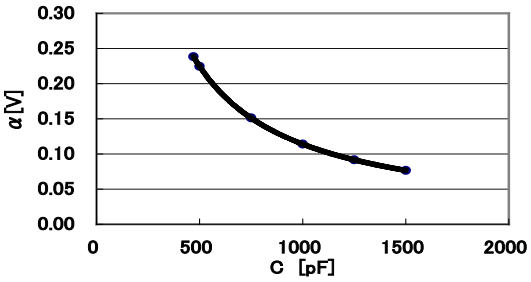
V: internal regulator voltage 5V(Typ.)

R': CR terminal internal impedance 5kΩ(Typ.)

$t_{discharge}[s] \fallingdotseq C \cdot R \cdot \ln[(1+\alpha)/0.4]$

α :See the right graph.

$t_{CHOP}[s] \fallingdotseq t_{ONMIN} + t_{discharge}$



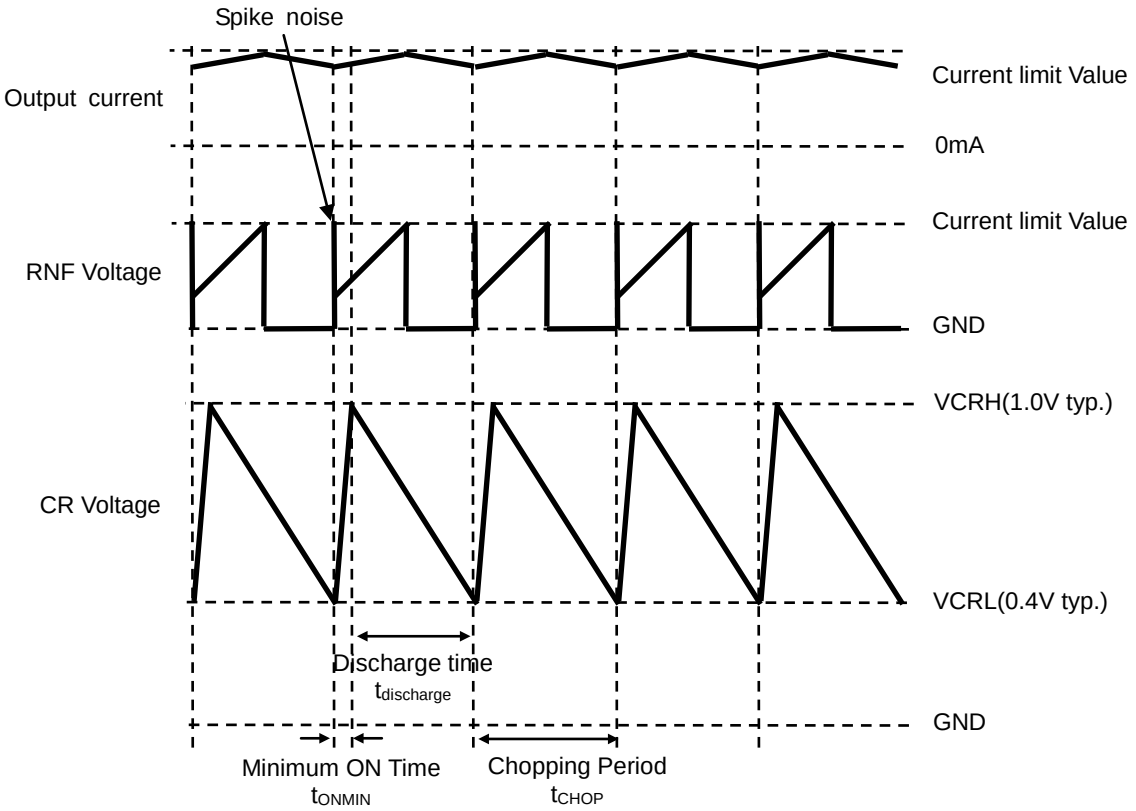


Figure 4 Timing chart of CR voltage, RNF voltage and output current

Attach a resistor of at least 10 kΩ to the CR terminal (10 kΩ~200 kΩ recommended) as lower values may keep the RC from reaching the VCRH voltage level. A capacitor in the range of 470 pF – 1500 pF is also recommended. As the capacitance value is increased, however, the noise-masking period (t_{onmin}) also increases, and there is a risk that the output current may exceed the current limit threshold due to the internal L and R components of the output motor coil. Also, ensure that the chopping period (t_{chop}) is not set longer than necessary, as doing so will increase the output ripple, thereby decreasing the average output current and yielding lower output rotation efficiency. The optimal value should reduce the motor drive noise while keeping distortion of the output current waveform to a minimum.

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○Current decay mode

The IC allows for a mixed decay mode in which the ratio of fast and slow decay can be optionally set.
The following diagrams show the operating state of each transistor and the regenerative current path during attenuation for each decay mode:

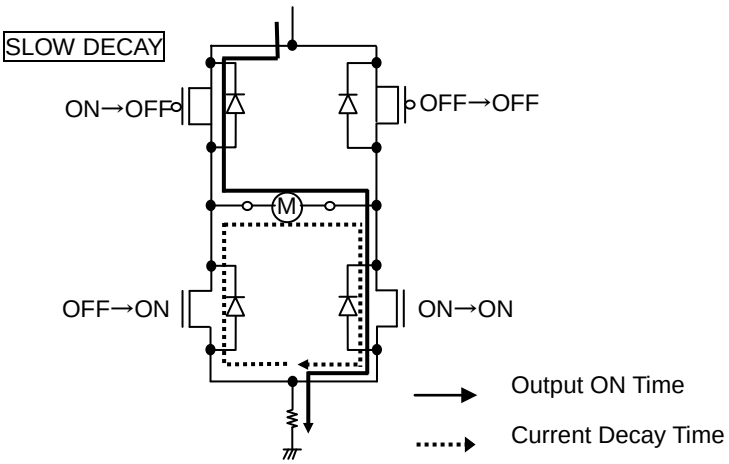
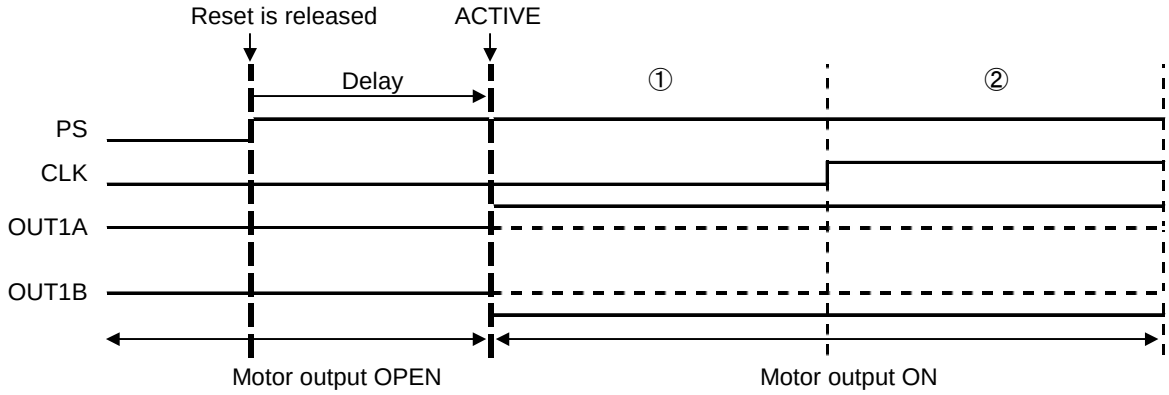


Figure 5. Route of Regenerated Current during Current Decay

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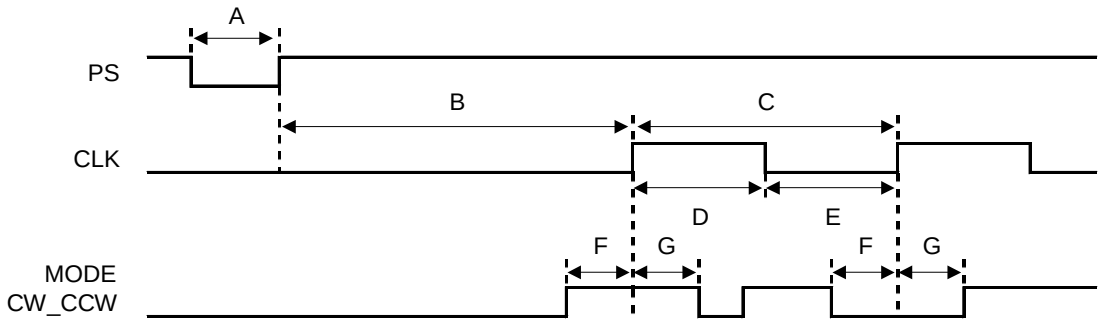
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●Translator circuit
This series builds in translator circuit and can drive stepping motor in CLK-IN mode.
The operation of the translator circuit in CLK-IN drive mode is described as below.
○Reset operation
The translator circuit is initialized by power ON Reset function and PS terminal.
・Initializing operation when power supply is turned on
①If power supply is turned on at PS=L (Please use this sequence as a general rule)
When power supply is turned on, the power ON reset function operates in IC and initialized, but as long as it is PS=L, the motor output is the OPEN state. After power supply is turned on, because of the changing of PS=L⇒H, the motor output becomes the ACTIVE state, and the excitation is started at the initial electrical angle.
But at the time of PS=L⇒H, it returns from the standby state to the normal state and there is a delay of 40μs(max.) until the motor output has become the ACTIVE state.



②If power supply is turned on at PS=H
When power supply is turned on, the power ON function in IC operates, and initialized before the motor output becomes the ACTIVE state, and the excitation is started at the initial electrical angle.
・Initializing operation during motor operating
Please input the reset signal to PS terminal when the translator circuit is initialized during motor operating. (Refer to P.14) But at the time of PS=L⇒H, it returns from the standby state to the normal state and there is a delay of 40μs (max.) until the motor output has become the ACTIVE state, so please be careful.

○Control input timing
Please input as shown below because the translator circuit operates at the rising edge of CLK signal. If you disobey this timing and input, then there is the possibility that the translator circuit does not operate as expected. In addition, at the time of PS=L⇒H, it returns from the standby state to the normal state and there is a delay of 40μs (max.) until the motor output has become the ACTIVE state, so within this delay interval there is no phase advance operation even if CLK is inputted.

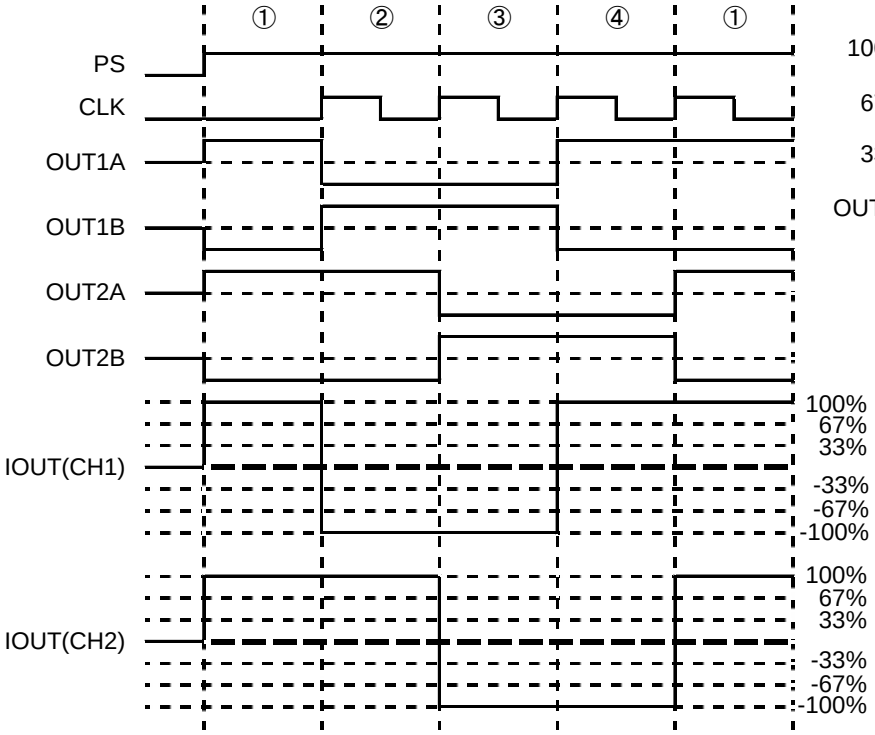


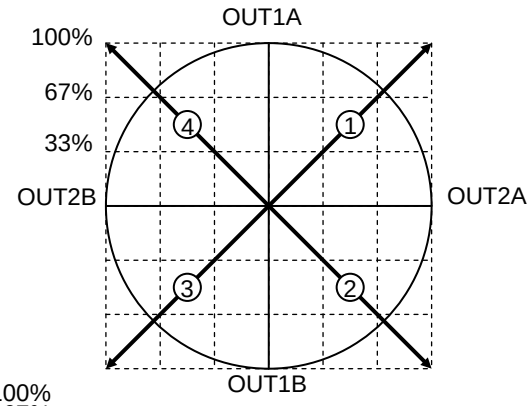
A:PS minimum input pulse width・・・・・・20μs
B:PS rising edge～CLK rising edge input possible maximum delay time・・・・・・40μs
C:CLK minimum period・・・・・・4μs
D:CLK minimum input H pulse width・・・・・・2μs
E:CLK minimum input L pulse width・・・・・・2μs
F:MODE,CW_CCW set-up time・・・・・・1μs
G:MODE,CW_CCW hold time・・・・・・1μs

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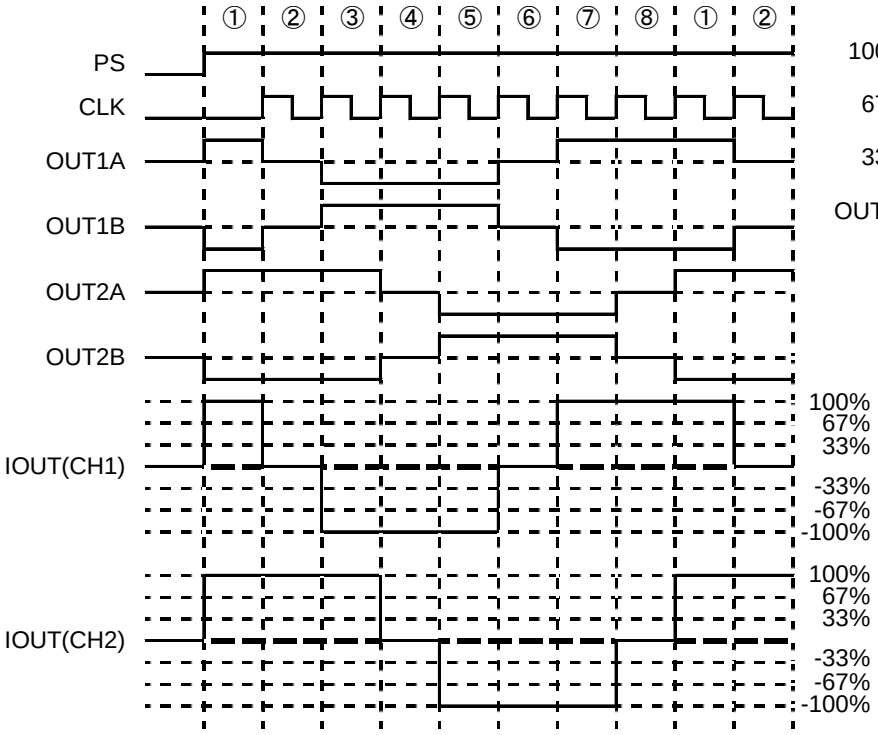
・FULL STEP (MODE=L, CW_CCW=L, ENABLE=H)

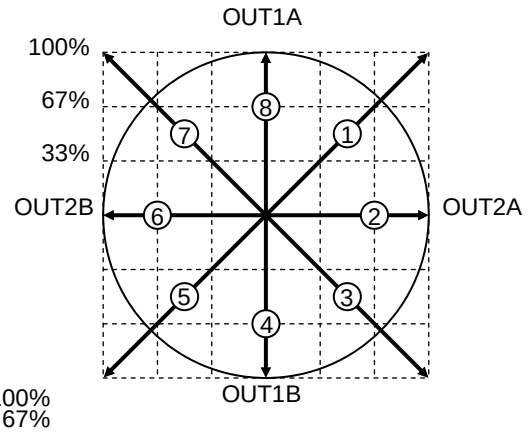




4CLK = Electrical angle 360˚

・HALF STEP (MODE=H, CW_CCW=L, ENABLE=H)





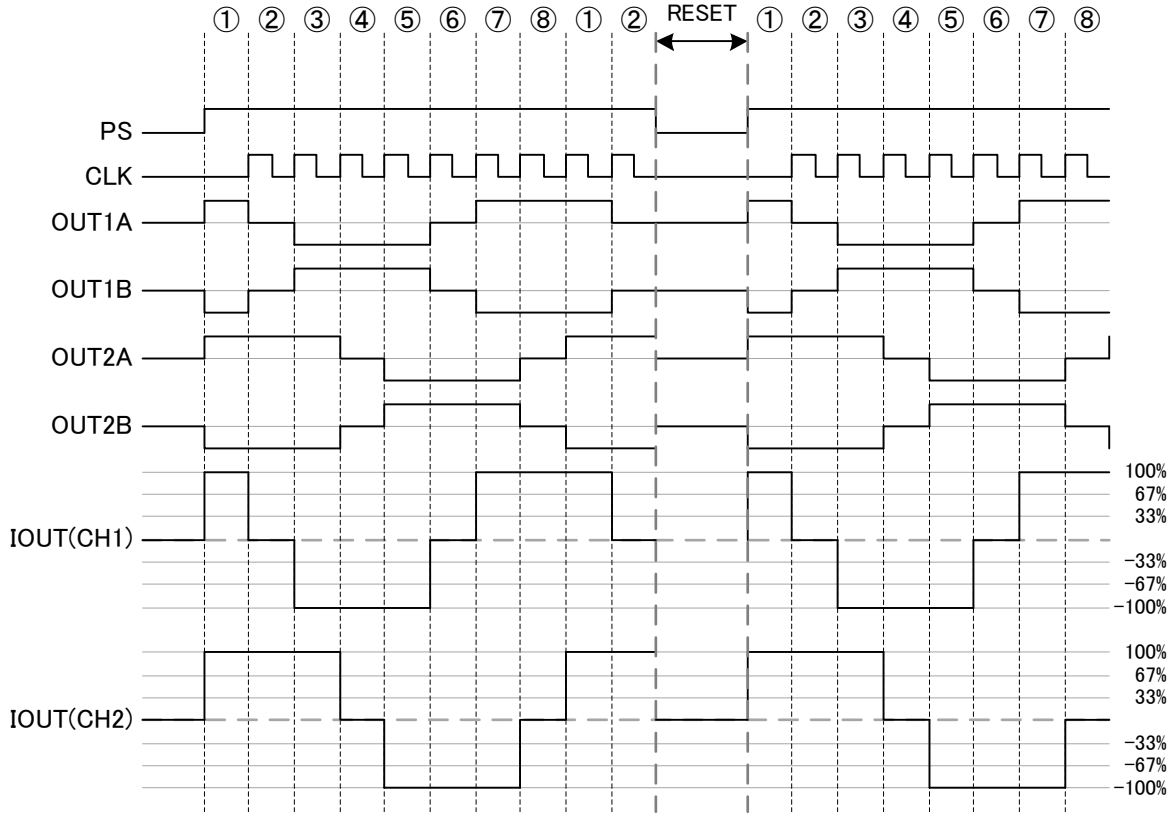
8CLK = Electrical angle 360˚

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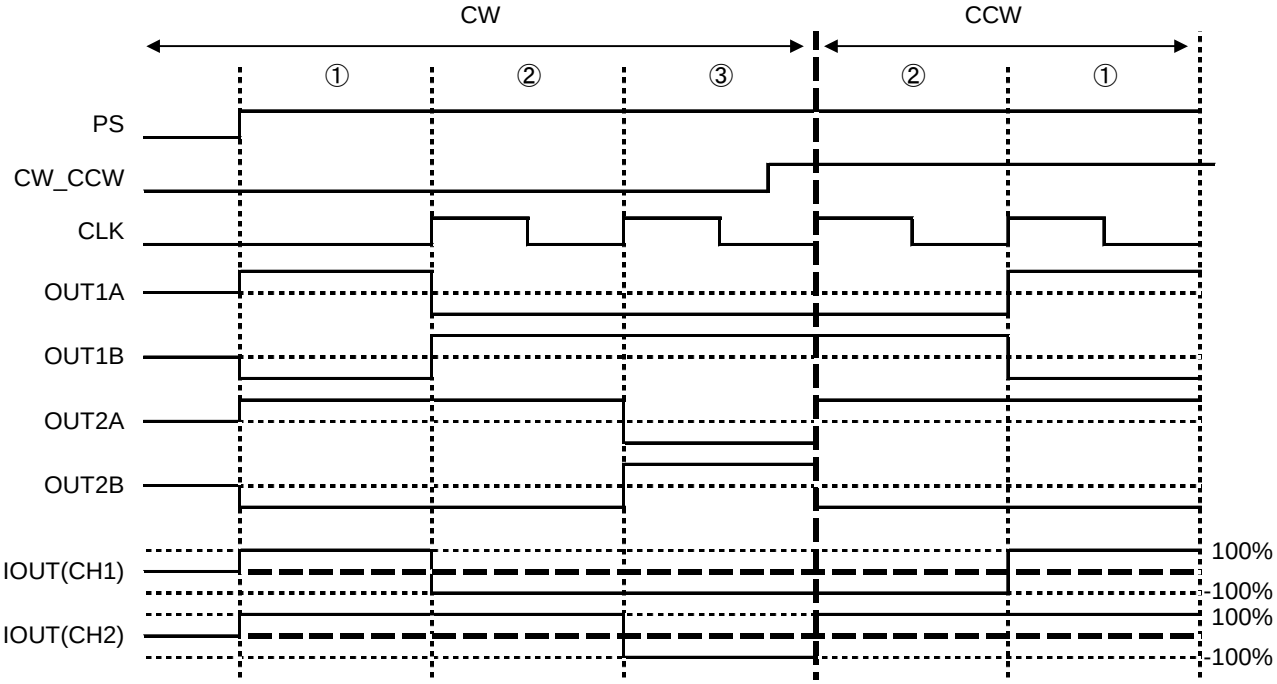
・Reset timing chart (HALF STEP, MODE=H, CW_CCW=L , ENABLE=H)

If the terminal PS is input to L, the reset operation is done with regardless of other input signals when reset the translator circuit while motor is working. At this time, IC internal circuit enters the standby mode, and makes the motor output OPEN.



・CW_CCW Switch timing chart (FULL STEP, MODE0=L, MODE1=L, ENABLE=H)

The switch of CW_CCW is reflected by the rising edge of CLK that comes immediately after the changes of the CW_CCW signal. However, depending on the state of operation of the motor at the switch the motor cannot follow even if the control on driver IC side is correspondent and there are possibilities of step-out and mistake step in motor, so please evaluate the sequence of the switch enough.

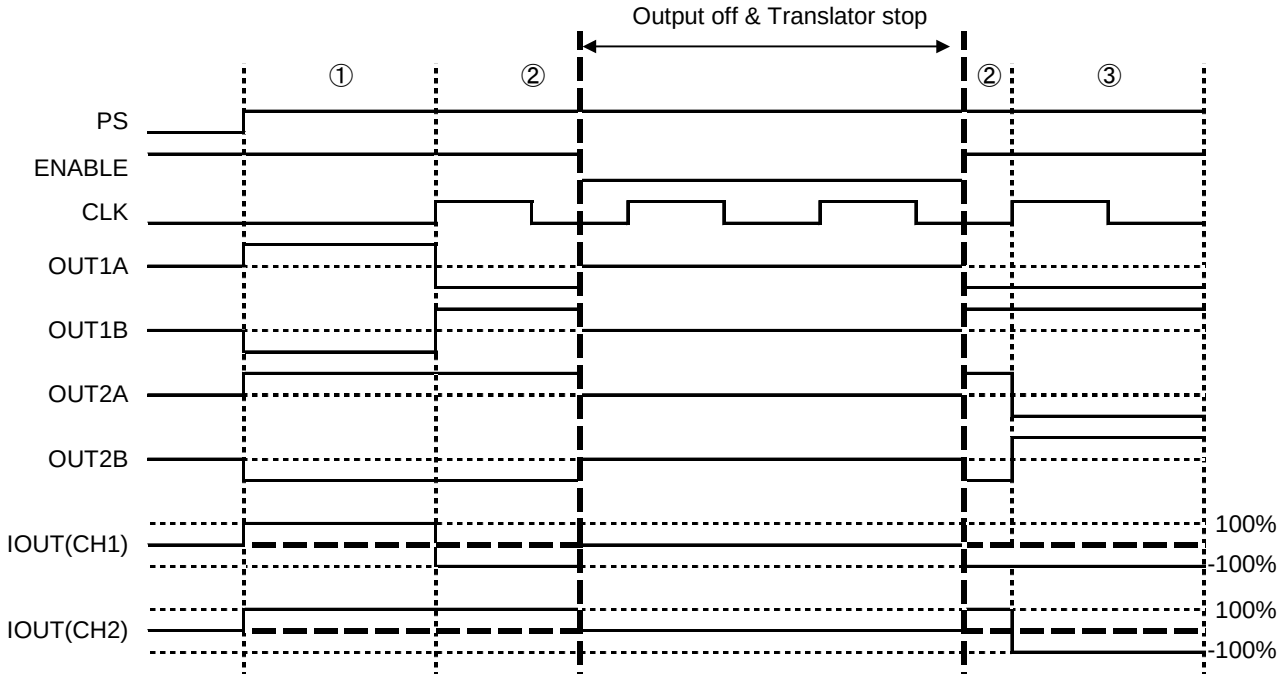


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・ ENABLE Switch timing chart (FULL STEP, MODE=L)

The switch of the ENABLE signal is reflected by the change in the ENABLE signal with regardless of other input signals. In the section of ENABLE=L, the motor output becomes OPEN and the electrical angle doesn't advance. Because the translator circuit stop and CLK input is canceled. Therefore, the progress of ENABLE=L→H is completed before the input of ENABLE=L. Excitation mode (MODE0, MODE1) also switches within ENABLE=L interval. Where excitation mode switched within ENABLE=L interval, restoring of ENABLE=L→H was done in the excitation mode after switch.



Restoring in the state prior to input of ENABLE=L

・ About the switch of the motor excitation mode

The switch of the excitation mode can be done with regardless of the CLK signal at the same time as changing of the signal MODE0 and MODE1. The following built-in function can prevent motor out-of-step caused by discrepancies of torque vector of transitional excitations during switch between excitation modes. However, due to operation state of motor during switch, motor may not act following control on IC side of controller, and thereby lead to out-of-step or miss step. Therefore, switch sequence shall be evaluated sufficiently before any decision.

・Cautions of bidirectional switch of CW_CCW and excitation modes (MODE)

As shown in the figure below, the area between the end of reset discharge (PS=L→H) and beginning of the first CLK signal input is defined as interval A, while the area post the end of the first CLK signal input is defined as interval B.

Interval A

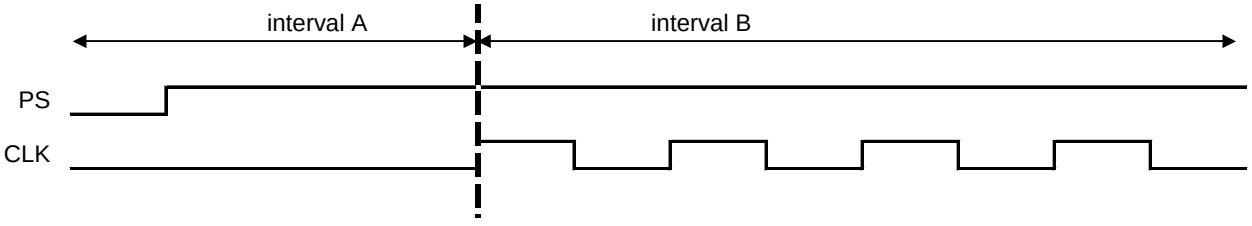
=> For CW_CCW, no limitation is applied on switch of excitation mode.

Interval B

=> In CLK1 period, or within ENABLE=L interval, CW_CCW and excitation mode can't be switched together.

Violation of this restriction may lead to false step (with one extra leading phase) or out-of-step.

Therefore, in case that CW_CCW and excitation modes are switched simultaneously, PS terminal must be input with reset signal. Then start to operate in interval A before carrying out such bidirectional switch.



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●Power dissipation

Please confirm that the IC's chip temperature Tj is not over 150℃, while considering the IC's power consumption (W), package power (Pd) and ambient temperature (Ta). When Tj=150℃ is exceeded the functions as a semiconductor do not operate and problems such as parasitism and leaks occur. Constant use under these circumstances leads to deterioration and eventually destruction of the IC. Tjmax=150℃ must be strictly obeyed under all circumstances.

○Thermal Calculation

The IC's consumed power can be estimated roughly with the power supply voltage (V_{CC}), circuit current (I_{CC}), output ON resistance (R_{ONH}、R_{ONL}) and motor output current value (I_{OUT}).

The calculation method during FULL STEP drive, SLOW DECAY mode is shown here:

Consumed power of the Vcc [W] = V_{CC} [V]・I_{CC} [A] ・・・・・・・①

Consumed power of the output DMOS [W] = $\underbrace{(R_{ONH}[\Omega] + R_{ONL}[\Omega])\cdot I_{OUT}[A]^2\cdot 2[ch]\cdot on_duty}_{\text{During output ON}}$

$+ \underbrace{(2\cdot R_{ONL}[\Omega])\cdot I_{OUT}[A]^2\cdot 2[ch]\cdot(1 - on_duty)}_{\text{During current decay}}$ ・・・・・・・②

However, on duty: PWM on duty = t_{on} / (t_{chop})

t_{on} varies depending on the L and R values of the motor coil and the current set value. Please confirm by actual measurement, or make an approximate calculation.

t_{chop} is the chopping period, which depends on the external CR. See P.9 for details.

IC number	Upper PchDMOS ON Resistance R_{ONH}[Ω] (Typ.)	Lower NchDMOS ON Resistance R_{ONL}[Ω] (Typ.)
BD63610AEFV	1.08	0.72

Consumed power of total IC W_total [W] = ① + ②

Junction temperature Tj = Ta[℃] + θ_{ja}[℃/W]・W_total [W]

However, the thermal resistance value θ_{ja} [℃/W] differs greatly depending on circuit board conditions. Refer to the derating curve on P.21.Also, we are taking measurements of thermal resistance value θ_{ja} of boards actually in use. Please feel free to contact our salesman. The calculated values above are only theoretical. For actual thermal design, please perform sufficient thermal evaluation for the application board used, and create the thermal design with enough margin to not exceed Tjmax=150℃.Although unnecessary with normal use, if the IC is to be used under especially strict heat conditions, please consider externally attaching a Schottky diode between the motor output terminal and GND to abate heat from the IC.

○Temperature Monitoring

In respect of BD63610AEFV, there is a way to directly measure the approximate chip temperature by using the TEST terminal with a protection diode for prevention from electrostatic discharge. However, temperature monitor using this TEST terminal is only for evaluation and experimenting, and must not be used in actual usage conditions.

(1) Measure the terminal voltage when a current of Idiode=50μA flows from the TEST terminal to the GND, without supplying VCC to the IC. This measurement is of the Vf voltage inside the diode.
(2) Measure the temperature characteristics of this terminal voltage. (Vf has a linear negative temperature factor against the temperature.) With the results of these temperature characteristics, chip temperature may be calibrated from the TEST terminal voltage.
(3) Supply VCC, confirm the TEST terminal voltage while running the motor, and the chip temperature can be approximated from the results of (2).

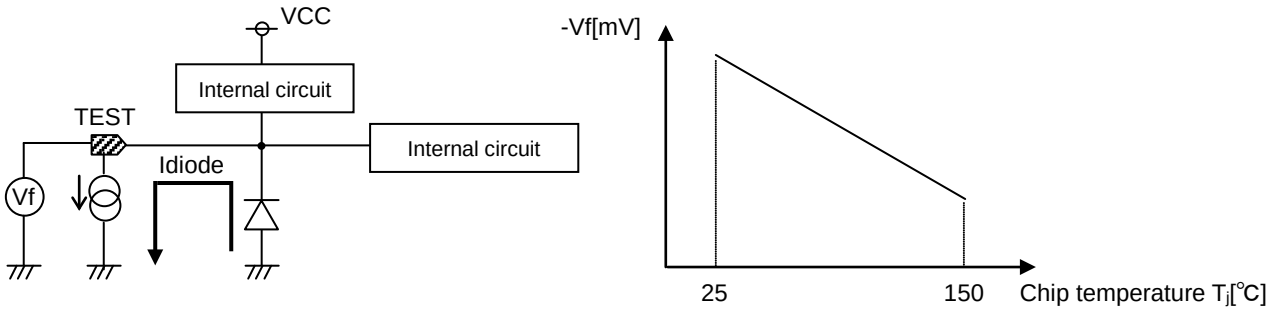


Figure 6. Model diagram for measuring chip temperature

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●**Example for applied circuit**

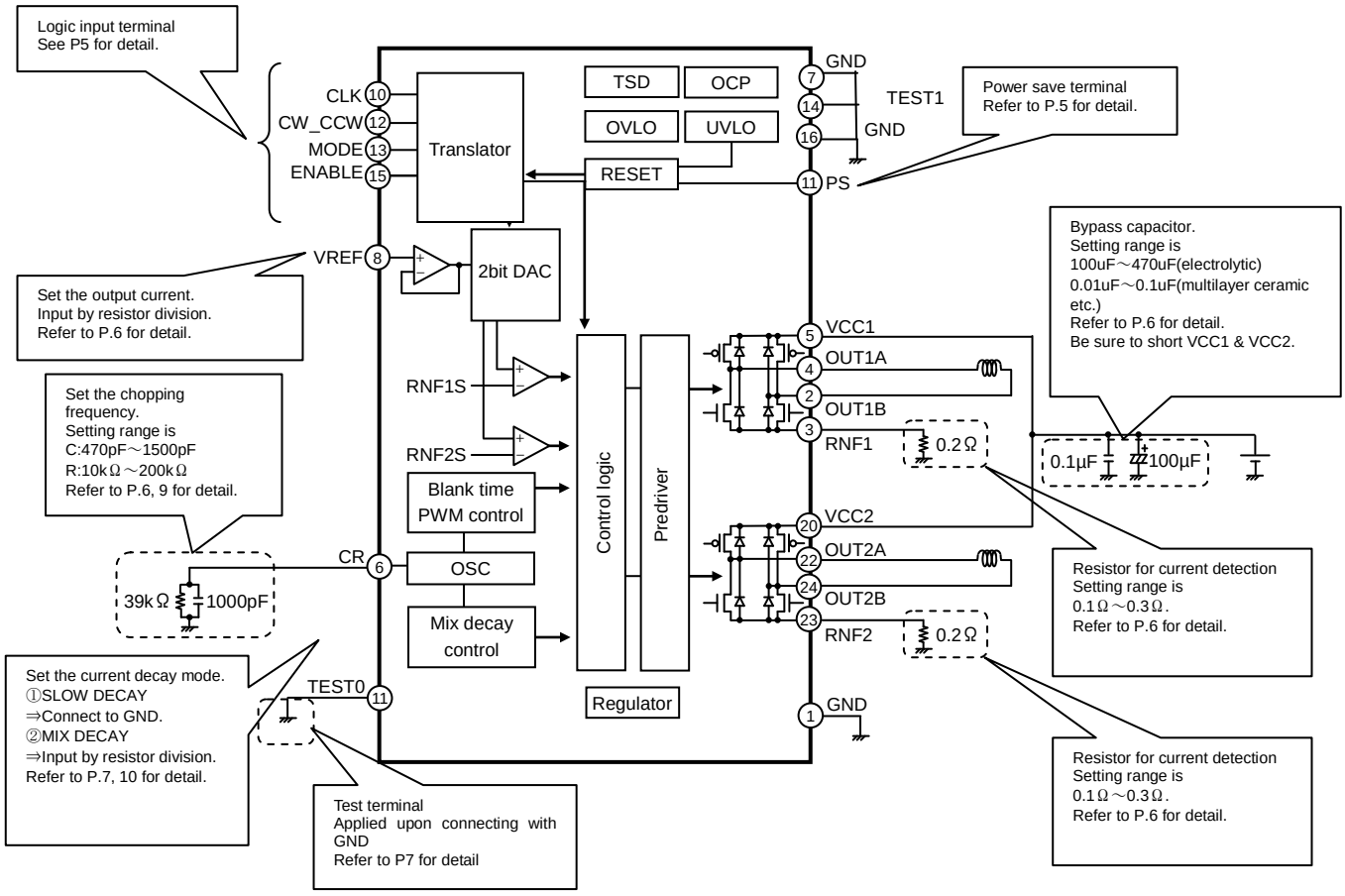


Figure7. BD63610AEFV block diagram and applied circuit diagram

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●**Input output equivalent circuit diagram**

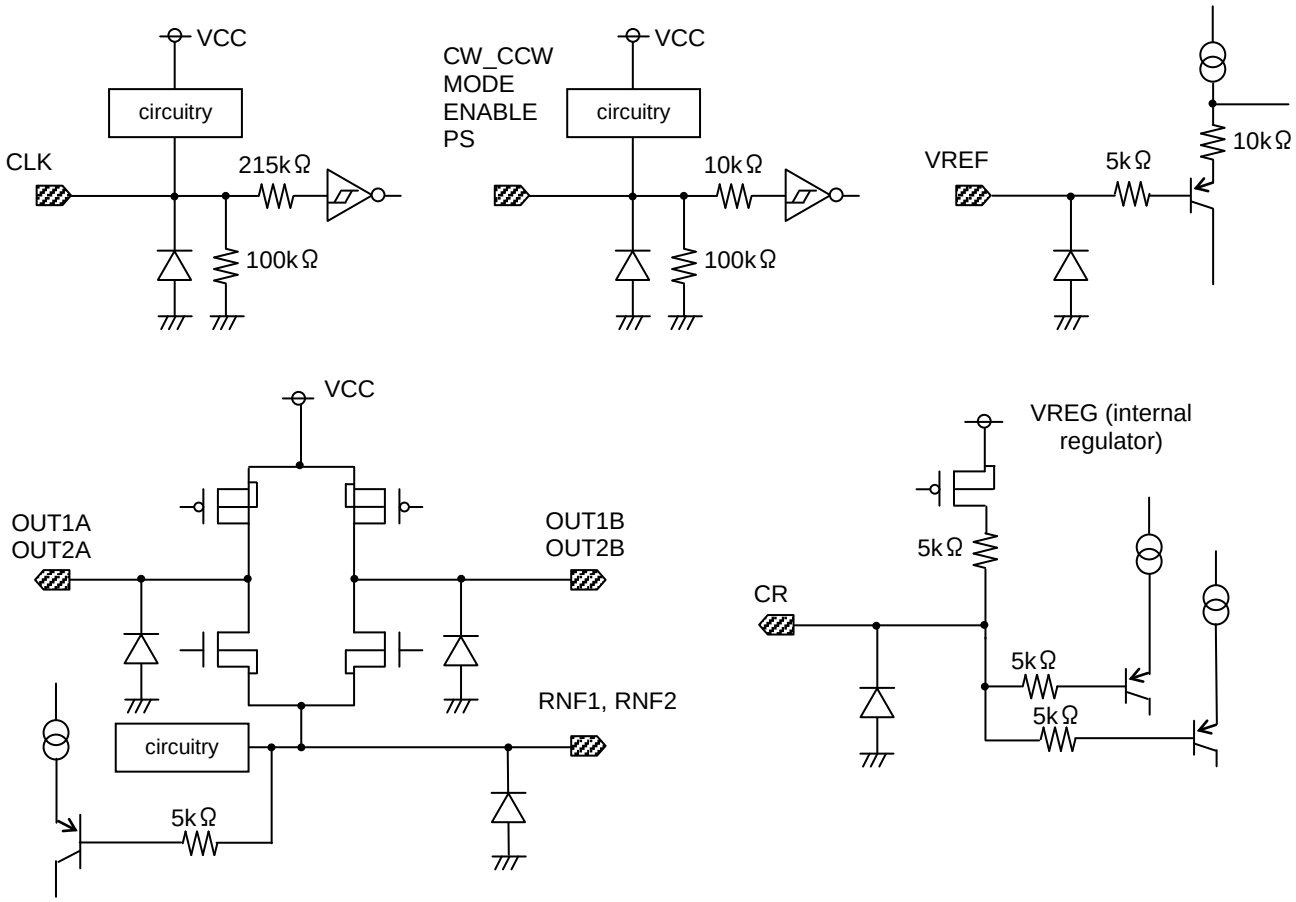


Figure 8. Input output equivalent circuit diagram

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Operational Notes

1. **Reverse Connection of Power Supply**
   Connecting the power supply in reverse polarity can damage the IC. Take precautions against reverse polarity when connecting the power supply, such as mounting an external diode between the power supply and the IC's power supply pins.

2. **Power Supply Lines**
   Design the PCB layout pattern to provide low impedance supply lines. Separate the ground and supply lines of the digital and analog blocks to prevent noise in the ground and supply lines of the digital block from affecting the analog block. Furthermore, connect a capacitor to ground at all power supply pins. Consider the effect of temperature and aging on the capacitance value when using electrolytic capacitors.

3. **Ground Voltage**
   Ensure that no pins are at a voltage below that of the ground pin at any time, even during transient condition.

4. **Ground Wiring Pattern**
   When using both small-signal and large-current ground traces, the two ground traces should be routed separately but connected to a single ground at the reference point of the application board to avoid fluctuations in the small-signal ground caused by large currents. Also ensure that the ground traces of external components do not cause variations on the ground voltage. The ground lines must be as short and thick as possible to reduce line impedance.

5. **Thermal Consideration**
   Should by any chance the power dissipation rating be exceeded the rise in temperature of the chip may result in deterioration of the properties of the chip. The absolute maximum rating of the Pd stated in this specification is when the IC is mounted on a 70mm x 70mm x 1.6mm glass epoxy board. In case of exceeding this absolute maximum rating, increase the board size and copper area to prevent exceeding the Pd rating.

6. **Recommended Operating Conditions**
   These conditions represent a range within which the expected characteristics of the IC can be approximately obtained. The electrical characteristics are guaranteed under the conditions of each parameter.

7. **Inrush Current**
   When power is first supplied to the IC, it is possible that the internal logic may be unstable and inrush current may flow instantaneously due to the internal powering sequence and delays, especially if the IC has more than one power supply. Therefore, give special consideration to power coupling capacitance, power wiring, width of ground wiring, and routing of connections.

8. **Operation Under Strong Electromagnetic Field**
   Operating the IC in the presence of a strong electromagnetic field may cause the IC to malfunction.

9. **Testing on Application Boards**
   When testing the IC on an application board, connecting a capacitor directly to a low-impedance output pin may subject the IC to stress. Always discharge capacitors completely after each process or step. The IC's power supply should always be turned off completely before connecting or removing it from the test setup during the inspection process. To prevent damage from static discharge, ground the IC during assembly and use similar precautions during transport and storage.

10. **Inter-pin Short and Mounting Errors**
    Ensure that the direction and position are correct when mounting the IC on the PCB. Incorrect mounting may result in damaging the IC. Avoid nearby pins being shorted to each other especially to ground, power supply and output pin. Inter-pin shorts could be due to many reasons such as metal particles, water droplets (in very humid environment) and unintentional solder bridge deposited in between pins during assembly to name a few.

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Operational Notes – continued

11. Unused Input Pins

Input pins of an IC are often connected to the gate of a MOS transistor. The gate has extremely high impedance and extremely low capacitance. If left unconnected, the electric field from the outside can easily charge it. The small charge acquired in this way is enough to produce a significant effect on the conduction through the transistor and cause unexpected operation of the IC. So unless otherwise specified, unused input pins should be connected to the power supply or ground line.

12. Regarding the Input Pin of the IC

This monolithic IC contains P+ isolation and P substrate layers between adjacent elements in order to keep them isolated. P-N junctions are formed at the intersection of the P layers with the N layers of other elements, creating a parasitic diode or transistor. For example (refer to figure below):

When GND > Pin A and GND > Pin B, the P-N junction operates as a parasitic diode.
When GND > Pin B, the P-N junction operates as a parasitic transistor.

Parasitic diodes inevitably occur in the structure of the IC. The operation of parasitic diodes can result in mutual interference among circuits, operational faults, or physical damage. Therefore, conditions that cause these diodes to operate, such as applying a voltage lower than the GND voltage to an input pin (and thus to the P substrate) should be avoided.

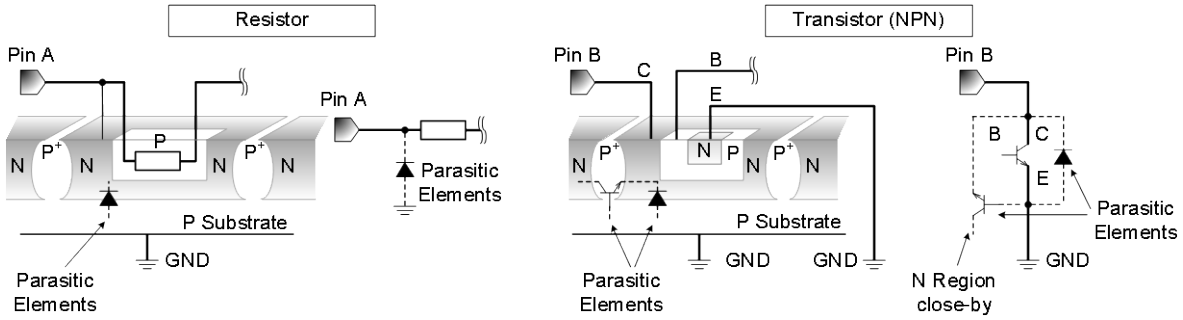


Figure 9. Example of monolithic IC structure

13. Area of Safe Operation (ASO)

Operate the IC such that the output voltage, output current, and power dissipation are all within the Area of Safe Operation (ASO).

14. Thermal Shutdown Circuit(TSD)

This IC has a built-in thermal shutdown circuit that prevents heat damage to the IC. Normal operation should always be within the IC's power dissipation rating. If however the rating is exceeded for a continued period, the junction temperature (Tj) will rise which will activate the TSD circuit that will turn OFF all output pins. When the Tj falls below the TSD threshold, the circuits are automatically restored to normal operation.
Note that the TSD circuit operates in a situation that exceeds the absolute maximum ratings and therefore, under no circumstances, should the TSD circuit be used in a set design or for any purpose other than protecting the IC from heat damage.

15. Over Current Protection Circuit (OCP)

This IC incorporates an integrated overcurrent protection circuit that is activated when the load is shorted. This protection circuit is effective in preventing damage due to sudden and unexpected incidents. However, the IC should not be used in applications characterized by continuous operation or transitioning of the protection circuit.

16. Operation Under Strong Electromagnetic Field (BD63610AEFV)

The IC is not designed for using in the presence of strong electromagnetic field. Be sure to confirm that no malfunction is found when using the IC in a strong electromagnetic field.

17. Metal on the backside (Define the side where product markings are printed as front) (BD63610AEFV)

The metal on the backside is shorted with the backside of IC chip therefore it should be connected to GND. Be aware that here is a possibility of malfunction or destruction if it is shorted with any potential other than GND.

18. TEST Terminal (BD63610AEFV)

Be sure to connect TEST pin to GND.

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●Thermal derating curve

This IC's package has exposed metal on the back, and it is possible to dissipate heat from a through hole in the back. Also, the back of board as well as the surfaces has large areas of copper foil heat dissipation patterns, greatly increasing power dissipation. The back metal is shorted with the back side of the IC chip, being a GND potential, therefore there is a possibility for malfunction if it is shorted with any potential other than GND, which should be avoided. Also, it is recommended that the back metal is soldered onto the GND to short. Please note that it has been assumed that this product will be used in the condition of this back metal performed heat dissipation treatment for increasing heat dissipation efficiency.

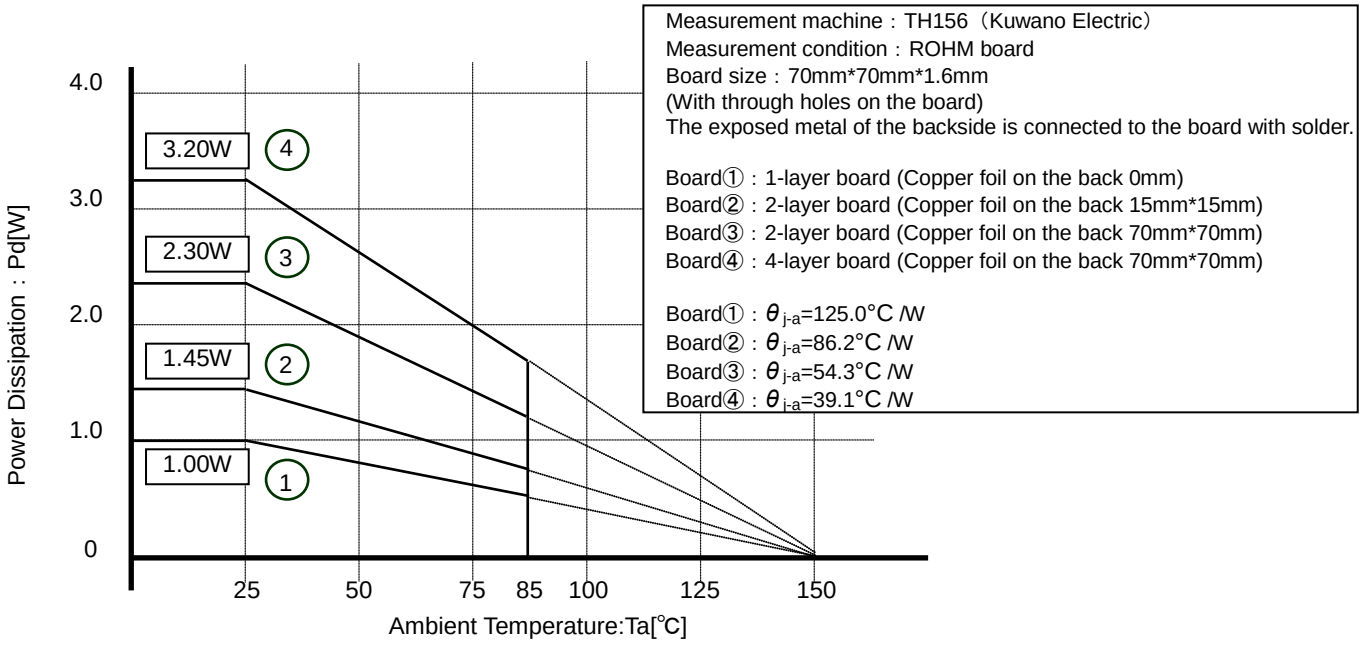
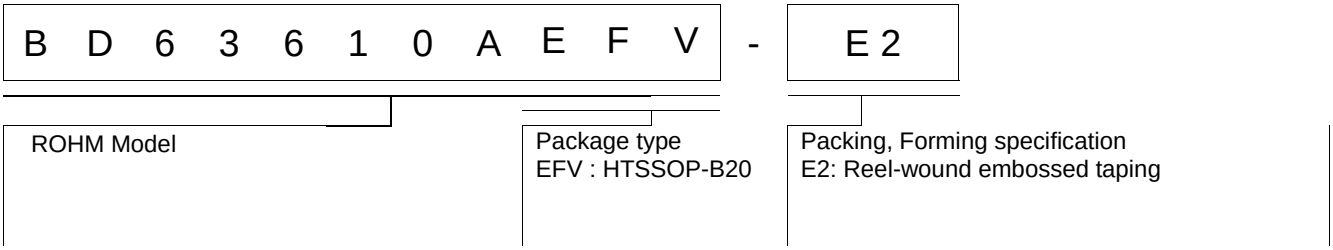


Figure 10. HTSSOP-B20 thermal derating curve

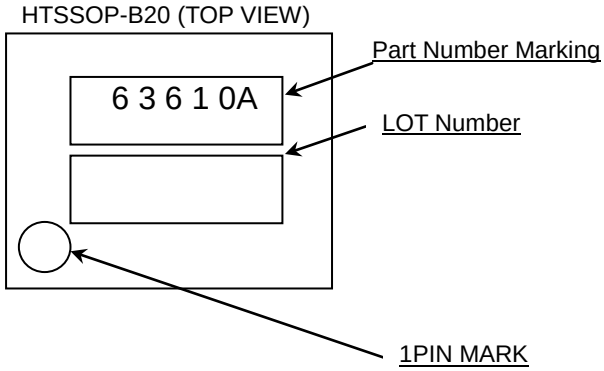
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●Selecting a model name when ordering



● **Marking Diagram**

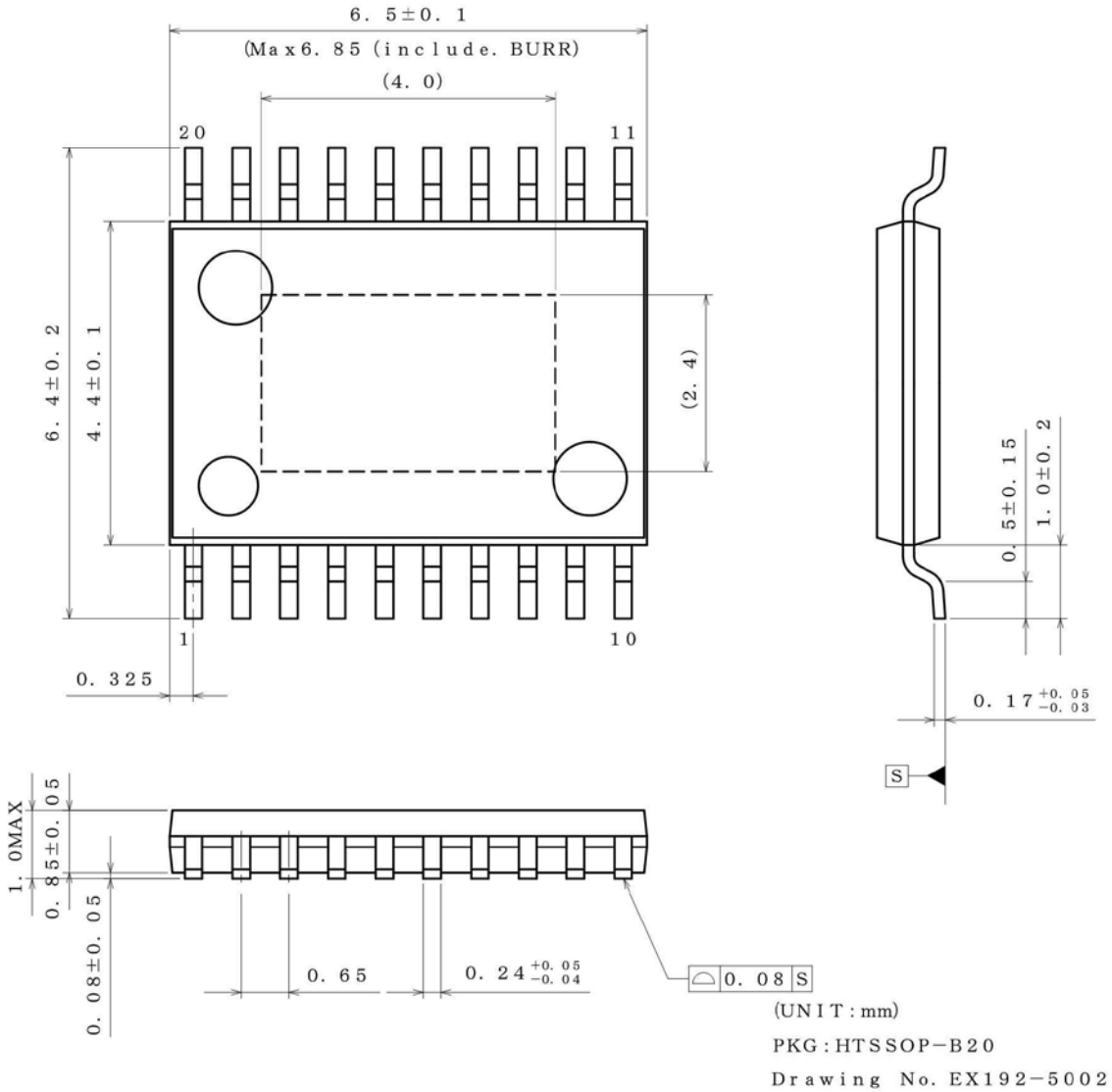


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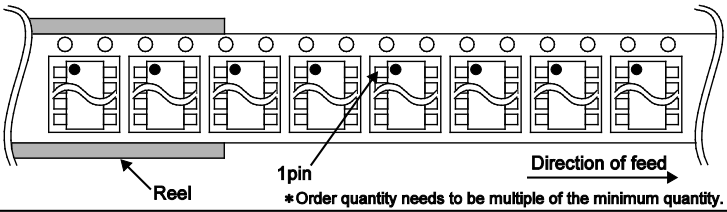
Physical Dimension, Tape and Reel Information

Package Name	HTSSOP-B20 (BD63610AEFV)



<Tape and Reel information>

Tape	Embossed carrier tape (with dry pack)
Quantity	2500pcs
Direction of feed	E2 (The direction is the 1pin of product is at the upper left when you hold reel on the left hand and you pull out the tape on the right hand)



*Order quantity needs to be multiple of the minimum quantity.

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(Note1) Medical Equipment Classification of the Specific Applications

JAPAN	USA	EU	CHINA
CLASSⅢ	CLASSⅢ	CLASSⅡb	CLASSⅢ
CLASSⅣ		CLASSⅢ	

2. ROHM designs and manufactures its Products subject to strict quality control system. However, semiconductor products can fail or malfunction at a certain rate. Please be sure to implement, at your own responsibilities, adequate safety measures including but not limited to fail-safe design against the physical injury, damage to any property, which a failure or malfunction of our Products may cause. The following are examples of safety measures:
   [a] Installation of protection circuits or other protective devices to improve system safety
   [b] Installation of redundant circuits to reduce the impact of single or multiple circuit failure

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   [a] Use of our Products in any types of liquid, including water, oils, chemicals, and organic solvents
   [b] Use of our Products outdoors or in places where the Products are exposed to direct sunlight or dust
   [c] Use of our Products in places where the Products are exposed to sea wind or corrosive gases, including Cl_2, H_2S, NH_3, SO_2, and NO_2
   [d] Use of our Products in places where the Products are exposed to static electricity or electromagnetic waves
   [e] Use of our Products in proximity to heat-producing components, plastic cords, or other flammable items
   [f] Sealing or coating our Products with resin or other coating materials
   [g] Use of our Products without cleaning residue of flux (even if you use no-clean type fluxes, cleaning residue of flux is recommended); or Washing our Products by using water or water-soluble cleaning agents for cleaning residue after soldering
   [h] Use of the Products in places subject to dew condensation

4. The Products are not subject to radiation-proof design.

5. Please verify and confirm characteristics of the final or mounted products in using the Products.

6. In particular, if a transient load (a large amount of load applied in a short period of time, such as pulse. is applied, confirmation of performance characteristics after on-board mounting is strongly recommended. Avoid applying power exceeding normal rated power; exceeding the power rating under steady-state loading condition may negatively affect product performance and reliability.

7. De-rate Power Dissipation depending on ambient temperature. When used in sealed area, confirm that it is the use in the range that does not exceed the maximum junction temperature.

8. Confirm that operation temperature is within the specified range described in the product specification.

9. ROHM shall not be in any way responsible or liable for failure induced under deviant condition from what is defined in this document.

Precaution for Mounting / Circuit board design

1. When a highly active halogenous (chlorine, bromine, etc.) flux is used, the residue of flux may negatively affect product performance and reliability.

2. In principle, the reflow soldering method must be used on a surface-mount products, the flow soldering method must be used on a through hole mount products. If the flow soldering method is preferred on a surface-mount products, please consult with the ROHM representative in advance.

For details, please refer to ROHM Mounting specification

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Precautions Regarding Application Examples and External Circuits

1. If change is made to the constant of an external circuit, please allow a sufficient margin considering variations of the characteristics of the Products and external components, including transient characteristics, as well as static characteristics.

2. You agree that application notes, reference designs, and associated data and information contained in this document are presented only as guidance for Products use. Therefore, in case you use such information, you are solely responsible for it and you must exercise your own independent verification and judgment in the use of such information contained in this document. ROHM shall not be in any way responsible or liable for any damages, expenses or losses incurred by you or third parties arising from the use of such information.

Precaution for Electrostatic

This Product is electrostatic sensitive product, which may be damaged due to electrostatic discharge. Please take proper caution in your manufacturing process and storage so that voltage exceeding the Products maximum rating will not be applied to Products. Please take special care under dry condition (e.g. Grounding of human body / equipment / solder iron, isolation from charged objects, setting of Ionizer, friction prevention and temperature / humidity control).

Precaution for Storage / Transportation

1. Product performance and soldered connections may deteriorate if the Products are stored in the places where:
   - [a] the Products are exposed to sea winds or corrosive gases, including Cl2, H2S, NH3, SO2, and NO2
   - [b] the temperature or humidity exceeds those recommended by ROHM
   - [c] the Products are exposed to direct sunshine or condensation
   - [d] the Products are exposed to high Electrostatic

2. Even under ROHM recommended storage condition, solderability of products out of recommended storage time period may be degraded. It is strongly recommended to confirm solderability before using Products of which storage time is exceeding the recommended storage time period.

3. Store / transport cartons in the correct direction, which is indicated on a carton with a symbol. Otherwise bent leads may occur due to excessive stress applied when dropping of a carton.

4. Use Products within the specified time after opening a humidity barrier bag. Baking is required before using Products of which storage time is exceeding the recommended storage time period.

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A two-dimensional barcode printed on ROHM Products label is for ROHM's internal use only.

Precaution for Disposition

When disposing Products please dispose them properly using an authorized industry waste company.

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Since concerned goods might be fallen under listed items of export control prescribed by Foreign exchange and Foreign trade act, please consult with ROHM in case of export.

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