

Date:- 07 Sep 2004

Data Sheet Issue:- 1

Rectifier Diode

Types W1263Y#160 to W1263Y#250

Old Type No.: SW02-25CXC565

Absolute Maximum Ratings

	VOLTAGE RATINGS	MAXIMUM LIMITS	UNITS
V_{RRM}	Repetitive peak reverse voltage, (note 1)	1600-2500	٧
V_{RSM}	Non-repetitive peak reverse voltage, (note 1)	1700-2600	V

	OTHER RATINGS	MAXIMUM LIMITS	UNITS
I _{F(AV)M}	Maximum average forward current, T _{sink} =55°C, (note 2)	1263	Α
$I_{F(AV)M}$	Maximum average forward current. T _{sink} =100°C, (note 2)	923	Α
I _{F(AV)M}	Maximum average forward current. T _{sink} =100°C, (note 3)	563	Α
I _{F(RMS)M}	Nominal RMS forward current, T _{sink} =25°C, (note 2)	2289	Α
I _{F(d.c.)}	D.C. forward current, T _{sink} =25°C, (note 4)	1972	Α
I _{FSM}	Peak non-repetitive surge t _p =10ms, V _{rm} =60%V _{RRM} , (note 5)	11.7	kA
I _{FSM2}	Peak non-repetitive surge t _p =10ms, V _{rm} ≤10V, (note 5)	12.9	kA
l ² t	I^2 t capacity for fusing t_p =10ms, V_{rm} =60% V_{RRM} , (note 5)	684×10 ³	A ² s
l ² t	l ² t capacity for fusing t _p =10ms, V _{rm} ≤10V, (note 5)	828×10 ³	A ² s
T _{j op}	Operating temperature range	-40 to +175	°C
T_{stg}	Storage temperature range	-40 to +200	°C

Notes:-

- 1) De-rating factor of 0.13% per °C is applicable for T_i below 25°C.
- 2) Double side cooled, single phase; 50Hz, 180° half-sinewave.
- 3) Single side cooled, single phase; 50Hz, 180° half-sinewave.
- 4) Double side cooled.
- 5) Half-sinewave, 175°C T_j initial.

Characteristics

	PARAMETER	MIN.	TYP.	MAX.	TEST CONDITIONS (Note 1)	UNITS
V_{FM}	Maximum peak forward voltage	-	-	2.12	I _{TM} =3770A	V
V_{T0}	Threshold voltage	-	-	0.87		V
r _T	Slope resistance	-	-	0.33		mΩ
I _{RRM}	Peak reverse current	-	-	30	Rated V _{RRM}	mA
I _{RRM}	Peak reverse current	-	-	30	Rated V _{RRM} , T _j =25°C	mA
Q _{rr}	Recovered charge	-	1300	-		μC
Q_{ra}	Recovered charge, 50% Chord	-	850	1150	FOOA + FOO110 di/d+ 10A/110 \/ FO\/	μC
Irr	Reverse recovery current	-	100	-	I_{TM} =500A, t_p =500 μ s, di/dt=10A/ μ s, V_r =50V	Α
t _{rr}	Reverse recovery time	-	17	-		μs
D	Thermal registeres junction to be stainly	-	-	0.05	Double side cooled	K/W
R_{thJK}	Thermal resistance, junction to heatsink	-	-	0.10	Single side cooled	K/W
F	Mounting force	5.5	-	8.3		kN
١٨/	Weight	-	90	-	Housing option YC	
W_t	Weight	-	140	-	Housing option YH	g

Notes:-

1) Unless otherwise indicated $T_j=175$ °C.

Notes on Ratings and Characteristics

1.0 Voltage Grade Table

Voltage Grade	$V_{DRM} \ V_{DSM} \ V_{RRM} \ V$	V _{RSM} V	V _D V _R DC V
16	1600	1700	1050
18	1800	1900	1150
20	2000	2100	1250
22	2200	2300	1350
24	2400	2500	1450
25	2500	2600	1500

2.0 Extension of Voltage Grades

This report is applicable to other voltage grades when supply has been agreed by Sales/Production.

3.0 De-rating Factor

A blocking voltage de-rating factor of 0.13%/°C is applicable to this device for T_i below 25°C.

4.0 Snubber Components

When selecting snubber components, care must be taken not to use excessively large values of snubber capacitor or excessively small values of snubber resistor. Such excessive component values may lead to device damage due to the large resultant values of snubber discharge current. If required, please consult the factory for assistance.

5.0 Computer Modelling Parameters

5.1 Device Dissipation Calculations

$$I_{AV} = \frac{-V_{T0} + \sqrt{{V_{T0}}^2 + 4 \cdot ff^2 \cdot r_T \cdot W_{AV}}}{2 \cdot ff^2 \cdot r_T} \qquad W_{AV} = \frac{\Delta T}{R_{th}}$$
 and:
$$\Delta T = T_{j \max} - T_K$$

Where $V_{T0}=0.87V$, $r_{T}=0.33m\Omega$,

 $R_{\it th}$ = Supplementary thermal impedance, see table below and

ff = Form factor, see table below.

Supplementary Thermal Impedance					
Conduction Angle	6 Phase (60°)	3 Phase (120°)	½ Wave (180°)	d.c.	
Square wave Double Side Cooled	0.069	0.061	0.057	0.05	
Square wave Single Side Cooled	0.119	0.111	0.107	0.1	
Sine wave Double Side Cooled	0.052	0.0513	0.0505		
Sine wave Single Side Cooled	0.102	0.1013	0.1005		

Form Factors					
Conduction Angle	6 Phase (60°)	3 Phase (120°)	½ Wave (180°)	d.c.	
Square wave	2.449	1.732	1.414	1	
Sine wave	2.778	1.879	1.57		

5.2 Calculating V_F using ABCD Coefficients

The on-state characteristic I_F vs. V_F, on page 6 is represented in two ways;

- (i) the well established V_{T0} and r_T tangent used for rating purposes and
- (ii) a set of constants A, B, C, D, forming the coefficients of the representative equation for V_F in terms of I_F given below:

$$V_F = A + B \cdot \ln(I_F) + C \cdot I_F + D \cdot \sqrt{I_F}$$

The constants, derived by curve fitting software, are given below for both hot and cold characteristics. The resulting values for V_F agree with the true device characteristic over a current range, which is limited to that plotted.

	25°C Coefficients	175°C Coefficients
Α	0.988190696	0.322649754
В	-0.02390475	0.08667911
С	1.37558×10 ⁻⁴	2.61136×10 ⁻⁴
D	0.01055999	1.61365×10 ⁻³

5.3 D.C. Thermal Impedance Calculation

$$r_{t} = \sum_{p=1}^{p=n} r_{p} \cdot \left(1 - e^{\frac{-t}{\tau_{p}}}\right)$$

Where p = 1 to n, n is the number of terms in the series and:

t = Duration of heating pulse in seconds.

r₊ = Thermal resistance at time t.

 r_p = Amplitude of p_{th} term.

 τ_p = Time Constant of r_{th} term.

The coefficients for this device are shown in the tables below:

D.C. Single Side Cooled					
Term	1	2	3	4	5
r_p	0.06158	8.43118×10 ⁻³	0.01031	0.01614	5.0391×10 ⁻³
$ au_{\!p}$	2.13613	1.2129	0.15024	0.04244	3.6777×10 ⁻³

D.C. Double Side Cooled							
Term	1	2	3	4			
r_p	0.02	9.9234×10 ⁻³	0.01434	4.284×10 ⁻³			
$ au_{\!\scriptscriptstyle D}$	0.33917	0.12691	0.03562	2.563×10 ⁻³			

6.0 Reverse recovery ratings

(i) Q_{ra} is based on 50% I_{rm} chord as shown in Fig. 1

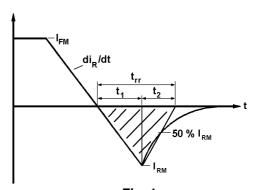


Fig. 1

(ii) Q_{rr} is based on a 150µs integration time i.e.

$$Q_{rr} = \int_{0}^{150 \,\mu s} i_{rr}.dt$$

(iii)
$$K Factor = \frac{t_1}{t_2}$$

Curves

Figure 1 - Forward characteristics of Limit device

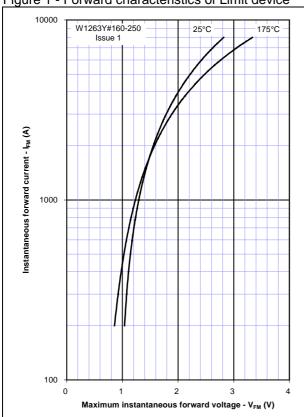


Figure 2 - Transient thermal impedance

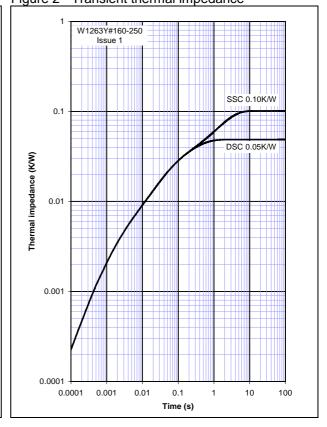


Figure 3 - Maximum surge Rating

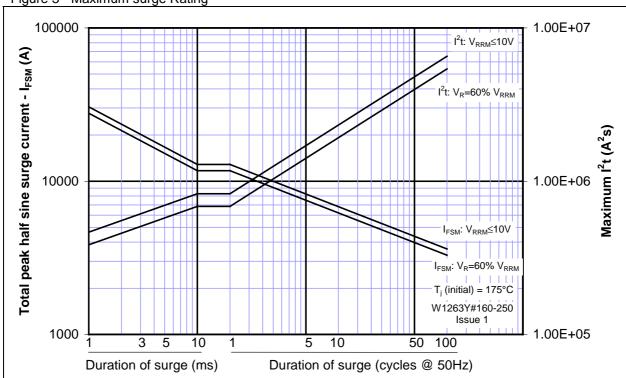


Figure 4 - Total recovered charge, Q_{rr}

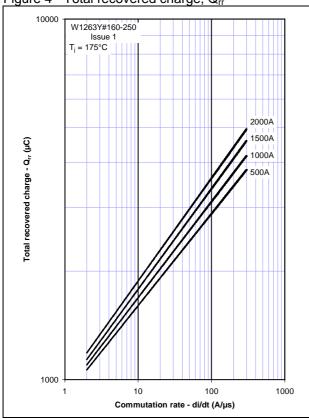


Figure 5 - Recovered charge, Q_{ra} (50% chord)

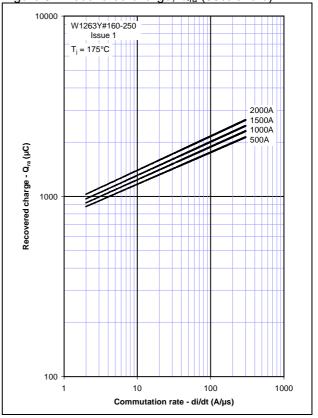


Figure 6 - Peak reverse recovery current, I_{rm}

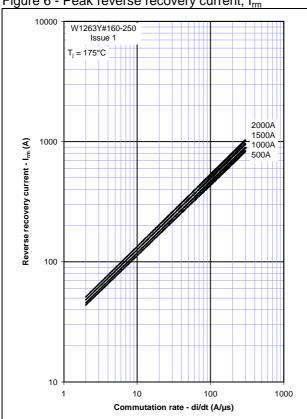


Figure 7 - Maximum recovery time, t_{rr} (50% chord)

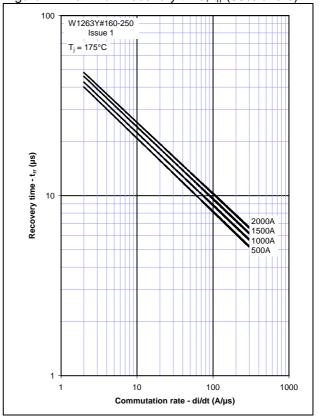


Figure 8 – Forward current vs. Power dissipation – Double Side Cooled

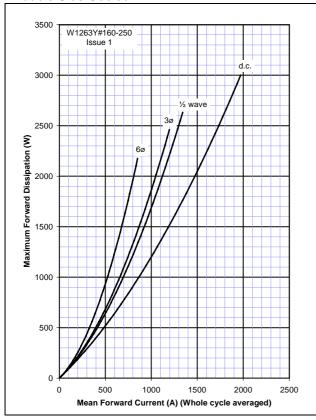


Figure 10 – Forward current vs. Power dissipation – Single Side Cooled

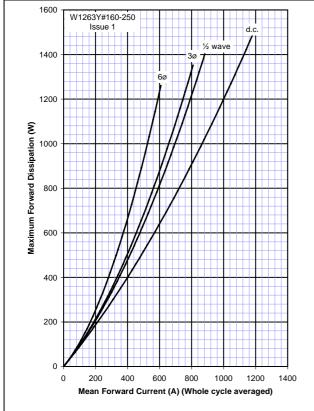


Figure 9 – Forward current vs. Heatsink temperature - Double Side Cooled

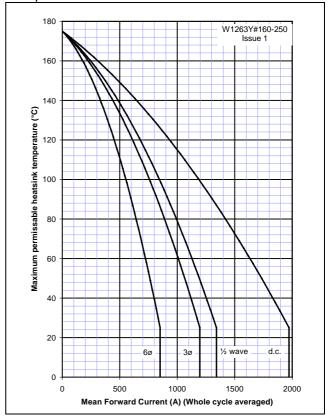
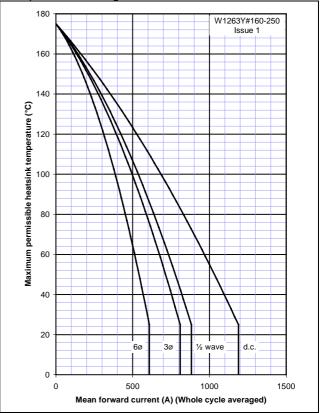
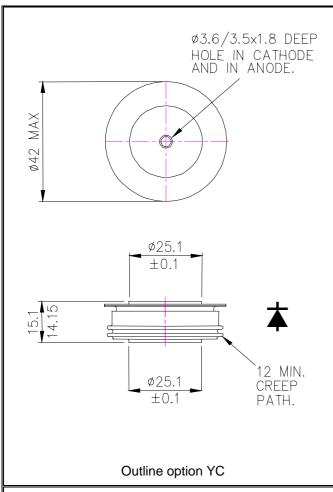
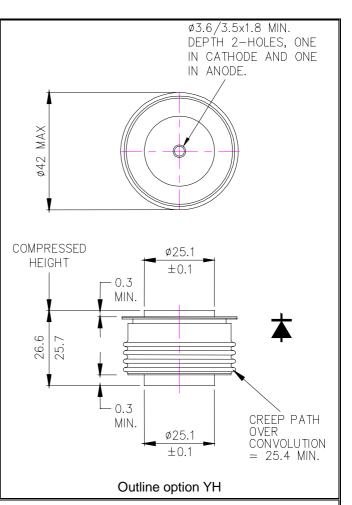


Figure 11 – Forward current vs. Heatsink temperature – Single Side Cooled



Outline Drawing & Ordering Information





ORDERING INFORMATION

(Please quote 10 digit code as below)

W1263	Y#	**	0
Fixed Type Code	Fixed outline code YC = 15.1mm Clamp Height YH = 26.6mm Clamp Height	Voltage code V _{DRM} /100 16-25	Fixed code

Order code: W1263YH250 - 2500V V_{RRM}, 26.6mm clamp height capsule.

IXYS Semiconductor GmbH

Edisonstraße 15 D-68623 Lampertheim Tel: +49 6206 503-0 Fax: +49 6206 503-627

E-mail: marcom@ixys.de

IXYS Corporation

3540 Bassett Street Santa Clara CA 95054 USA Tel: +1 (408) 982 0700 Fax: +1 (408) 496 0670

E-mail: sales@ixys.net

WESTCODE An IXYS Company

www.westcode.com

www.ixys.com

Westcode Semiconductors Ltd

Langley Park Way, Langley Park, Chippenham, Wiltshire SN15 1GE. Tel: +44 (0)1249 444524

Fax: +44 (0)1249 444524

E-mail: WSL.sales@westcode.com

Westcode Semiconductors Inc

3270 Cherry Avenue Long Beach CA 90807 USA Tel: +1 (562) 595 6971 Fax: +1 (562) 595 8182

E-mail: WSI.sales@westcode.com

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