

# R6535KNX3

## Nch 650V 35A Power MOSFET

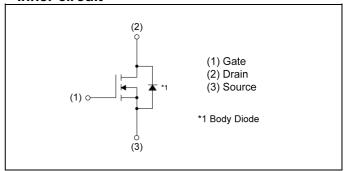
$V_{DSS}$	650V
R <sub>DS(on)</sub> (Max.)	0.115Ω
I <sub>D</sub>	±35A
$P_{D}$	370W

# Outline TO-220AB

## Features

- 1) Low on-resistance
- 2) Ultra fast switching
- 3) Parallel use is easy
- 4) Pb-free lead plating ; RoHS compliant

## •Inner circuit



# Application

Switching

Packaging specifications

Packing	Tube
Packing code	C16
Marking	R6535KNX3
Quantity (pcs)	1000

# ullet Absolute maximum ratings (T<sub>a</sub> = 25°C ,unless otherwise specified)

Parameter	Symbol	Value	Unit	
Drain - Source voltage		V <sub>DSS</sub>	650	V
Continuous drain current (T <sub>c</sub> = 25°C)		I <sub>D</sub> *1	±35	Α
Pulsed drain current		I <sub>DP</sub> *2	±105	Α
Coto Course valters	static	.,	±20	V
Gate - Source voltage	AC(f>1Hz)	V <sub>GSS</sub>	±30	V
Avalanche current, single pulse		I <sub>AS</sub>	6.6	Α
Avalanche energy, single pulse		E <sub>AS</sub> *3	867	mJ
Power dissipation (T <sub>c</sub> = 25°C)	P <sub>D</sub>	370	W	
Junction temperature	T <sub>j</sub>	150	°C	
Operating junction and storage tempera	ature range	T <sub>stg</sub>	-55 to +150	°C

# ●Thermal resistance

Downwoodow	Currele e l	Values			1.1:4
Parameter	Symbol	Min.	Тур.	Max.	Unit
Thermal resistance, junction - case	R <sub>thJC</sub> *4	-	-	0.34	°C/W
Thermal resistance, junction - ambient	R <sub>thJA</sub>	-	-	80	°C/W
Soldering temperature, wavesoldering for 10s	T <sub>sold</sub>	-	-	265	°C

# • Electrical characteristics $(T_a = 25^{\circ}C)$

Darameter	Cumb al	Conditions	Values			Unit
Parameter	Symbol	Conditions	Min.	Тур.	Max.	Offic
Drain - Source breakdown voltage	V <sub>(BR)DSS</sub>	$V_{GS} = 0V$ , $I_D = 1mA$	650	-	-	V
		V <sub>DS</sub> = 650V, V <sub>GS</sub> = 0V				
Zero gate voltage drain current	I <sub>DSS</sub>	$T_j = 25^{\circ}C$	-	-	100	μΑ
aram can one		$T_j = 125^{\circ}C$	-	-	1000	
Gate - Source leakage current	I <sub>GSS</sub>	$V_{GS}$ = ±20V, $V_{DS}$ = 0V	1	-	±100	nA
Gate threshold voltage	shold voltage V <sub>GS(th)</sub>		3.0	-	5.0	٧
		V <sub>GS</sub> = 10V, I <sub>D</sub> = 18.1A				
Static drain - source on - state resistance	R <sub>DS(on)</sub> *5	$T_j = 25^{\circ}C$	-	0.098	0.115	Ω
		$T_j = 125^{\circ}C$	-	0.200	-	
Gate resistance	$R_{G}$	f = 1MHz, open drain	-	1.1	-	Ω

# ● Electrical characteristics (T<sub>a</sub> = 25°C)

Darramatar	Cymah al	Conditions		Unit		
Parameter	Symbol	Conditions	Min.	Тур.	Max.	Unit
Input capacitance	C <sub>iss</sub>	V <sub>GS</sub> = 0V	-	3000	-	
Output capacitance	C <sub>oss</sub>	V <sub>DS</sub> = 25V	-	2500	-	pF
Reverse transfer capacitance	C <sub>rss</sub>	f = 1MHz	-	85	-	
Turn - on delay time	t <sub>d(on)</sub> *5	$V_{DD} \simeq 300V$ , $V_{GS} = 10V$	-	45	-	
Rise time	t <sub>r</sub> *5	I <sub>D</sub> = 17.5A	-	100	-	
Turn - off delay time	t <sub>d(off)</sub> *5	R <sub>L</sub> ~ 17.4Ω	-	115	-	ns
Fall time	<b>t</b> <sub>f</sub> *5	$R_G = 10\Omega$	-	65	-	

# ● Gate charge characteristics (T<sub>a</sub> = 25°C)

Darameter	Cumb al	Conditions	Values			Lloit
Parameter	Symbol	Conditions	Min.	Тур.	Max.	Unit
Total gate charge	$Q_g^{*5}$	V <sub>DD</sub> ≈ 300V	-	72	-	
Gate - Source charge	Q <sub>gs</sub> *5	I <sub>D</sub> = 35A	-	20	-	nC
Gate - Drain charge	Q <sub>gd</sub> *5	V <sub>GS</sub> = 10V	-	30	-	
Gate plateau voltage	V <sub>(plateau)</sub>	$V_{DD} \simeq 300V$ , $I_D = 35A$	-	6.6	-	V

<sup>\*1</sup> Limited only by maximum channel temperature allowed.

<sup>\*2</sup> Pw ≤ 10µs, Duty cycle ≤ 1%

<sup>\*3</sup> L $\doteqdot$ 50mH, V<sub>DD</sub>=50V, R<sub>G</sub>=25 $\Omega$ , STARTING T<sub>j</sub>=25 $^{\circ}$ C

<sup>\*4</sup> T<sub>C</sub>=25°C

<sup>\*5</sup> Pulsed

# ●Body diode electrical characteristics (Source-Drain) (T<sub>a</sub> = 25°C)

Parameter	Symbol	Conditions		Unit		
- Farameter	Symbol	Conditions	Min.	Тур.	Max.	Offic
Source current	I <sub>S</sub> *1	T - 25°C	-	-	35	А
Pulsed source current	I <sub>SP</sub> *2	T <sub>C</sub> = 25°C	-	-	105	Α
Source-Drain voltage	V <sub>SD</sub> *5	$V_{GS} = 0V, I_{S} = 35A$	-	-	1.5	٧
Reverse recovery time	<b>t</b> <sub>rr</sub> *5		-	660	-	ns
Reverse recovery charge	Q <sub>rr</sub> *5	I <sub>S</sub> = 35Α di/dt = 100Α/μs	-	14.5	-	μC
Peak reverse recovery current	<sub>rr</sub> *5		-	44	-	Α

## • Electrical characteristic curves

Fig.1 Power Dissipation Derating Curve

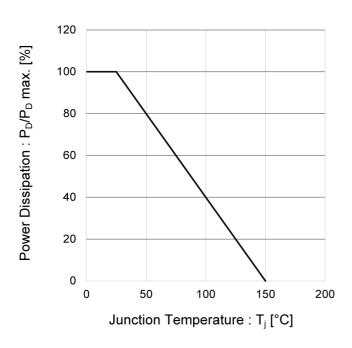


Fig.2 Drain Current Derating
Curve vs. Ambient Temperature

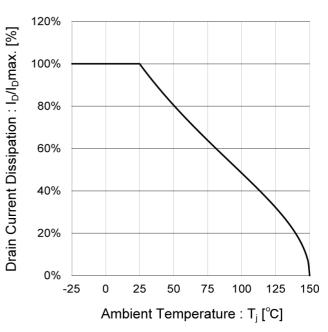


Fig.3 Normalized Transient Thermal Resistance vs. Pulse Width

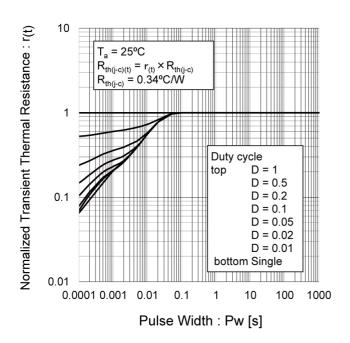
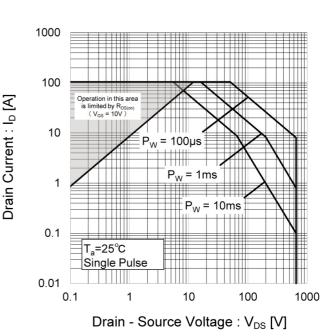


Fig.4 Maximum Safe Operating Area



### • Electrical characteristic curves

Fig.5 Avalanche Energy Derating
Curve vs. Junction Temperature

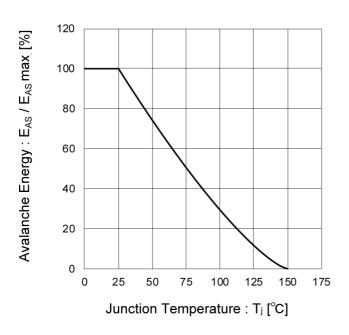


Fig.6 Normalized Breakdown Voltage vs. Junction Temperature

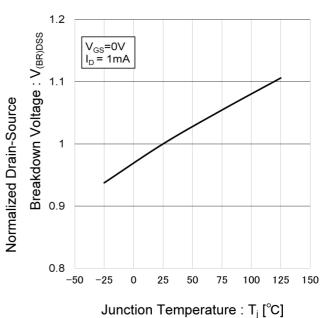


Fig.7 Typical Output Characteristics(I)

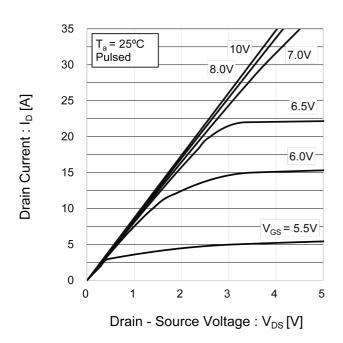
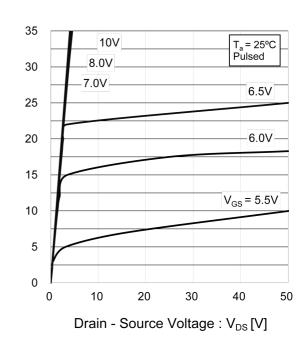


Fig.8 Typical Output Characteristics(II)



Drain Current : I<sub>D</sub> [A]

#### Electrical characteristic curves

Fig.9 Typical Transfer Characteristics

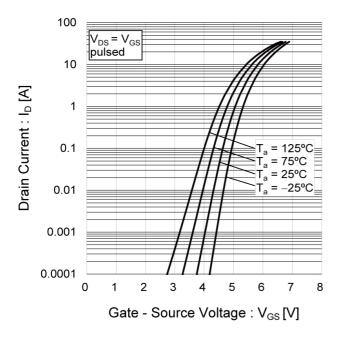


Fig.10 Normalized Gate Threshold .

Voltage vs Junction Temperature

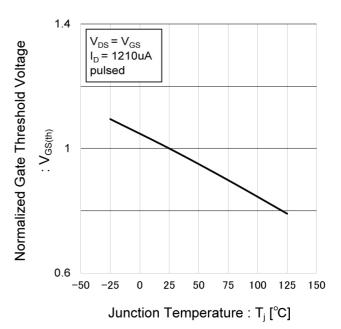


Fig.11 Static Drain - Source On - State Resistance vs. Drain Current

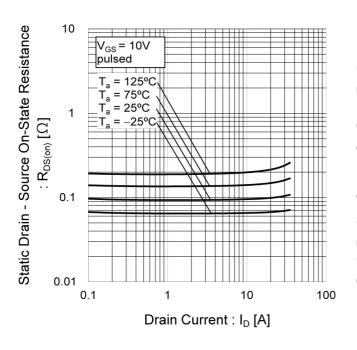
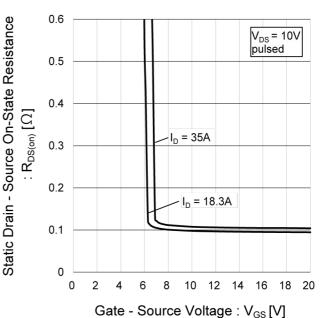


Fig.12 Static Drain - Source On - State Resistance vs. Gate Source Voltage



## • Electrical characteristic curves

Fig.13 Normalized Static Drain - Source On - State Resistance vs. Junction Temperature

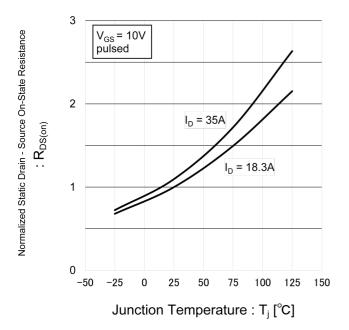


Fig.14 Typical Capacitance vs.

Drain - Source Voltage

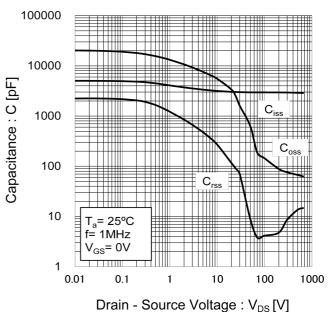


Fig.15 Switching Characteristics

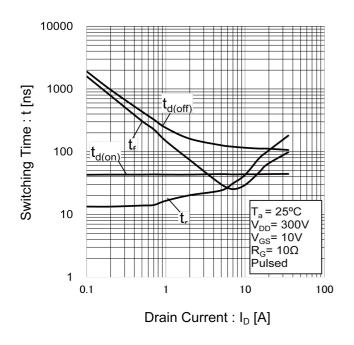
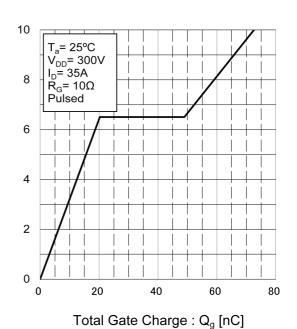


Fig.16 Typical Gate Charge



Gate - Source Voltage : V<sub>GS</sub> [V]

R6535KNX3

## • Electrical characteristic curves

Fig.17 Source Current vs. Source - Drain Voltage

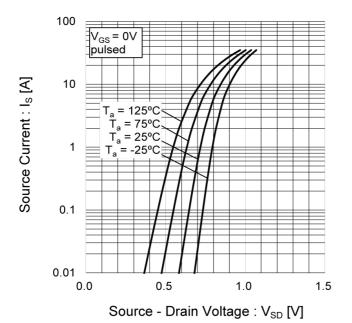
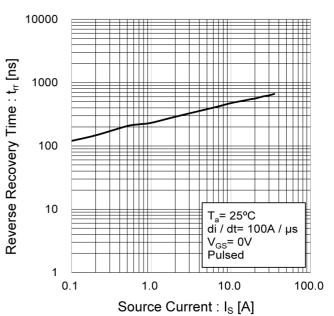


Fig.18 Reverse Recovery Time vs. Source Current



## Measurement circuits

Fig.1-1 Switching Time Measurement Circuit

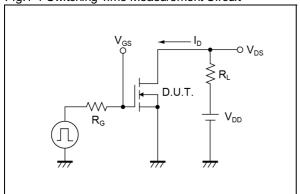


Fig.2-1 Gate Charge Measurement Circuit

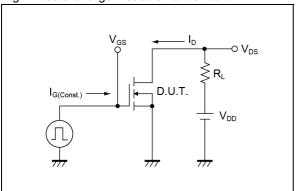


Fig.3-1 Avalanche Measurement Circuit

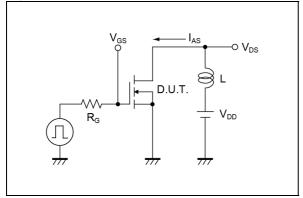


Fig.4-1 trr Measurement Circuit

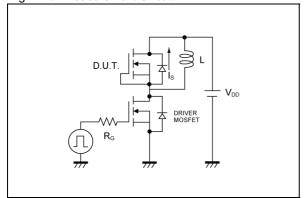


Fig.1-2 Switching Waveforms

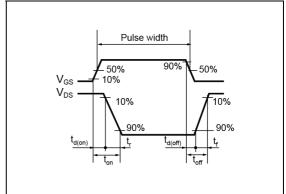


Fig.2-2 Gate Charge Waveform

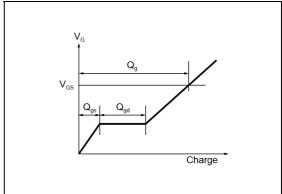


Fig.3-2 Avalanche Waveform

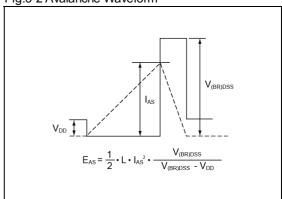
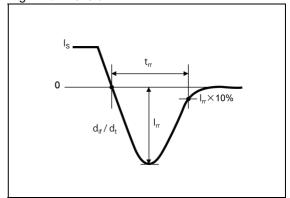
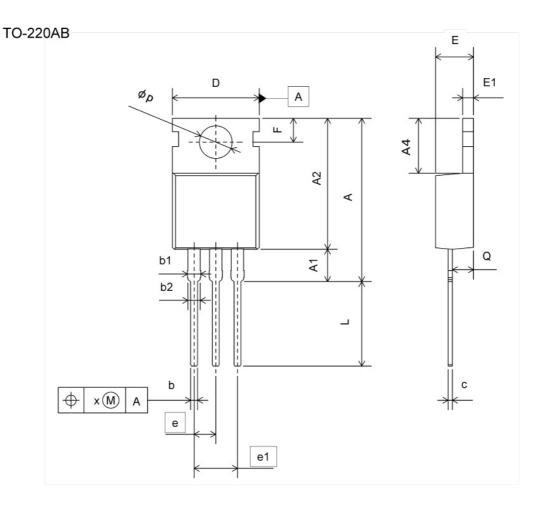


Fig.4-2 trr Waveform



# Dimensions



DIM	MILIME	TERS	INC	HES
DIM	MIN	MAX	MIN	MAX
Α	18.30	20.00	0.720	0.787
A1	3.60	4.00	0.142	0.157
A2	14.70	16.00	0.579	0.630
A4	6.30	6.60	0.248	0.260
b	0.65	0.95	0.026	0.037
b1	1.20	1.75	0.047	0.069
b2	1.20	1.70	0.047	0.067
С	0.35	0.65	0.014	0.026
D	9.96	10.36	0.392	0.408
E	4.24	4.64	0.167	0.183
E1	1.14	1.40	0.045	0.055
е	2.	54	0.1	00
e1	5.	08	0.2	200
F	2.60	3.00	0.102	0.118
L	9.47	10.37	0.373	0.408
$\phi$ p	3.69	3.99	0.145	0.157
Q	2.30	2.70	0.091	0.106
х	-0	0.38	<u>=</u>	0.015

Dimension in mm/inches



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CLASSIII	00/1	CLASS II b	OTHITI
	CLASSII		CLASSⅢ
CLASSIV	02,10011	CLASSⅢ	02,1002

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  - [e] Use of our Products in proximity to heat-producing components, plastic cords, or other flammable items
  - [f] Sealing or coating our Products with resin or other coating materials
  - [g] Use of our Products without cleaning residue of flux (Exclude cases where no-clean type fluxes is used. However, recommend sufficiently about the residue.); or Washing our Products by using water or water-soluble cleaning agents for cleaning residue after soldering
  - [h] Use of the Products in places subject to dew condensation
- 4. The Products are not subject to radiation-proof design.
- 5. Please verify and confirm characteristics of the final or mounted products in using the Products.
- 6. In particular, if a transient load (a large amount of load applied in a short period of time, such as pulse, is applied, confirmation of performance characteristics after on-board mounting is strongly recommended. Avoid applying power exceeding normal rated power; exceeding the power rating under steady-state loading condition may negatively affect product performance and reliability.
- 7. De-rate Power Dissipation depending on ambient temperature. When used in sealed area, confirm that it is the use in the range that does not exceed the maximum junction temperature.
- 8. Confirm that operation temperature is within the specified range described in the product specification.
- 9. ROHM shall not be in any way responsible or liable for failure induced under deviant condition from what is defined in this document.

#### Precaution for Mounting / Circuit board design

- 1. When a highly active halogenous (chlorine, bromine, etc.) flux is used, the residue of flux may negatively affect product performance and reliability.
- 2. In principle, the reflow soldering method must be used on a surface-mount products, the flow soldering method must be used on a through hole mount products. If the flow soldering method is preferred on a surface-mount products, please consult with the ROHM representative in advance.

For details, please refer to ROHM Mounting specification

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- 1. If change is made to the constant of an external circuit, please allow a sufficient margin considering variations of the characteristics of the Products and external components, including transient characteristics, as well as static characteristics.
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#### **Precaution for Electrostatic**

This Product is electrostatic sensitive product, which may be damaged due to electrostatic discharge. Please take proper caution in your manufacturing process and storage so that voltage exceeding the Products maximum rating will not be applied to Products. Please take special care under dry condition (e.g. Grounding of human body / equipment / solder iron, isolation from charged objects, setting of lonizer, friction prevention and temperature / humidity control).

## **Precaution for Storage / Transportation**

- 1. Product performance and soldered connections may deteriorate if the Products are stored in the places where:
  - [a] the Products are exposed to sea winds or corrosive gases, including Cl<sub>2</sub>, H<sub>2</sub>S, NH<sub>3</sub>, SO<sub>2</sub>, and NO<sub>2</sub>
  - [b] the temperature or humidity exceeds those recommended by ROHM
  - [c] the Products are exposed to direct sunshine or condensation
  - [d] the Products are exposed to high Electrostatic
- Even under ROHM recommended storage condition, solderability of products out of recommended storage time period
  may be degraded. It is strongly recommended to confirm solderability before using Products of which storage time is
  exceeding the recommended storage time period.
- 3. Store / transport cartons in the correct direction, which is indicated on a carton with a symbol. Otherwise bent leads may occur due to excessive stress applied when dropping of a carton.
- 4. Use Products within the specified time after opening a humidity barrier bag. Baking is required before using Products of which storage time is exceeding the recommended storage time period.

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