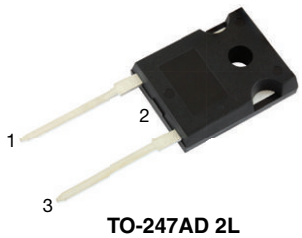
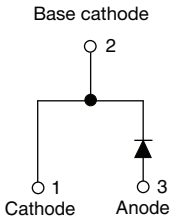
1200 V Power SiC Gen 3 Merged PIN Schottky Diode, 15 A



TO-247AD 2L



LINKS TO ADDITIONAL RESOURCES

3D Models

PRIMARY CHARACTERISTICS	
I_F	15 A
V_R	1200 V
V_F at I_F at 25 °C, typ.	1.35 V
T_J max.	175 °C
I_R at V_R at 175 °C	6.5 µA
Q_C (V_R = 800 V)	81 nC
Package	TO-247AD 2L
Circuit configuration	Single

FEATURES

- Majority carrier diode using Schottky technology on SiC wide band gap material
- Improved V_F and efficiency by thin wafer technology
- Positive V_F temperature coefficient for easy paralleling
- Virtually no recovery tail and no switching losses
- Temperature invariant switching behavior
- 175 °C maximum operating junction temperature
- MPS structure for high ruggedness to forward current surge events
- Meets JESD 201 class 1A whisker test
- Solder bath temperature 275 °C maximum, 10 s per JESD 22-B106
- Material categorization: for definitions of compliance please see www.vishay.com/doc?99912

RoHS COMPLIANT

HALOGEN FREE

DESCRIPTION / APPLICATIONS

Wide band gap SiC based 1200 V Schottky diode, designed for high performance and ruggedness.

Optimum choice for high speed hard switching and efficient operation over a wide temperature range, it is also recommended for all applications suffering from Silicon ultrafast recovery behavior.

Typical applications include AC/DC PFC and DC/DC ultra high frequency output rectification in FBPS and LLC converters.

MECHANICAL DATA

Case: TO-247AD 2L

Molding compound meets UL 94 V-0 flammability rating Base P/N-M3 - halogen-free, RoHS-compliant

Terminals: matte tin plated leads, solderable per J-STD-002 and JESD 22-B102

Mounting torque: 10 in-lbs maximum

MAXIMUM RATINGS (T_A = 25 °C unless otherwise specified)

PARAMETER	SYMBOL	TEST CONDITIONS	VALUES	UNITS
Peak repetitive reverse voltage	V_{RRM}		1200	V
Continuous forward current	I_F (1)	T_C = 147 °C (DC)	15	A
	I_F (2)	T_C = 155 °C (DC)		
DC blocking voltage	V_{DC}		1200	V
Repetitive peak forward current	I_{FRM}	T_C = 25 °C, f = 50 Hz, square wave, DC = 25 %	70	A
Non-repetitive peak forward surge current	I_{FSM}	T_C = 25 °C, t_p = 10 ms, half sine wave	110	A
		T_C = 110 °C, t_p = 10 ms, half sine wave	105	
Power dissipation	P_{tot} (1)	T_C = 25 °C	150	W
		T_C = 110 °C	65	
	P_{tot} (2)	T_C = 25 °C	214	W
		T_C = 110 °C	93	
I^2t value	$\int i^2 dt$	T_C = 25 °C	61	A^2s
		T_C = 110 °C	54	
Operating junction and storage temperatures	T_J (3), T_{Stg}		-55 to +175	°C

Notes

(1) Based on maximum R_{th}

(2) Based on typical R_{th}

(3) The heat generated must be less than the thermal conductivity from junction-to-ambient: $dP_D/dT_J < 1/R_{\theta JA}$

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ELECTRICAL SPECIFICATIONS (T_J = 25 °C unless otherwise specified)						
PARAMETER	**SYMBOL**	**TEST CONDITIONS**	**MIN.**	**TYP.**	**MAX.**	**UNITS**
Forward voltage	V_F	I_F = 15 A	-	1.35	1.5	V
		I_F = 15 A, T_J = 150 °C	-	1.73	2.0	
		I_F = 15 A, T_J = 175 °C	-	1.85	-	
Reverse leakage current	I_R	V_R = V_R rated	-	1	75	µA
		V_R = V_R rated, T_J = 150 °C	-	3.5	160	
		V_R = V_R rated, T_J = 175 °C	-	6.5	-	
Total capacitance	C	V_R = 1 V, f = 1 MHz	-	900	-	pF
		V_R = 800 V, f = 1 MHz	-	56	-	
Total capacitive charge	Q_C	V_R = 800 V, f = 1 MHz	-	81	-	nC

THERMAL - MECHANICAL SPECIFICATIONS (T_A = 25 °C unless otherwise specified)						
PARAMETER	**SYMBOL**	**TEST CONDITIONS**	**MIN.**	**TYP.**	**MAX.**	**UNITS**
Thermal resistance, junction-to-case	R_{thJC}		-	0.7	1	°C/W
Marking device			3C15EP12L			

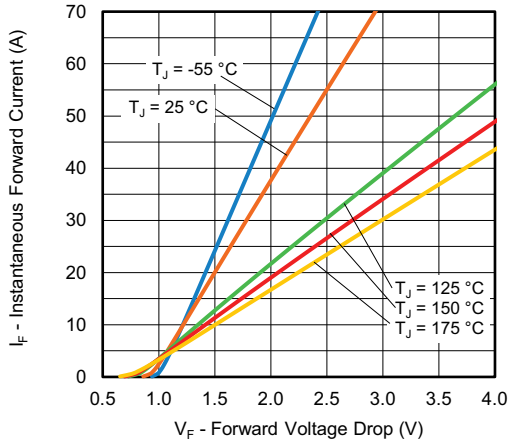


Fig. 1 - Typical Forward Voltage Drop Characteristics

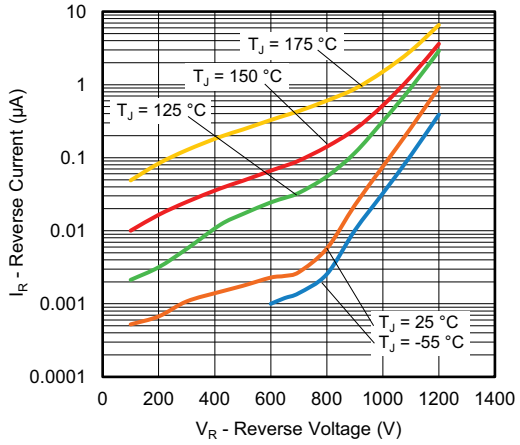


Fig. 2 - Typical Values of Reverse Current vs. Reverse Voltage

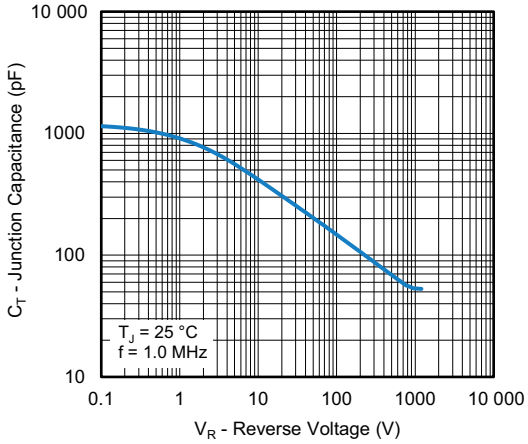


Fig. 3 - Typical Junction Capacitance vs. Reverse Voltage

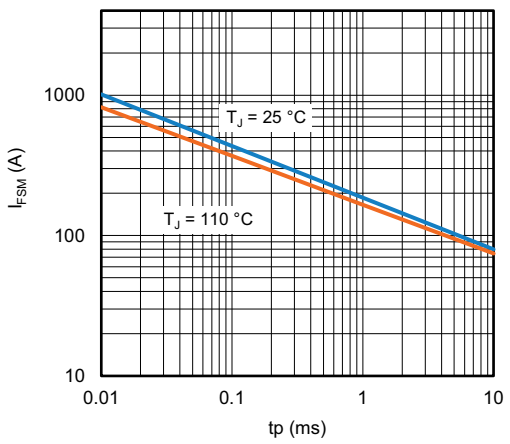


Fig. 4 - Non-Repetitive Peak Forward Surge Current vs. Pulse Duration (Square Wave)

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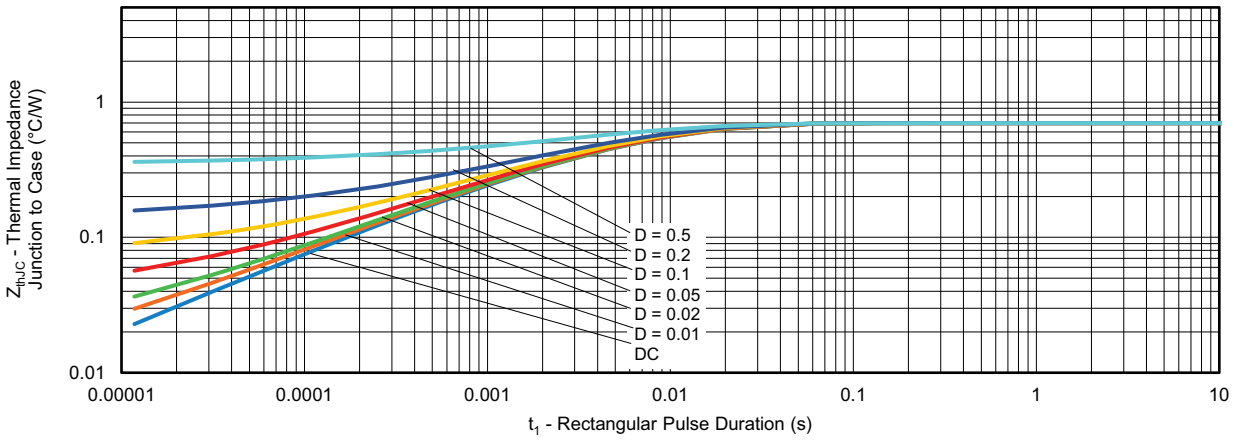


Fig. 5 - Typical Thermal Impedance Z_{thJC} Characteristics

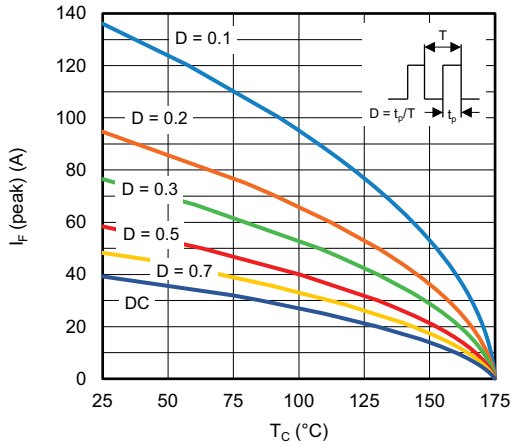


Fig. 6 - Peak Forward Current vs. Maximum Allowable Case Temperature

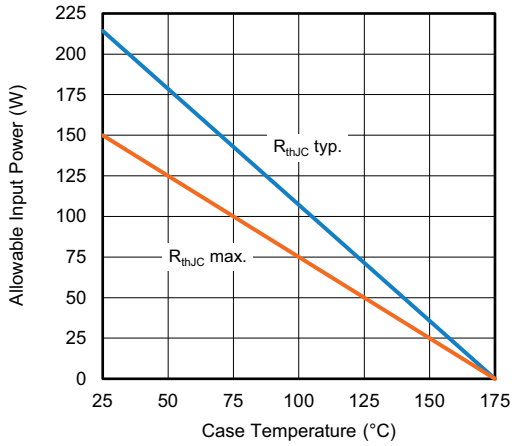


Fig. 7 - Forward Power Loss Characteristics

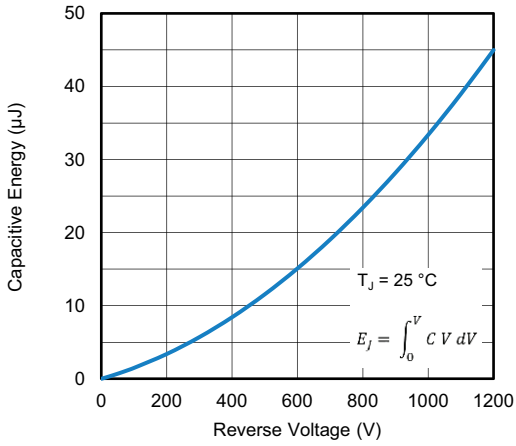


Fig. 8 - Typical Capacitive Energy vs. Reverse Voltage

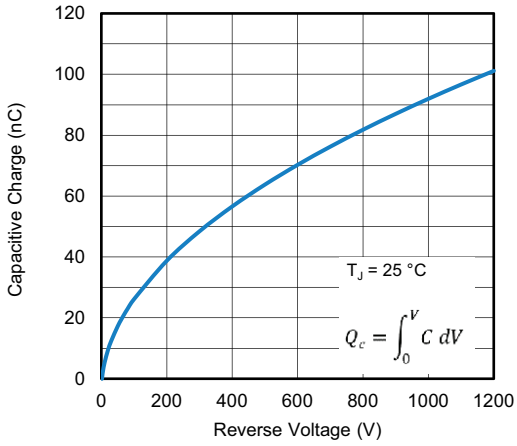
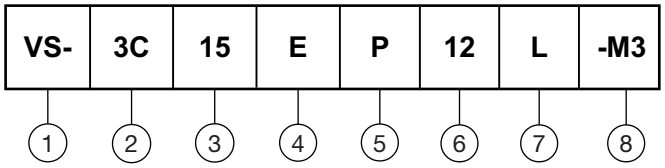


Fig. 9 - Typical Capacitive Charge vs. Reverse Voltage

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ORDERING INFORMATION TABLE

Device code



1 - Vishay Semiconductors product
2 - 3C = SiC diode, Generation 3
3 - Current rating (15 = 15 A)
4 - E = single diode
5 - P = package TO-247
6 - Voltage rating: 12 = 1200 V
7 - L = long lead
8 - Environmental digit:
-M3 = halogen-free, RoHS-compliant, and termination lead (Pb)-free

ORDERING INFORMATION		
PREFERRED P/N	**BASE QUANTITY**	**PACKAGING DESCRIPTION**
VS-3C15EP12L-M3	25 / tube	Antistatic plastic tubes

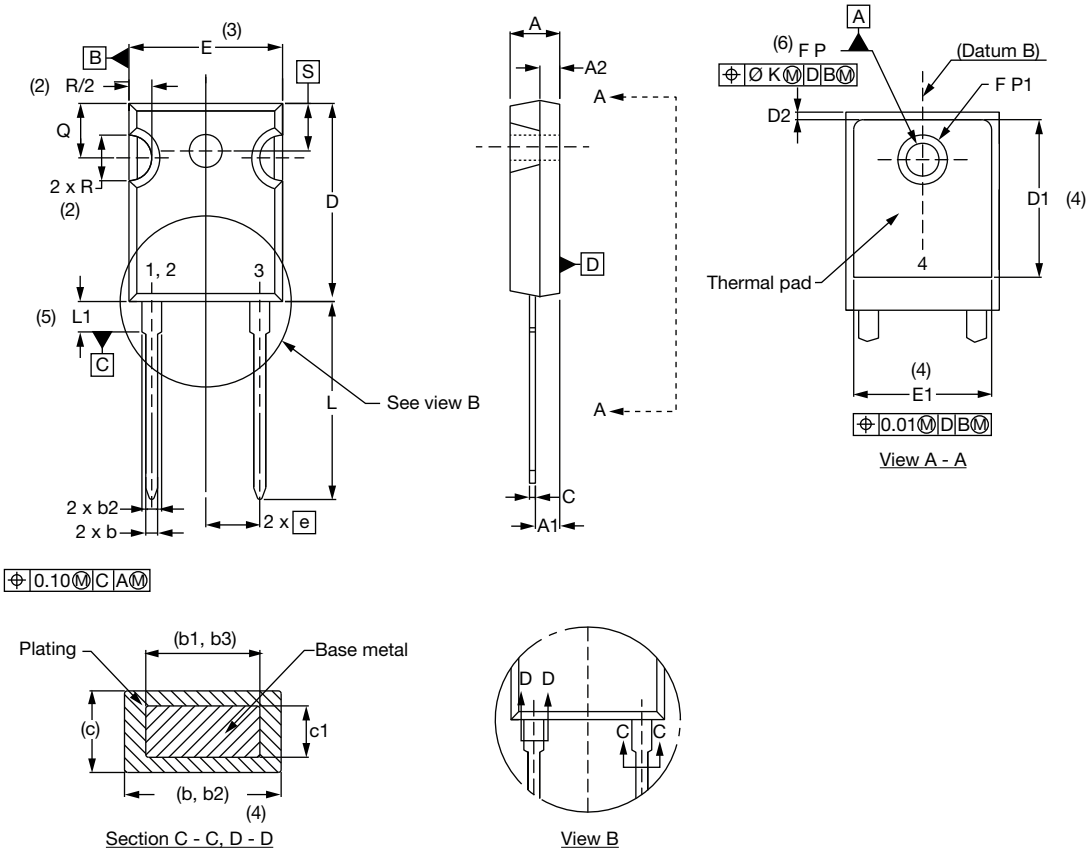
LINKS TO RELATED DOCUMENTS	
Dimensions	www.vishay.com/doc?95536
Part marking information	www.vishay.com/doc?95648

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TO-247AD 2L

DIMENSIONS in millimeters and inches



SYMBOL	MILLIMETERS		INCHES		NOTES
	MIN.	MAX.	MIN.	MAX.	
A	4.65	5.31	0.183	0.209	
A1	2.21	2.59	0.087	0.102	
A2	1.50	2.49	0.059	0.098	
b	0.99	1.40	0.039	0.055	
b1	0.99	1.35	0.039	0.053	
b2	1.65	2.39	0.065	0.094	
b3	1.65	2.34	0.065	0.092	
c	0.38	0.89	0.015	0.035	
c1	0.38	0.84	0.015	0.033	
D	19.71	20.70	0.776	0.815	3
D1	13.08	-	0.515	-	4
D2	0.51	1.35	0.020	0.053	

SYMBOL	MILLIMETERS		INCHES		NOTES
	MIN.	MAX.	MIN.	MAX.	
E	15.29	15.87	0.602	0.625	3
E1	13.46	-	0.53	-	
e	5.46 BSC		0.215 BSC		
Ø K	0.254		0.010		
L	19.81	20.32	0.780	0.800	
L1	3.71	4.29	0.146	0.169	
Ø P	3.56	3.66	0.14	0.144	
Ø P1	-	6.98	-	0.275	
Q	5.31	5.69	0.209	0.224	
R	4.52	5.49	0.178	0.216	
S	5.51 BSC		0.217 BSC		

Notes

(1) Dimensioning and tolerancing per ASME Y14.5M-1994

(2) Contour of slot optional

(3) Dimension D and E do not include mold flash. These dimensions are measured at the outermost extremes of the plastic body

(4) Thermal pad contour optional with dimensions D1 and E1

(5) Lead finish uncontrolled in L1

(6) Ø P to have a maximum draft angle of 1.5 to the top of the part with a maximum hole diameter of 3.91 mm (0.154")

(7) Outline conforms to JEDEC® outline TO-247 with exception of dimension A min., D, E min., Q min., S, and note 4

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