40V PNP SMALL-SIGNAL TRANSISTOR IN SOT23

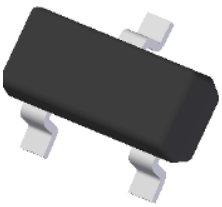
Features

- Ideal for Medium Power Amplification and Switching
- Complementary NPN Type: MMBT4401
- **Totally Lead-Free & Fully RoHS Compliant (Notes 1 & 2)**
- **Halogen and Antimony Free. "Green" Device (Note 3)**
- **For automotive applications requiring specific change control (i.e. parts qualified to AEC-Q100/101/104/200, PPAP capable, and manufactured in IATF 16949 certified facilities), please contact us or your local Diodes representative. https://www.diodes.com/quality/product-definitions/**

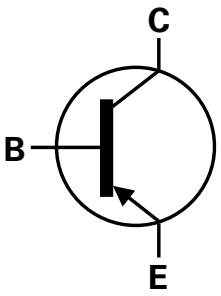
Mechanical Data

- Package: SOT23
- Package Material: Molded Plastic "Green" Compound UL Flammability Rating 94V-0
- Moisture Sensitivity: Level 1 per J-STD-020
- Terminals: Finish - Matte Tin Plated Leads, Solderable per MIL-STD-202, Method 208 e3
- Weight: 0.008 grams (Approximate)

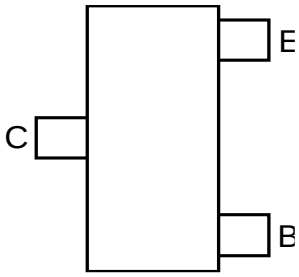
SOT23



Top View



Device Symbol



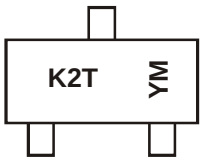
Top View
Pinout

Ordering Information (Note 4)

Orderable Part Number	Package	Marking	Reel Size (inches)	Tape Width (mm)	Packing	
					Qty.	Carrier
MMBT4403-7-F	SOT23	K2T	7	8	3,000	Reel
MMBT4403-13-F	SOT23	K2T	13	8	10,000	Reel

Notes:
1. No purposely added lead. Fully EU Directive 2002/95/EC (RoHS), 2011/65/EU (RoHS 2) & 2015/863/EU (RoHS 3) compliant.
2. See https://www.diodes.com/quality/lead-free/ for more information about Diodes Incorporated's definitions of Halogen- and Antimony-free, "Green" and Lead-free.
3. Halogen- and Antimony-free "Green" products are defined as those which contain <900ppm bromine, <900ppm chlorine (<1500ppm total Br + Cl) and <1000ppm antimony compounds.
4. For packaging details, go to our website at https://www.diodes.com/design/support/packaging/diodes-packaging/.

Marking Information



K2T = Product Type Marking Code
YM = Date Code Marking
Y or $\overline{Y}$ or $\underline{Y}$ = Year (ex: M = 2025)
M = Month (ex: 2 = February)

Date Code Key

Year	2003	-	2025	2026	2027	2028	2029	2030	2031	2032	2033	2034
Code	P	-	M	N	P	R	S	T	U	V	W	X

Month	Jan	Feb	Mar	Apr	May	Jun	Jul	Aug	Sep	Oct	Nov	Dec
Code	1	2	3	4	5	6	7	8	9	O	N	D

Absolute Maximum Ratings (@T_A = +25°C, unless otherwise specified.)

Characteristic	Symbol	Value	Unit
Collector-Base Voltage	V_{CBO}	-40	V
Collector-Emitter Voltage	V_{CEO}	-40	V
Emitter-Base Voltage	V_{EBO}	-6	V
Collector Current - Continuous (Note 7)	I_C	-600	mA
Peak Pulse Collector Current	I_{CM}	-1	A

Thermal Characteristics (@T_A = +25°C, unless otherwise specified.)

Characteristic		Symbol	Value	Unit
Collector Power Dissipation	(Note 5)	P_D	310	mW
	(Note 6)		350	
Thermal Resistance, Junction to Ambient	(Note 5)	$R_{\theta JA}$	403	°C/W
	(Note 6)		357	
Thermal Resistance, Junction to Leads	(Note 7)	$R_{\theta JL}$	350	°C/W
Thermal Resistance, Junction to Case	(Note 5)	$R_{\theta JC}$	55	°C/W
Operating and Storage Temperature Range		T_J, T_{STG}	-55 to +150	°C

Notes:
5. For the device mounted on minimum recommended pad layout FR4 PCB with high coverage of single sided 1oz copper, in still air conditions.
6. For the device mounted on 15mm x 15mm x 1.6mm FR4 PCB with high coverage of single sided 1oz copper, in still air conditions.
7. Thermal resistance from junction to solder-point (at the end of the collector lead).

Thermal Characteristics and Derating Information

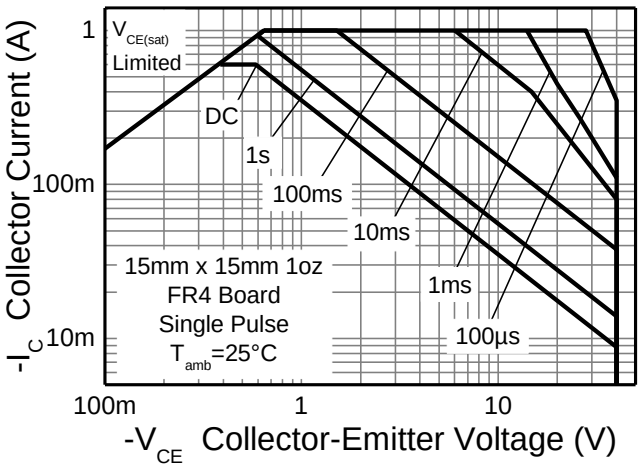


Fig 1. Safe Operating Area

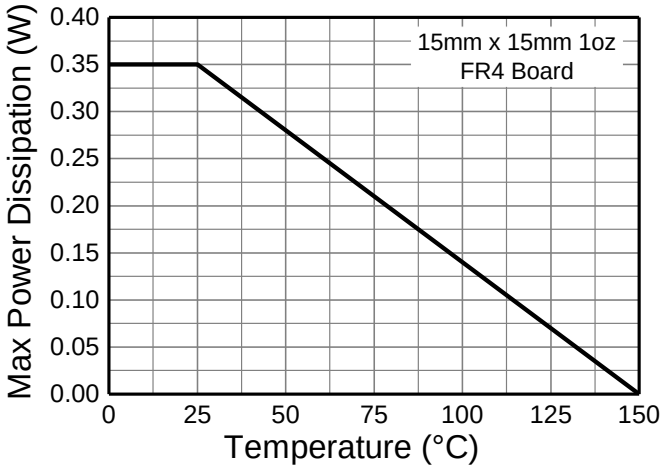


Fig 2. Derating Curve

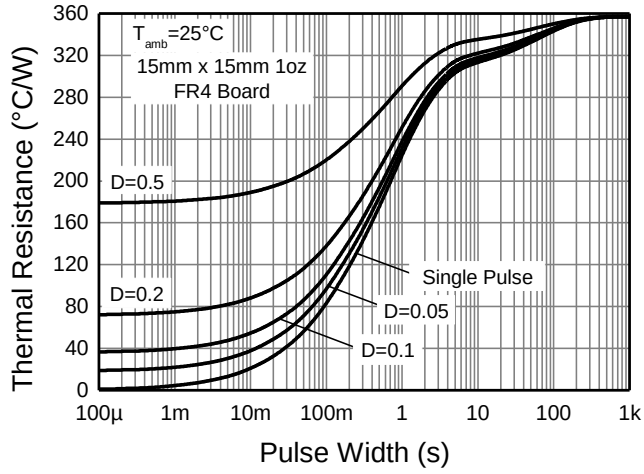


Fig 3. Transient Thermal Impedance

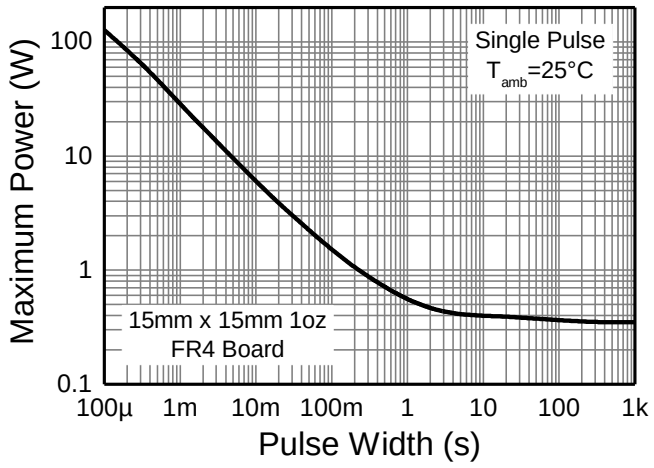


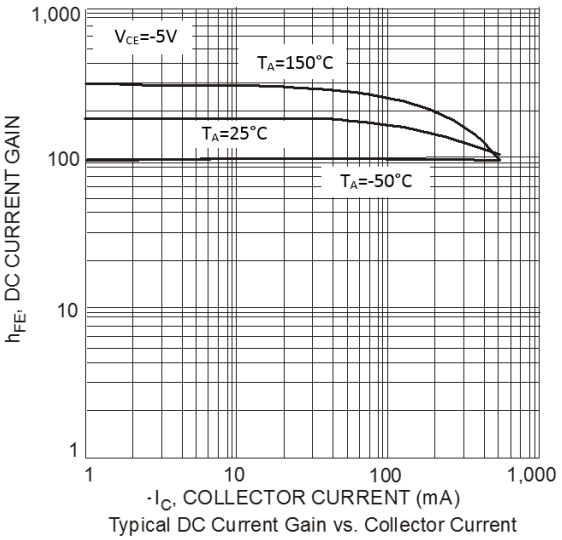
Fig 4. Pulse Power Dissipation

Electrical Characteristics (@T_A = +25°C, unless otherwise specified.)

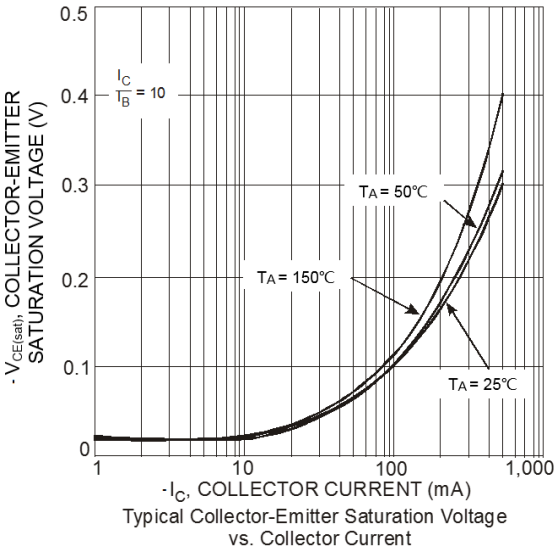
Characteristic	Symbol	Min	Max	Unit	Test Condition
OFF CHARACTERISTICS (Note 8)					
Collector-Base Breakdown Voltage	BV_{CBO}	-40	—	V	I_C = -100μA
Collector-Emitter Breakdown Voltage	BV_{CEO}	-40	—	V	I_C = -10mA
Emitter-Base Breakdown Voltage	BV_{EBO}	-6	—	V	I_E = -100μA
Collector Cutoff Current	I_{CEX}	—	-100	nA	V_{CE} = -35V, $V_{EB(off)}$ = -0.4V
Base Cutoff Current	I_{BL}	—	-100	nA	V_{CE} = -35V, $V_{EB(off)}$ = -0.4V
ON CHARACTERISTICS (Note 8)					
DC Current Gain	h_{FE}	30 60 100 100 20	— — — 300 —	—	I_C = -100μA, V_{CE} = -1V I_C = -1.0mA, V_{CE} = -1V I_C = -10mA, V_{CE} = -1V I_C = -150mA, V_{CE} = -2V I_C = -500mA, V_{CE} = -2V
Collector-Emitter Saturation Voltage	$V_{CE(sat)}$	—	-0.40 -0.75	V	I_C = -150mA, I_B = -15mA I_C = -500mA, I_B = -50mA
Base-Emitter Saturation Voltage	$V_{BE(sat)}$	-0.75 —	-0.95 -1.30	V	I_C = -150mA, I_B = -15mA I_C = -500mA, I_B = -50mA
SMALL-SIGNAL CHARACTERISTICS					
Output Capacitance	C_{obo}	—	8.5	pF	V_{CB} = -10V, f = 1.0MHz, I_E = 0
Input Capacitance	C_{ibo}	—	30	pF	V_{EB} = -0.5V, f = 1.0MHz, I_C = 0
Input Impedance	h_{ie}	1.5	15	kΩ	V_{CE} = -10V, I_C = -1mA, f = 1kHz
Voltage Feedback Ratio	h_{re}	0.1	8.0	x 10^{-4}	
Small-Signal Current Gain	h_{fe}	60	500	—	
Output Admittance	h_{oe}	1.0	100	μS	
Current Gain-Bandwidth Product	f_T	200	—	MHz	V_{CE} = -10V, I_C = -20mA, f = 100MHz
SWITCHING CHARACTERISTICS					
Delay Time	t_d	—	15	ns	V_{CC} = -30V, I_C = -150mA, $V_{BE(off)}$ = -2V, I_{B1} = -15mA
Rise Time	t_r	—	20	ns	
Storage Time	t_s	—	225	ns	V_{CC} = -30V, I_C = -150mA, I_{B1} = $-I_{B2}$ = -15mA
Fall Time	t_f	—	30	ns	

Note: 8. Short duration pulse test used to minimize self-heating effect.

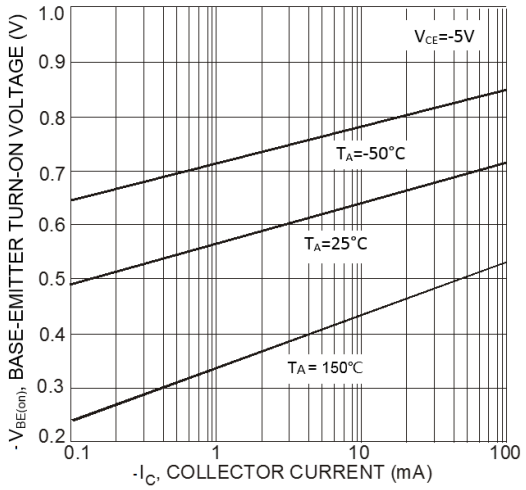
Typical Electrical Characteristics (@T_A = +25°C, unless otherwise specified.)



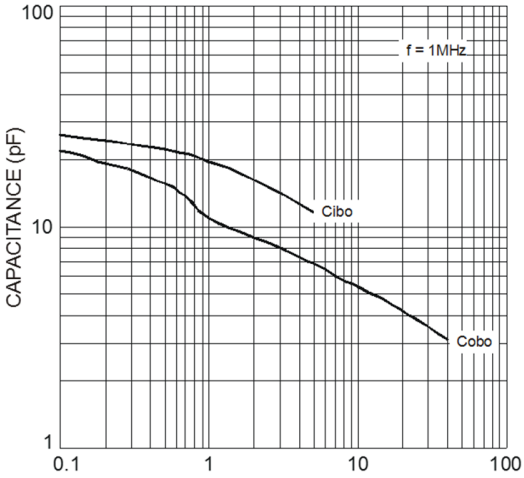
Typical DC Current Gain vs. Collector Current



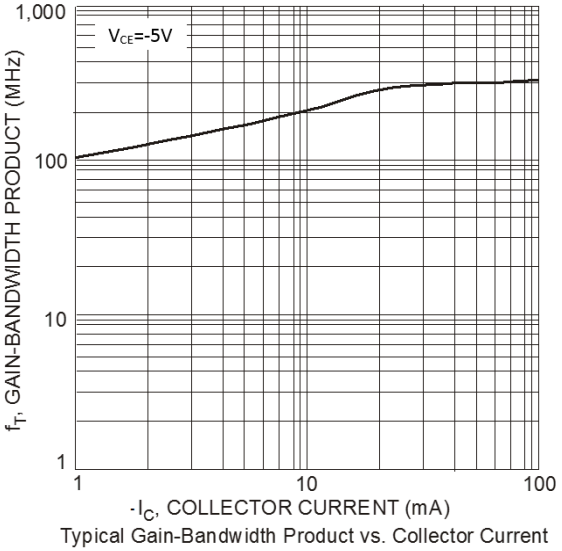
Typical Collector-Emitter Saturation Voltage vs. Collector Current



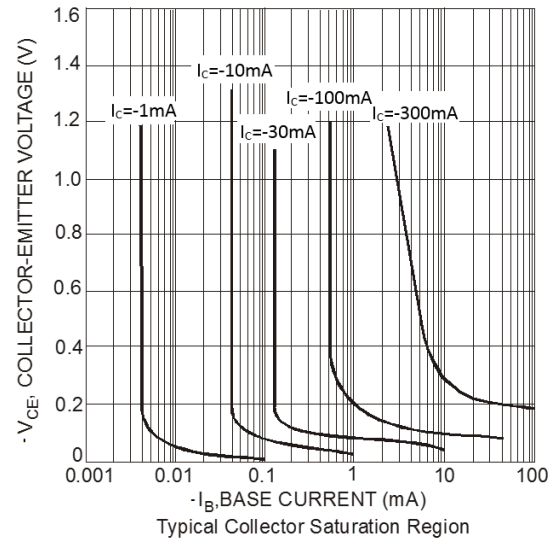
Typical Base-Emitter Turn-On Voltage vs. Collector Current



Typical Capacitance Characteristics



Typical Gain-Bandwidth Product vs. Collector Current

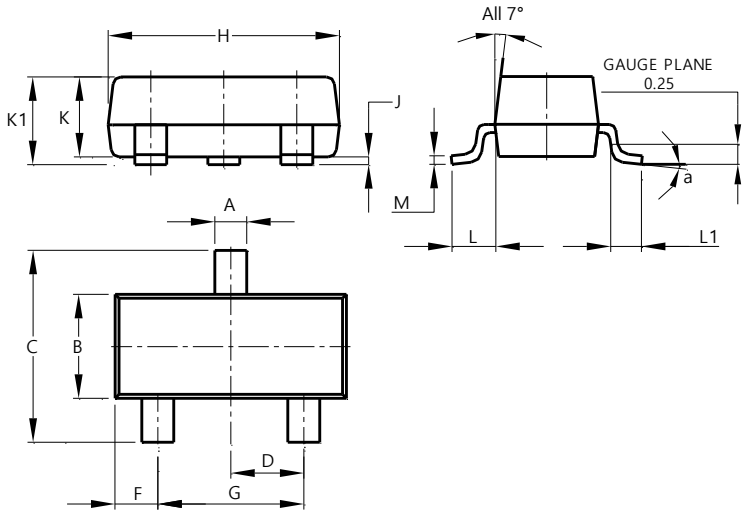


Typical Collector Saturation Region

Package Outline Dimensions

Please see http://www.diodes.com/package-outlines.html for the latest version.

SOT23

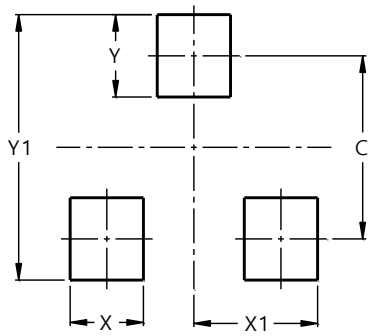


SOT23			
Dim	**Min**	**Max**	**Typ**
A	0.37	0.51	0.40
B	1.20	1.40	1.30
C	2.30	2.50	2.40
D	0.89	1.03	0.915
F	0.45	0.60	0.535
G	1.78	2.05	1.83
H	2.80	3.00	2.90
J	0.013	0.10	0.05
K	0.890	1.00	0.975
K1	0.903	1.10	1.025
L	0.45	0.61	0.55
L1	0.25	0.55	0.40
M	0.085	0.150	0.110
a	0°	8°	--
All Dimensions in mm			

Suggested Pad Layout

Please see http://www.diodes.com/package-outlines.html for the latest version.

SOT23



Dimensions	Value (in mm)
C	2.0
X	0.8
X1	1.35
Y	0.9
Y1	2.9

IMPORTANT NOTICE

1. DIODES INCORPORATED (Diodes) AND ITS SUBSIDIARIES MAKE NO WARRANTY OF ANY KIND, EXPRESS OR IMPLIED, WITH REGARDS TO ANY INFORMATION CONTAINED IN THIS DOCUMENT, INCLUDING, BUT NOT LIMITED TO, THE IMPLIED WARRANTIES OF MERCHANTABILITY, FITNESS FOR A PARTICULAR PURPOSE OR NON-INFRINGEMENT OF THIRD PARTY INTELLECTUAL PROPERTY RIGHTS (AND THEIR EQUIVALENTS UNDER THE LAWS OF ANY JURISDICTION).

2. The Information contained herein is for informational purpose only and is provided only to illustrate the operation of Diodes' products described herein and application examples. Diodes does not assume any liability arising out of the application or use of this document or any product described herein. This document is intended for skilled and technically trained engineering customers and users who design with Diodes' products. Diodes' products may be used to facilitate safety-related applications; however, in all instances customers and users are responsible for (a) selecting the appropriate Diodes products for their applications, (b) evaluating the suitability of Diodes' products for their intended applications, (c) ensuring their applications, which incorporate Diodes' products, comply the applicable legal and regulatory requirements as well as safety and functional-safety related standards, and (d) ensuring they design with appropriate safeguards (including testing, validation, quality control techniques, redundancy, malfunction prevention, and appropriate treatment for aging degradation) to minimize the risks associated with their applications.

3. Diodes assumes no liability for any application-related information, support, assistance or feedback that may be provided by Diodes from time to time. Any customer or user of this document or products described herein will assume all risks and liabilities associated with such use, and will hold Diodes and all companies whose products are represented herein or on Diodes' websites, harmless against all damages and liabilities.

4. Products described herein may be covered by one or more United States, international or foreign patents and pending patent applications. Product names and markings noted herein may also be covered by one or more United States, international or foreign trademarks and trademark applications. Diodes does not convey any license under any of its intellectual property rights or the rights of any third parties (including third parties whose products and services may be described in this document or on Diodes' website) under this document.

5. Diodes' products are provided subject to Diodes' Standard Terms and Conditions of Sale (https://www.diodes.com/about/company/terms-and-conditions/terms-and-conditions-of-sales/) or other applicable terms. This document does not alter or expand the applicable warranties provided by Diodes. Diodes does not warrant or accept any liability whatsoever in respect of any products purchased through unauthorized sales channel.

6. Diodes' products and technology may not be used for or incorporated into any products or systems whose manufacture, use or sale is prohibited under any applicable laws and regulations. Should customers or users use Diodes' products in contravention of any applicable laws or regulations, or for any unintended or unauthorized application, customers and users will (a) be solely responsible for any damages, losses or penalties arising in connection therewith or as a result thereof, and (b) indemnify and hold Diodes and its representatives and agents harmless against any and all claims, damages, expenses, and attorney fees arising out of, directly or indirectly, any claim relating to any noncompliance with the applicable laws and regulations, as well as any unintended or unauthorized application.

7. While efforts have been made to ensure the information contained in this document is accurate, complete and current, it may contain technical inaccuracies, omissions and typographical errors. Diodes does not warrant that information contained in this document is error-free and Diodes is under no obligation to update or otherwise correct this information. Notwithstanding the foregoing, Diodes reserves the right to make modifications, enhancements, improvements, corrections or other changes without further notice to this document and any product described herein. This document is written in English but may be translated into multiple languages for reference. Only the English version of this document is the final and determinative format released by Diodes.

8. Any unauthorized copying, modification, distribution, transmission, display or other use of this document (or any portion hereof) is prohibited. Diodes assumes no responsibility for any losses incurred by the customers or users or any third parties arising from any such unauthorized use.

9. This Notice may be periodically updated with the most recent version available at https://www.diodes.com/about/company/terms-and-conditions/important-notice

www.diodes.com