

# SANYO Semiconductors DATA SHEET

An ON Semiconductor Company

# TIG064E8 — N-Channel IGBT Light-Controlling Flash Applications

#### **Features**

- · Low-saturation voltage
- · Enhansment type
- · Mounting Height 0.9mm, Mounting Area 8.12mm<sup>2</sup>
- · Halogen free compliance

- · Low voltage drive (2.5V)
- · Built-in Gate-to-Emitter protection diode
- dv / dt guarantee\*

### **Specifications**

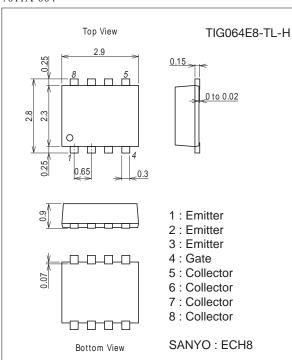
#### Absolute Maximum Ratings at Ta=25°C

Parameter	Symbol	Conditions	Ratings	Unit
Collector-to-Emitter Voltage	VCES		400	V
Gate-to-Emitter Voltage (DC)	VGES		±4	V
Gate-to-Emitter Voltage (Pulse)	VGES	PW≤1ms	±5	V
Collector Current (Pulse)	ICP	V <sub>GE</sub> =2.5V, C <sub>M</sub> =100μF	150	Α
Maximum Collector-to-Emitter dv / dt	dV <sub>CE</sub> / dt	V <sub>CE</sub> ≤320V, starting Tch=25°C	400	V/μs
Channel Temperature	Tch		150	°C
Storage Temperature	Tstg		-40 to +150	°C

<sup>\*:</sup> Concerning dv / dt (slope of Collector Voltage at the time of Turn-OFF), dv / dt > 400V / µs will be 100% screen-detected in the circuit shown as Fig. 1.

#### **Package Dimensions**

unit : mm (typ) 7011A-004



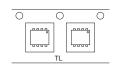
#### **Product & Package Information**

• Package : ECH8

• JEITA, JEDEC :-

• Minimum Packing Quantity : 3000 pcs./reel

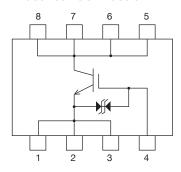
#### Packing Type: TL



#### Marking



#### **Electrical Connection**



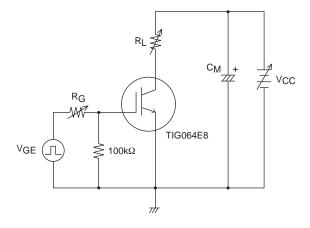
#### **SANYO Semiconductor Co., Ltd.**

http://semicon.sanyo.com/en/network

#### Electrical Characteristics at Ta=25°C

Parameter	Symbol	Conditions	Ratings			Unit
		Conditions	min	typ	max	Offic
Collector-to-Emitter Breakdown Voltage	V(BR)CES	I <sub>C</sub> =2mA, V <sub>GE</sub> =0V	400			V
Collector-to-Emitter Cutoff Current	ICES	V <sub>CE</sub> =320V, V <sub>GE</sub> =0V			10	μΑ
Gate-to-Emitter Leakage Current	IGES	VGE=±4V, VCE=0V			±10	μΑ
Gate-to-Emitter Threshold Voltage	V <sub>GE</sub> (off)	V <sub>CE</sub> =10V, I <sub>C</sub> =1mA	0.4		0.9	V
Collector-to-Emitter Saturation Voltage	V <sub>CE</sub> (sat)	I <sub>C</sub> =100A, V <sub>GE</sub> =2.5V		4.2	7	V
Input Capacitance	Cies			3100		pF
Output Capacitance	Coes	V <sub>CE</sub> =10V, f=1MHz		30		pF
Reverse Transfer Capacitance	Cres			23		pF

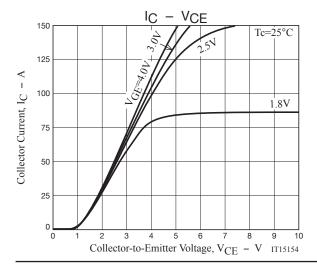
Fig.1 Large Current R Load Switching Circuit

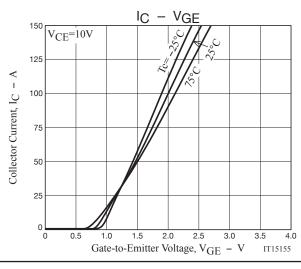


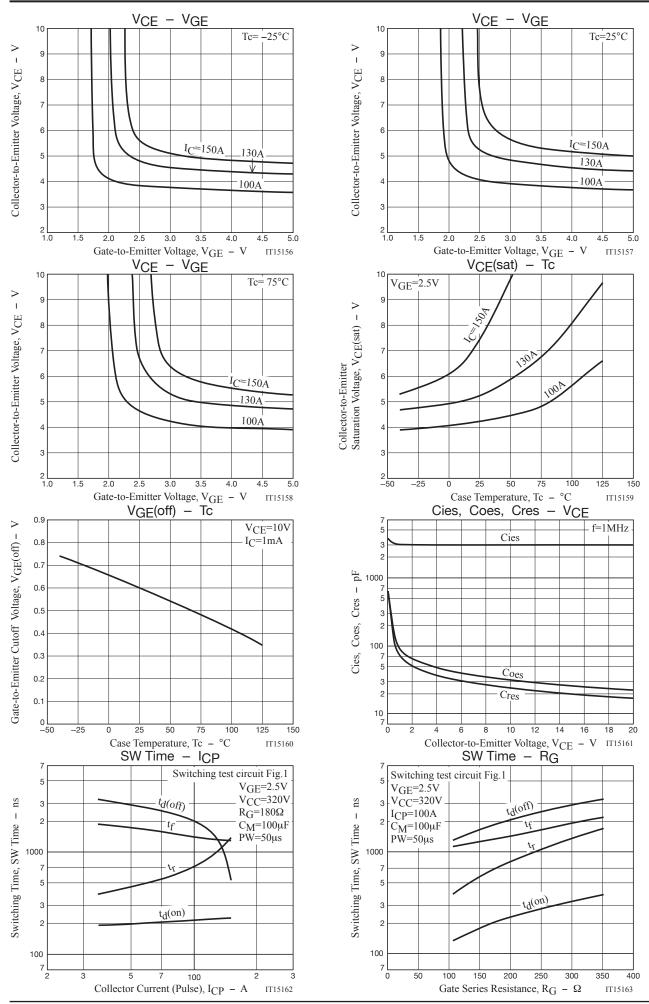
Note1. Gate Series Resistance  $R_G \ge 160\Omega$  is recommended for protection purpose at the time of turn OFF. However, if  $dv / dt \le 400V / \mu s$  is satisfied at customer's actual set evaluation,  $R_G < 160\Omega$  can also be used. Note2. The collector voltage gradient dv / dt must be smaller than  $400V / \mu s$  to protect the device when it is turned off.

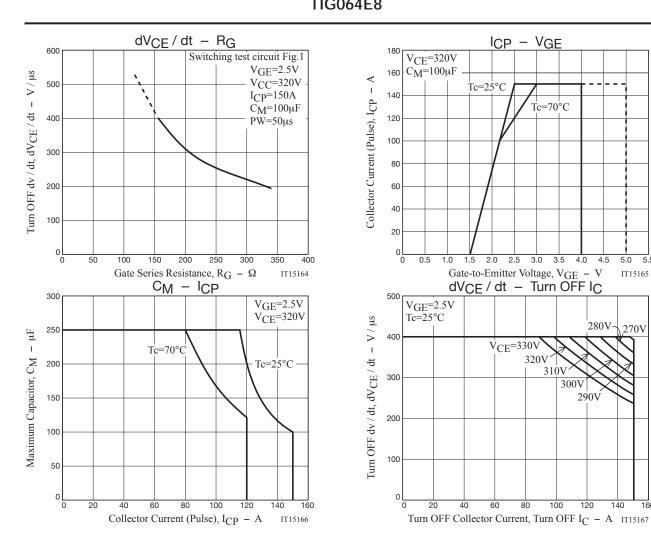
#### **Ordering Information**

Device Package		Shipping	memo	
TIG064E8-TL-H	ECH8	3,000pcs./reel	Pb Free and Halogen Free	









5.0

IT15165

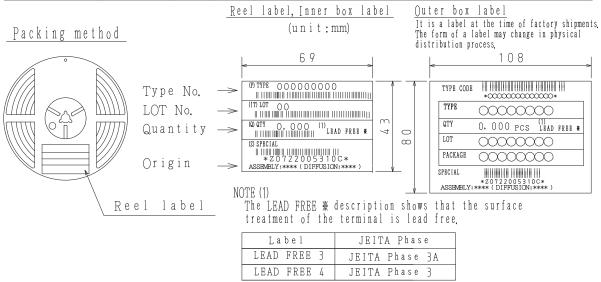
270V

#### **Embossed Taping Specification**

#### TIG064E8-TL-H

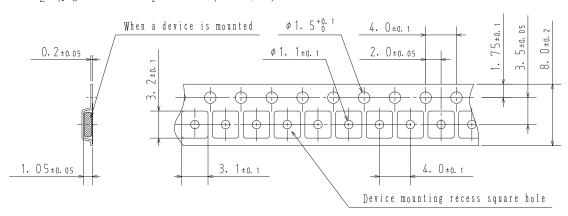
#### 1. Packing Format

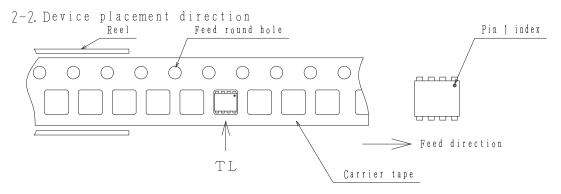
Package Name	Carrier Tape	Maximum Number of devices contained (pcs)			Packing	format
	Туре	Reel	Inner box	Outer box	Inner $BOX(C-1)$	Outer BOX (A-7)
ECH8	СРН6	3, 000	15, 000	90,000	5 reels contained	6 inner boxes contained
					Dimensions:mm (external)	Dimensions:mm (external)
					183×72×185	440×195×210



### 2. Taping configuration

2-1. Carrier tape size (unit:mm)





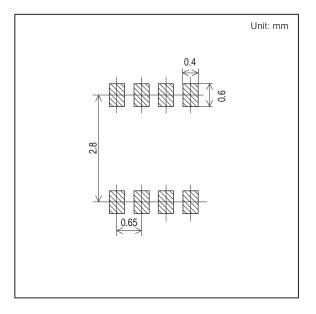
Those with pin 1 index on the feed hole side · · · · · TL

# **Outline Drawing**

TIG064E8-TL-H

# Mass (g) Unit 0.02 \* For reference mm 0. 15<sup>+0. 1</sup><sub>-0. 05</sub> 0. 25±0.06 2. 9±0.06 0~0.02 2. 8±0. 05 2. 3±0.06 LOT No. 0. 25±0.06 0. 3<sup>+0. 1</sup> PIN#1 0. 9±0. 05 0.05 \$ \$

# Land Pattern Example



Note: TIG064E8 has protection diode between gate and emitter but handling it requires sufficient care to be taken.

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