

Note: This datasheet may be out of date. Please download the latest datasheet of CSTCE14M7V51-R0 from the official website of Murata Manufacturing Co., Ltd.

http://www.murata.com/en/products/productdetail?partno=CSTCE14M7V51-R0

CSTCE14M7V51-R0

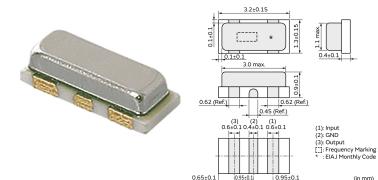








Appearance & Shape





Features

MURATA's package technology expertise has enabled the development of the Chip CERALOCK(R) with built-in load capacitors.

High-density mounting is made possible by the small package and the elimination of the need for an external load capacitor.

Features

- 1. Oscillation circuits do not require external load capacitors.
- 2. Available for a wide frequency range.
- 3. Extremely small and have a low profile.
- 4. No adjustment is necessary for oscillation circuits.



Applications

Other Usage Consumer/Industrial	Other L
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Packaging Information

Packaging	Specifications	Standard Packing Quantity
R0	180mm Embossed Tape	3000

1 of 2

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- 2. This datasheet has only typical specifications because there is no space for detailed specifications
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Product Search Data Sheet

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CSTCE14M7V51-R0



Specifications

Product Type	Ceramic Resonator	
Floudict Type	(CERALOCK)	
Series	CSTCE_V	
Frequency	14.746MHz	
Frequency Tolerance	+/-0.50% max.	
Operating Temperature	-20°C~80°C	
Range		
Frequency Shift by	+/-0.30% max.	
Temperature	, , , , , , , , , , , , , , , , , , , ,	
Frequency Aging	+/-0.20% max.	
Resonant Impedance (R1)	40ohm max.	
Built-in Load Capacitance	555	
(CL1/CL2)	5pF	
Shape	SMD	
Wash	Not available	
L x W (size)	3.2x1.3mm	
Mass	17mg	

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