

# **MOSFET** - Power, N-Channel

## 20 V, 14 A, 6.8 m $\Omega$ , Single ECH8

## **ECH8420**

## **Features**

- $R_{DS}(on)1 = 5.2 \text{ m}\Omega \text{ (Typ.)}$ • ON-resistance
- 1.8 V Drive
- Protection Diode in
- This Device is Pb-Free and Halide Free

#### **Package Dimension**

Unit: mm (typ) 7011A-002 ECH8420-TL-H **TopView** 0 to 0.02 

Figure 1. Package Dimensions

**Bottom View** 

ECH8

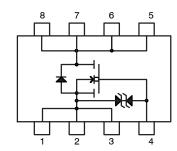


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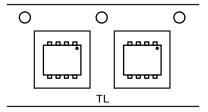
#### **MARKING DIAGRAM**



## **ELECTRICAL CONNECTION**



#### **PACKING TYPE: TL**



#### **ORDERING INFORMATION**

Device	Package	Shipping <sup>†</sup>
ECH8420-TL-H	SOT-28FL / ECH8	3000 /
	(Pb-Free,	Tape & Reel
	Halide Free)	

<sup>†</sup>For information on tape and reel specifications, including part orientation and tape sizes, please refer to our Tape and Reel Packaging Specification Brochure, BRD8011/D.

## **Specifications**

## **ABSOLUTE MAXIMUM RATINGS** at $T_A = 25^{\circ}C$

Parameter	Symbol	Conditions	Ratings	Unit
Drain-to-Source Voltage	$V_{DSS}$		20	V
Gate-to-Source Voltage	$V_{GSS}$		±12	V
Drain Current (DC)	I <sub>D</sub>		14	Α
Drain Current (Pulse)	I <sub>DP</sub>	PW ≤ 10 μs, duty cycle ≤ 1%	50	Α
Allowable Power Dissipation	$P_{D}$	When mounted on ceramic substrate (900 mm <sup>2</sup> × 0.8 mm)	1.6	W
Channel Temperature	T <sub>ch</sub>		150	°C
Storage Temperature	T <sub>stg</sub>		-55 to +150	°C

Stresses exceeding those listed in the Maximum Ratings table may damage the device. If any of these limits are exceeded, device functionality should not be assumed, damage may occur and reliability may be affected.

## **ELECTRICAL CHARACTERISTICS** at $T_A = 25$ °C

				Ratings		
Parameter	Symbol	Conditions	Min	Тур	Max	Unit
Drain-to-Source Breakdown Voltage	V <sub>(BR)DSS</sub>	I <sub>D</sub> = 1 mA, V <sub>GS</sub> = 0 V	20	-	_	V
Zero-Gate Voltage Drain Current	I <sub>DSS</sub>	V <sub>DS</sub> = 20 V, V <sub>GS</sub> = 0V	-	-	1	μΑ
Gate-to-Source Leakage Current	I <sub>GSS</sub>	$V_{GS} = \pm 8 \text{ V}, V_{DS} = 0 \text{ V}$	-	-	±10	μΑ
Cutoff Voltage	V <sub>GS</sub> (off)	V <sub>DS</sub> = 10 V, I <sub>D</sub> = 1 mA	0.4	-	1.3	V
Forward Transfer Admittance	yfs	V <sub>DS</sub> = 10 V, I <sub>D</sub> = 7 A	-	14.5	_	S
Static Drain-to-Source On-State	R <sub>DS</sub> (on)1	I <sub>D</sub> = 7 A, V <sub>GS</sub> = 4.5 V	-	5.2	6.8	mΩ
Resistance	R <sub>DS</sub> (on)2	I <sub>D</sub> = 4 A, V <sub>GS</sub> = 2.5 V	-	8	11.5	mΩ
	R <sub>DS</sub> (on)3	I <sub>D</sub> = 2 A, V <sub>GS</sub> = 1.8 V	-	15	22.5	mΩ
Input Capacitance	Ciss	V <sub>DS</sub> = 10 V, f = 1 MHz	-	2430	_	pF
Output Capacitance	Coss	1	-	410	_	pF
Reverse Transfer Capacitance	Crss	1	-	330	_	pF
Turn-ON Delay Time	t <sub>d</sub> (on)	See specified Test Circuit.	-	21	_	ns
Rise Time	t <sub>r</sub>	1	-	88	_	ns
Turn-OFF Delay Time	t <sub>d</sub> (off)	1	-	210	_	ns
Fall Time	t <sub>f</sub>	1	_	115	_	ns
Total Gate Charge	Qg	V <sub>DS</sub> = 10 V, V <sub>GS</sub> = 4.5 V,	-	29	_	nC
Gate-to-Source Charge	Qgs	I <sub>D</sub> = 14 A	-	4.8	-	nC
Gate-to-Drain "Miller" Charge	Qgd	1	-	8.7	_	nC
Diode Forward Voltage	V <sub>SD</sub>	I <sub>S</sub> = 14 A, V <sub>GS</sub> = 0 V	-	0.75	1.2	V

Product parametric performance is indicated in the Electrical Characteristics for the listed test conditions, unless otherwise noted. Product performance may not be indicated by the Electrical Characteristics if operated under different conditions.

## **Switching Time Test Circuit**

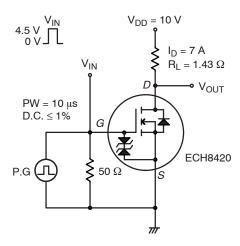
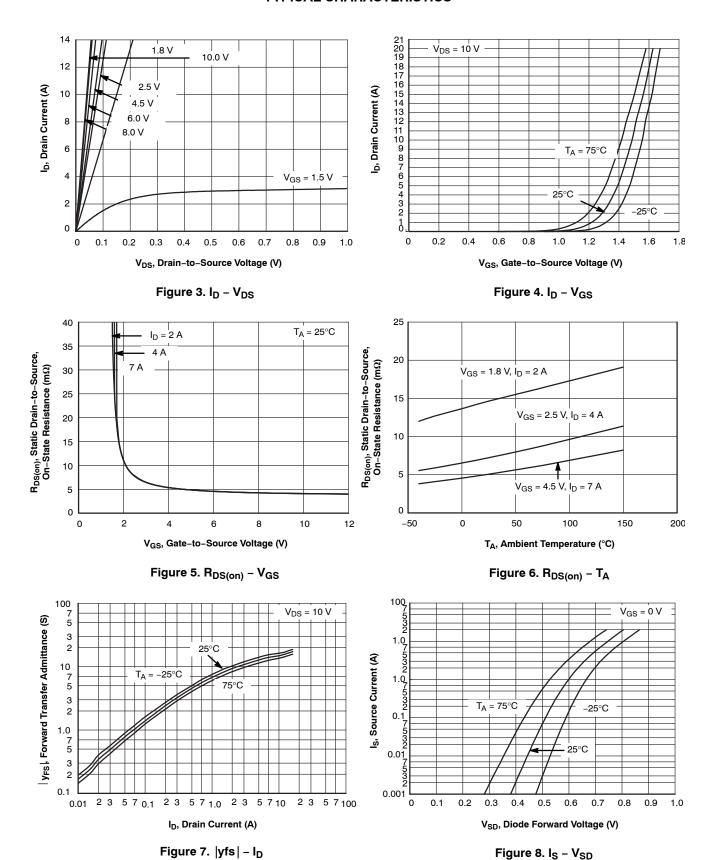


Figure 2. Switching Time Test Circuit

#### **TYPICAL CHARACTERISTICS**



## TYPICAL CHARACTERISTICS (continued)

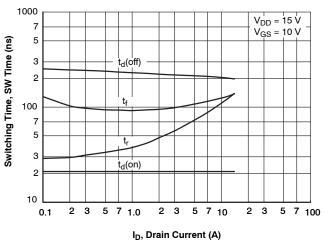


Figure 9. SW Time - I<sub>D</sub>

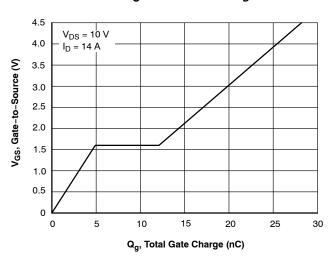


Figure 11. V<sub>GS</sub> - Qg

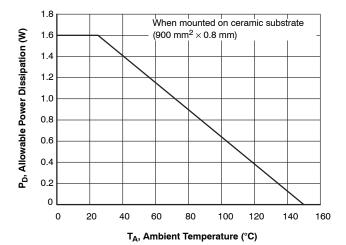


Figure 13. P<sub>D</sub> - T<sub>A</sub>

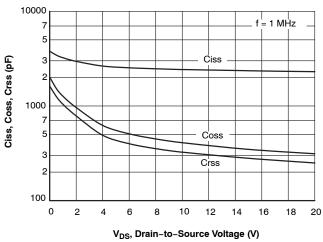


Figure 10. Ciss, Coss, Crss - V<sub>DS</sub>

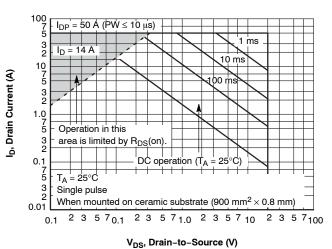
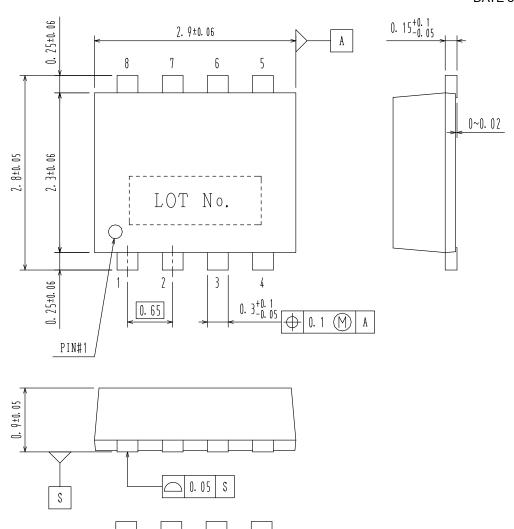


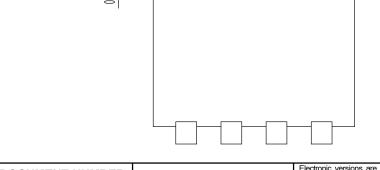
Figure 12. ASO



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