

# ARF446G ARF447G

\*G Donotes RoHS Compliant, Pb Free Terminal Finish

# **RF POWER MOSFETs**

## N-CHANNEL ENHANCEMENT MODE

250V 140W 65MHz

The ARF446 and ARF447 comprise a symmetric pair of common source RF power transistors designed for push-pull scientific, commercial, medical and industrial RF power amplifier applications up to 65MHz.

• Specified 250 Volt, 40.68 MHz Characteristics:

Output Power = 140 Watts.

Gain = 15dB (Class C)

Efficiency = 75%

- Low Cost Common Source RF Package.
- Very High Breakdown for Improved Ruggedness.
- Low Thermal Resistance.
- Nitride Passivated Die for Improved Reliability.

#### **MAXIMUM RATINGS**

All Ratings: T<sub>C</sub> = 25°C unless otherwise specified.

Symbol	Parameter	ARF446G/447G	UNIT	
V <sub>DSS</sub>	Drain-Source Voltage	900	Volts	
$V_{DGO}$	Drain-Gate Voltage	900	Volts	
I <sub>D</sub>	Continuous Drain Current @ T <sub>C</sub> = 25°C	6.5	Amps	
V <sub>GS</sub>	Gate-Source Voltage	±30	Volts	
P <sub>D</sub>	Total Power Dissipation @ T <sub>C</sub> = 25°C	230	Watts	
R <sub>0JC</sub>	Junction to Case	0.55	°C/W	
T <sub>J</sub> ,T <sub>STG</sub>	Operating and Storage Junction Temperature Range	-55 to 150	- °C	
T <sub>L</sub>	Lead Temperature: 0.063" from Case for 10 Sec.	300		

## STATIC ELECTRICAL CHARACTERISTICS

Symbol	Characteristic / Test Conditions	MIN	ТҮР	MAX	UNIT	
BV <sub>DSS</sub>	Drain-Source Breakdown Voltage ( $V_{GS} = 0V, I_D = 250 \mu A$ )	900		Volts		
V <sub>DS</sub> (ON)	On State Drain Voltage (1) $(I_D(ON) = 3.5A, V_{GS} = 10V)$			7		
	Zero Gate Voltage Drain Current (V <sub>DS</sub> = V <sub>DSS</sub> , V <sub>GS</sub> = 0V)			25		
DSS	Zero Gate Voltage Drain Current (V <sub>DS</sub> = 0.8 V <sub>DSS</sub> , V <sub>GS</sub> = 0V, T <sub>C</sub> = 125°C)			250	μΑ	
l <sub>GSS</sub>	Gate-Source Leakage Current (V <sub>GS</sub> = ±30V, V <sub>DS</sub> = 0V)			±100	nA	
9 <sub>fs</sub>	Forward Transconductance $(V_{DS} = 25V, I_{D} = 3.5A)$	4	5.7		mhos	
V <sub>GS</sub> (TH)	Gate Threshold Voltage $(V_{DS} = V_{GS}, I_{D} = 50 \text{mA})$	2		5	Volts	

CAUTION: These Devices are Sensitive to Electrostatic Discharge. Proper Handling Procedures Should Be Followed.

Microsemi Website - http://www.microsemi.com

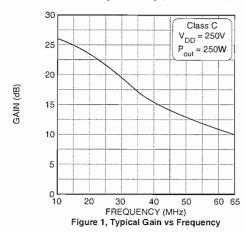
Symbol	Characteristic	Test Conditions	MIN	TYP	мах	UNIT
C <sub>iss</sub>	Input Capacitance	V <sub>GS</sub> = 0V		1500	1800	
C <sub>oss</sub>	Output Capacitance	V <sub>DS</sub> = 300V		70	130	рF
C <sub>rss</sub>	Reverse Transfer Capacitance	f = 1 MHz		27	50	
t <sub>d(on)</sub>	Turn-on Delay Time	V <sub>GS</sub> = 15V		7	15	
t <sub>r</sub>	Rise Time	$V_{DD} = 0.5 V_{DSS}$		5	10	ns
t <sub>d(off)</sub>	Turn-off Delay Time	$I_{D} = I_{D[Cont.]} @ 25^{\circ}C$		23	40	115
t <sub>f</sub>	Fall Time	$R_{G} = 1.6\Omega$		12	25	

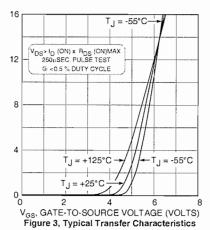
## **FUNCTIONAL CHARACTERISTICS**

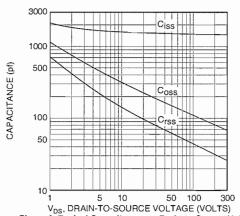
Symbol	Characteristic	Test Conditions	MIN	TYP	МАХ	UNIT	
G <sub>PS</sub>	Common Source Amplifier Power Gain	f = 27.12 MHz		20		dB	
η	Drain Efficiency	$V_{GS} = 0V$ $V_{DD} = 300V$		80		%	
ψ	Electrical Ruggedness VSWR 20:1	P <sub>out</sub> = 140W	No Degradation in Output Power				
G <sub>PS</sub>	Common Source Amplifier Power Gain f = 40.68 MHz		13	15		dB	
η	Drain Efficien <b>c</b> y	$V_{GS} = 0V$ $V_{DD} = 250V$	70	75		%	
Ψ	Electrical Ruggedness VSWR 20:1	P <sub>out</sub> = 140W	No Deg	egradation in Output Power			

① Pulse Test: Pulse width < 380 n6, Duty Cycle < 2%

Microsemi Reserves the right to change, without notice, the specifications and information contained herein.







V<sub>DS</sub>, DRAIN-TO-SOURCE VOLTAGE (VOLTS)
Figure 2, Typical Capacitance vs. Drain-to-Source Voltage

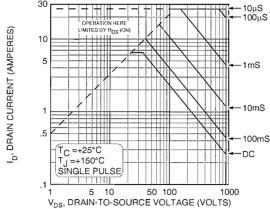
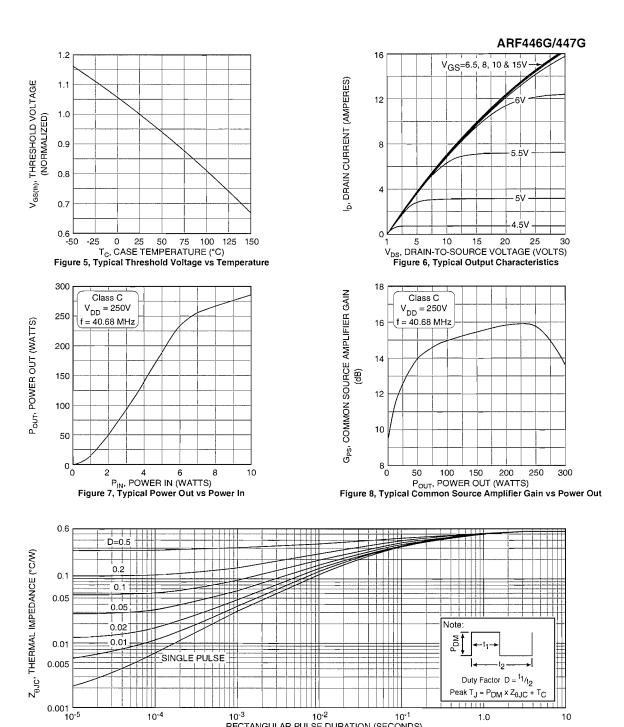


Figure 4, Typical Maximum Safe Operating Area

<sub>ID</sub>, DRAIN CURRENT (AMPERES)



10<sup>-4</sup> 10<sup>-3</sup> 10<sup>-2</sup> 10<sup>-1</sup> 1.0

RECTANGULAR PULSE DURATION (SECONDS)

Figure 9, Typical Maximum Effective Transient Thermal Impedance, Junction-To-Case vs Pulse Duration

Table 1 - Typical Class C Large Signal Input-Output Impedance

Freq. (MHz)	<b>Z</b> <sub>in</sub> (Ω)	<b>Z</b> <sub>OL</sub> (Ω)
2.0	20.40 - j 9.6	142.0 - j 20
13.5	2.10 - j 6.4	73.0 - j 71
27.0	0.50 - j 2.3	30.0 - j 57
40.0	0.30 - j 0.4	15.0 - j 42
65.0	0.46 + j 2.0	6.2 - j 25

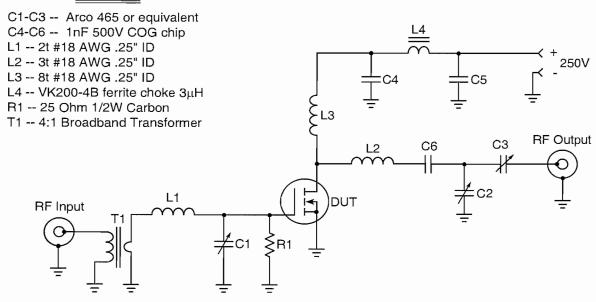
 $Z_{\text{in}}$  - gate shunted by  $25\Omega$ 

Z<sub>OL</sub> - conjugate of optimum load impedance for 250W at 250V

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#### 40.68 MHz Test Circuit

## Parts List



### TO-247 Package Outline

@1 SAC: Tin, Silver, Copper

