NPN/NPN general purpose double transistors

October 2025 Product data sheet

1. General description

NPN/NPN general-purpose double transistors in an SOT457 (SC-74) plastic package.

PNP/PNP complement: BC807DS NPN/PNP complement: BC817DPN

2. Features and benefits

Reduces component count

Reduces pick and place costs

3. Applications

· General purpose switching and amplification

4. Quick reference data

Table 1. Quick reference data

| Symbol | Parameter | Conditions | | Min | Тур | Max | Unit |
|------------------|---------------------------|--|-----|-----|-----|-----|------|
| Per transistor | | | | | | | |
| V _{CEO} | collector-emitter voltage | open base | | - | - | 45 | V |
| I _C | collector current | | | - | - | 500 | mA |
| I _{CM} | peak collector current | single pulse; t _p ≤ 1 ms | | - | - | 1 | Α |
| h _{FE} | DC current gain | V _{CE} = 1 V; I _C = 100 mA | [1] | 160 | - | 400 | |

[1] Pulsed test: $t_p \le 300 \mu s$; $\delta \le 0.02$

5. Pinning information

Table 2. Pinning information

| Pin | Symbol | Description | Simplified outline | Graphic symbol |
|-----|--------|---------------|--------------------|----------------|
| 1 | E1 | emitter TR1 | | C1 B2 E2 |
| 2 | B1 | base TR1 | <u> </u> | |
| 3 | C2 | collector TR2 | | (TR1 TR2) |
| 4 | E2 | emitter TR2 | | |
| 5 | B2 | base TR2 | TSOP6 (SOT457) | |
| 6 | C1 | collector TR1 | | sym020 |



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6. Ordering information

Table 3. Ordering information

| Type number | Package | ackage | | | |
|-------------|---------|--|---------|--|--|
| | Name | Description | Version | | |
| BC817DS | TSOP6 | plastic, surface-mounted package (SC-74; TSOP6); 6 leads | SOT457 | | |

7. Marking

Table 4. Marking codes

| Type number | Marking code |
|-------------|--------------|
| BC817DS | и3 |

8. Limiting values

Table 5. Limiting values

In accordance with the Absolute Maximum Rating System (IEC 60134).

| Symbol | Parameter | Conditions | | Min | Max | Unit |
|------------------|---------------------------|-------------------------------------|-----|-----|-----|------|
| Per transist | or | | | | | |
| V _{CBO} | collector-base voltage | open emitter | | - | 50 | V |
| V _{CEO} | collector-emitter voltage | open base | | - | 45 | V |
| V _{EBO} | emitter-base voltage | open collector | | - | 5 | V |
| I _C | collector current | | | - | 500 | mA |
| I _{CM} | peak collector current | single pulse; t _p ≤ 1 ms | | - | 1 | Α |
| I _{BM} | peak base current | | | - | 200 | mA |
| P _{tot} | total power dissipation | T _{amb} ≤ 25 °C | [1] | - | 370 | mW |
| Per device | | | | | | |
| P _{tot} | total power dissipation | T _{amb} ≤ 25 °C | [1] | - | 600 | mW |
| T _j | junction temperature | | | - | 150 | °C |
| T _{amb} | ambient temperature | | | -65 | 150 | °C |
| T _{stg} | storage temperature | | | -65 | 150 | °C |

^[1] Device mounted on an FR4 Printed-Circuit Board (PCB); single-sided copper; tin plated; mounting pad for collector 1 cm².

9. Thermal characteristics

Table 6. Thermal characteristics

| Symbol | Parameter | Conditions | | Min | Тур | Max | Unit |
|----------------------|---|-------------|-----|-----|-----|-----|------|
| Per device | | | | | | | |
| R _{th(j-a)} | thermal resistance from junction to ambient | in free air | [1] | - | - | 208 | K/W |

[1] Device mounted on an FR4 PCB; single-sided copper; tin-plated; mounting pad for collector 1 cm ².

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10. Characteristics

Table 7. Characteristics

 T_{amb} = 25 °C unless otherwise specified.

| Symbol | Parameter | Conditions | | Min | Тур | Max | Unit |
|--------------------|--------------------------------------|---|---------|-----|-----|-----|------|
| Per transist | tor | | | | | | |
| I _{CBO} | collector-base cut-off | V _{CB} = 20 V; I _E = 0 A | | - | - | 100 | nA |
| | current | V _{CB} = 20 V; I _E = 0 A; T _j = 150 °C | | - | - | 5 | μΑ |
| I _{EBO} | emitter-base cut-off current | V _{EB} = 5 V; I _C = 0 A | | - | - | 100 | nA |
| h _{FE} | DC current gain | V _{CE} = 1 V; I _C = 100 mA | [1] | 160 | - | 400 | |
| | | V _{CE} = 1 V; I _C = 500 mA | [1] | 40 | - | - | |
| V _{CEsat} | collector-emitter saturation voltage | I _C = 500 mA; I _B = 50 mA | [1] | - | - | 700 | mV |
| V _{BE} | base-emitter voltage | V _{CE} = 1 V; I _C = 500 mA | [1] [2] | - | - | 1.2 | V |
| C _c | collector capacitance | V _{CB} = 10 V; I _E = 0 A; i _e = 0 A; f = 1 MHz | | - | 5 | - | pF |
| f _T | transition frequency | V _{CE} = 5 V; I _C = 10 mA; f = 100 MHz | | 100 | - | - | MHz |

- 1] Pulsed test: $t_p \le 300 \,\mu\text{s}$; $\delta \le 0.02$
- [2] V_{BE} decreases by approximately -2 mV/k with increasing temperature.

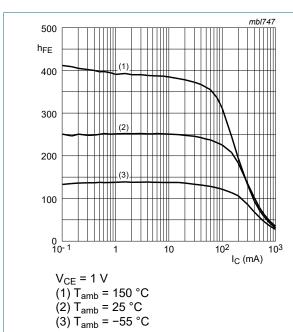


Fig. 1. DC current gain as a function of collector current; typical values

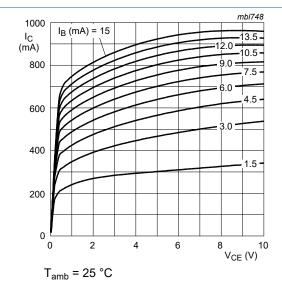
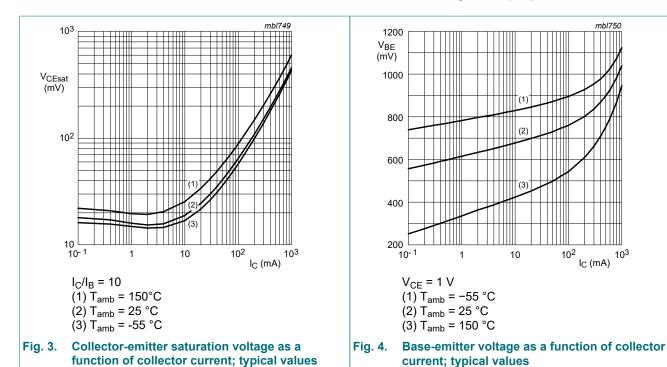
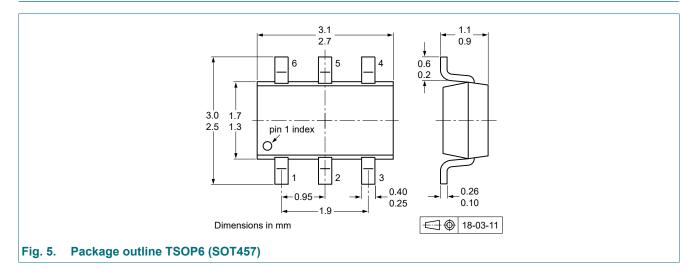


Fig. 2. Collector current as a function of collectoremitter voltage; typical values

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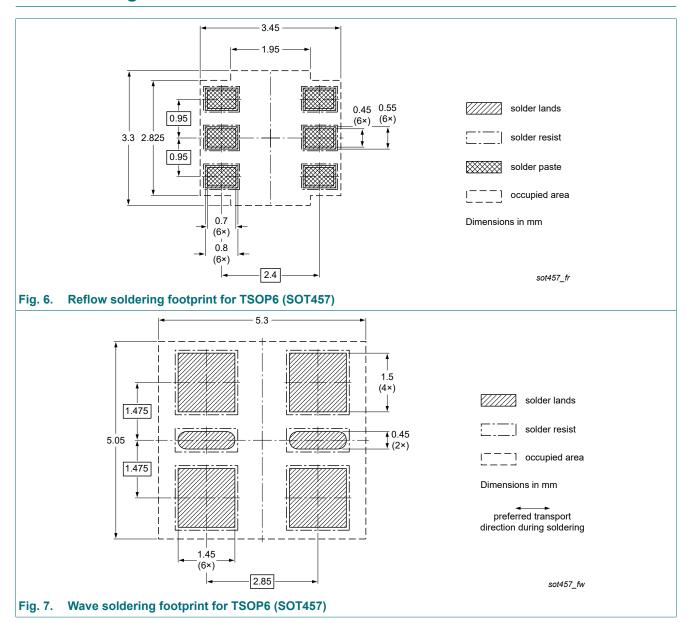


11. Package outline



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12. Soldering



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13. Revision history

Table 8. Revision history

| Table of Revision metery | | | | | | | |
|--------------------------|--------------|--|---------------|-------------|--|--|--|
| Data sheet ID | Release date | Data sheet status | Change notice | Supersedes | | | |
| BC817DS v.4 | 20251001 | Product data sheet | - | BC817DS v.3 | | | |
| Modifications: | | Product(s) changed to non-automotive qualification. Please refer to nexperia.com for automotive (-Q) product alternative(s). | | | | | |
| BC817DS v.3 | 20190625 | Product data sheet | - | BC817DS v.2 | | | |
| BC817DS v.2 | 20021122 | Product data sheet | - | BC817DS v.1 | | | |
| BC817DS v.1 | 20020809 | Product data sheet | - | - | | | |
| | | | | | | | |

14. Legal information

Data sheet status

| Document status [1][2] | Product status [3] | Definition |
|--------------------------------|-----------------------|---|
| Objective [short] data sheet | Development | This document contains data from the objective specification for product development. |
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| Product [short] data sheet | Production | This document contains the product specification. |

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