

Photocouplers Photorelay

TLP171D

1. Applications

- · Mechanical relay replacements
- · Security Systems
- · Modem/Fax PC Cards
- Private Branch Exchanges (PBXs)
- · Measuring Instruments

2. General

The TLP171D photorelay consists of a photo MOSFET optically coupled to an infrared LED. It is housed in a 4-pin package with 2.54-mm lead pitch and 2.1-mm height. This photorelay requires 0.2 mA of LED current to turn it on. It is suitable for applications that need electrical power savings.

3. Features

- (1) Package: SOP(2.54SOP4) (Height 2.1 mm, pitch 2.54 mm)
- (2) Normally opened (1-Form-A)
- (3) OFF-state output terminal voltage: 200 V (min)
- (4) Trigger LED current: 0.1 mA (max)(t≤1s)

0.2 mA (max)(t > 1 s)

- (5) ON-state current: 200 mA (max)
- (6) ON-state resistance: 8Ω (max)
- (7) Isolation voltage: 1500 Vrms (min)
- (8) Safety Standards

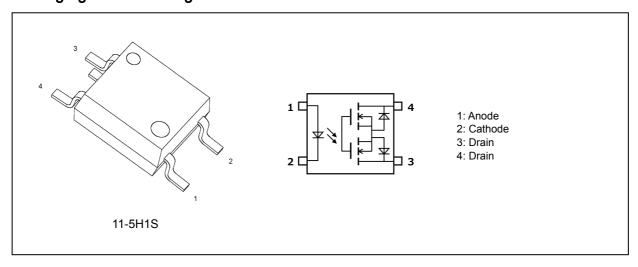
UL-recognized: UL 1577, File No.E67349

cUL-recognized: CSA Component Acceptance Service No.5A File No.E67349

VDE-approved: EN 60747-5-5 (Note 1)

Note 1: When a VDE approved type is needed, please designate the Option (V4).

4. Packaging and Pin Assignment



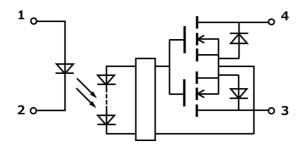
Start of commercial production

2012-10

Downloaded from Arrow.com.



5. Internal Circuit



6. Absolute Maximum Ratings (Note) (Unless otherwise specified, Ta = 25 °C)

| | Characteristics | Symbol | Note | Rating | Unit | |
|----------|-----------------------------------|---------------------------|-----------------------------------|----------|------------|-------|
| LED | Input forward current | | I _F | | 30 | mA |
| | Input forward current derating | (T _a ≥ 25 °C) | $\Delta I_F/\Delta T_a$ | | -0.3 | mA/°C |
| | Input forward current (pulsed) | (100 μs pulse, 100 pps) | I _{FP} | | 1 | Α |
| | Input reverse voltage | | V_R | | 5 | V |
| | Input power dissipation | | P_{D} | | 50 | mW |
| | Input power dissipation derating | $(T_a \ge 25 ^{\circ}C)$ | $\Delta P_D/\Delta T_a$ | | -0.5 | mW/°C |
| | Junction temperature | | Tj | | 125 | °C |
| Detector | OFF-state output terminal voltage | | V _{OFF} | | 200 | V |
| | ON-state current | | I _{ON} | | 200 | mA |
| | ON-state current derating | $(T_a \ge 25 ^{\circ}C)$ | Δl _{ON} /ΔT _a | | -2.0 | mA/°C |
| | ON-state current (pulsed) | (t = 100 ms) | I _{ONP} | | 600 | mA |
| | Output power dissipation | | Po | | 300 | mW |
| | Output power dissipation derating | $(T_a \ge 25 ^{\circ}C)$ | $\Delta P_{O}/\Delta T_{a}$ | | -3.0 | mW/°C |
| | Junction temperature | | Tj | | 125 | °C |
| Common | Storage temperature | | T _{stg} | | -55 to 125 | |
| | Operating temperature | | T _{opr} | | -40 to 85 | |
| | Lead soldering temperature | T _{sol} | | 260 | °C | |
| | Isolation voltage | AC, 60 s, R.H. ≤ 60 % | BV _S | (Note 1) | 1500 | Vrms |

Note: Using continuously under heavy loads (e.g. the application of high temperature/current/voltage and the significant change in temperature, etc.) may cause this product to decrease in the reliability significantly even if the operating conditions (i.e. operating temperature/current/voltage, etc.) are within the absolute maximum ratings.

Please design the appropriate reliability upon reviewing the Toshiba Semiconductor Reliability Handbook ("Handling Precautions"/"Derating Concept and Methods") and individual reliability data (i.e. reliability test report and estimated failure rate, etc).

Note 1: This device is considered as a two-terminal device: Pins 1 and 2 are shorted together, and pins 3 and 4 are shorted together.

7. Recommended Operating Conditions (Note)

| Characteristics | Symbol | Note | Min | Тур. | Max | Unit |
|-----------------------|------------------|------|-----|------|-----|------|
| Supply voltage | V_{DD} | | _ | _ | 160 | ٧ |
| Input forward current | I _F | | _ | 0.5 | 25 | mA |
| ON-state current | I _{ON} | | _ | _ | 160 | mA |
| Operating temperature | T _{opr} | | -20 | _ | 65 | Ç |

Note: The recommended operating conditions are given as a design guide necessary to obtain the intended performance of the device. Each parameter is an independent value. When creating a system design using this device, the electrical characteristics specified in this data sheet should also be considered.



8. Electrical Characteristics (Unless otherwise specified, Ta = 25 °C)

| | Characteristics | Symbol | Note | Test Condition | Min | Тур. | Max | Unit |
|----------|-----------------------|------------------|------|--------------------------|-----|------|------|------|
| LED | Input forward voltage | V _F | | I _F = 10 mA | 1.1 | 1.27 | 1.4 | V |
| | Input reverse current | I _R | | V _R = 5 V | | | 10 | μΑ |
| | Input capacitance | Ct | | V = 0 V, f = 1 MHz | | 30 | _ | pF |
| Detector | OFF-state current | I _{OFF} | | V _{OFF} = 200 V | | 1 | 1000 | nA |
| | Output capacitance | C _{OFF} | | V = 0 V, f = 1 MHz | | 90 | _ | pF |

9. Coupled Electrical Characteristics (Unless otherwise specified, Ta = 25 °C)

| Characteristics | Symbol | Note | Test Condition | Min | Тур. | Max | Unit |
|---------------------|-----------------|------|--|-----|-------|-----|------|
| Trigger LED current | I _{FT} | | I _{ON} = 200 mA, t≤1s | - | 0.02 | 0.1 | mA |
| | | | I _{ON} = 200 mA, t>1s | | _ | 0.2 | mA |
| Return LED current | I _{FC} | | I _{OFF} = 100 μA | I | 0.001 | | mA |
| ON-state resistance | R _{ON} | | I_{ON} = 200 mA, I_F = 0.5 mA, t < 1 s | | 5 | 8 | Ω |

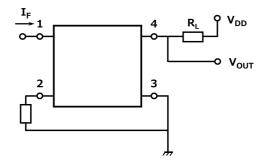
10. Isolation Characteristics (Unless otherwise specified, T_a = 25 °C)

| Characteristics | Symbol | Note | Test Condition | Min | Тур. | Max | Unit |
|-------------------------------------|----------------|----------|---------------------------------|----------------------|------|-----|------|
| Total capacitance (input to output) | Cs | (Note 1) | V _S = 0 V, f = 1 MHz | | 8.0 | | pF |
| Isolation resistance | R _S | (Note 1) | V_S = 500 V, R.H. \leq 60 % | 5 × 10 ¹⁰ | 1014 | _ | Ω |
| Isolation voltage | BVS | (Note 1) | AC, 60 s | 1500 | _ | _ | Vrms |

Note 1: This device is considered as a two-terminal device: Pins 1 and 2 are shorted together, and pins 3 and 4 are shorted together.

11. Switching Characteristics (Unless otherwise specified, T_a = 25 °C)

| Characteristics | Symbol | Note | Test Condition | Min | Тур. | Max | Unit |
|-----------------|------------------|------|---|-----|------|-----|------|
| Turn-on time | t _{ON} | | See Fig. 11.1. $R_L = 200 \Omega$, $V_{DD} = 20 V$, $I_F = 0.5 mA$ | _ | 3.5 | 10 | ms |
| | | | See Fig. 11.1. R _L = 200 Ω , V _{DD} = 20 V, I _F = 1.0 mA | _ | 1.5 | 5 | |
| Turn-off time | t _{OFF} | | See Fig. 11.1. R _L = 200 Ω , V _{DD} = 20 V, I _F = 0.5 mA | _ | 1 | 5 | |
| | | | See Fig. 11.1. R _L = 200 Ω , V _{DD} = 20 V, I _F = 1.0 mA | _ | 1 | 5 | |



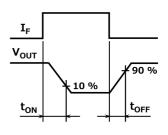


Fig. 11.1 Switching Time Test Circuit and Waveform



12. Characteristics Curves

12.1. Characteristics Curves (Note)

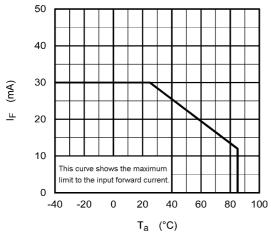


Fig. 12.1.1 I_F - T_a

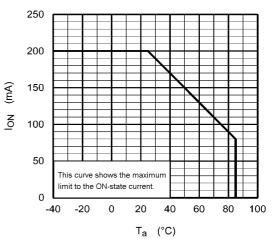


Fig. 12.1.2 I_{ON} - T_a

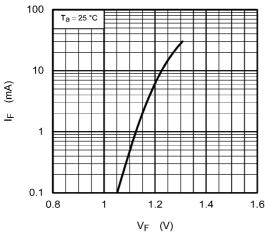


Fig. 12.1.3 I_F - V_F

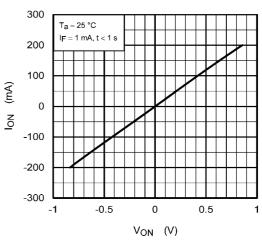


Fig. 12.1.4 I_{ON} - V_{ON}

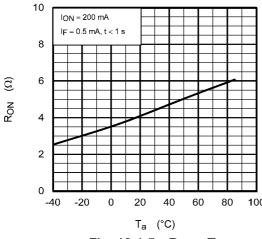


Fig. 12.1.5 R_{ON} - T_a

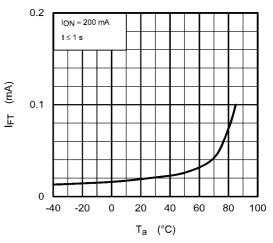


Fig. 12.1.6 I_{FT} - T_a

Rev.5.0



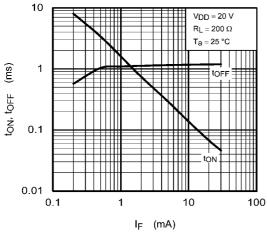


Fig. 12.1.7 t_{ON}, t_{OFF} - I_F

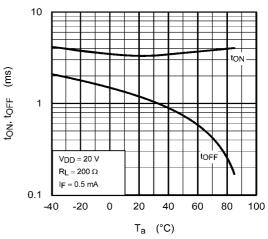


Fig. 12.1.8 t_{ON} , t_{OFF} - T_a

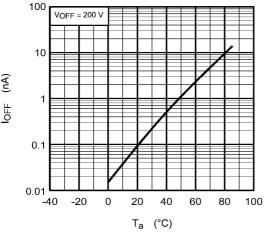


Fig. 12.1.9 I_{OFF} - T_a

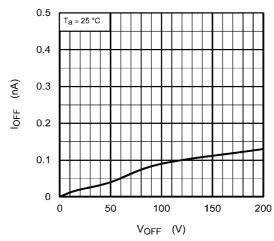


Fig. 12.1.10 I_{OFF} - V_{OFF}

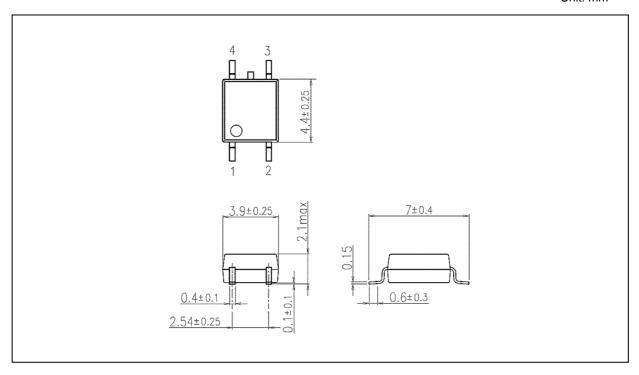
Note: The above characteristics curves are presented for reference only and not guaranteed by production test, unless otherwise noted.

Rev.5.0



Package Dimensions

Unit: mm



Weight: 0.1 g (typ.)

| | Package Name(s) |
|------------------|-----------------|
| TOSHIBA: 11-5H1S | |



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