

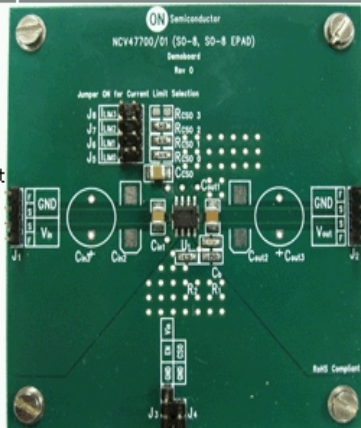


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## NV47701PDAJGEVB: 350 mA LDO Regulator Evaluation Board

### Evaluation/Development Tool Description

The NCV47701 is a 350 mA output current integrated low dropout regulator designed for use in harsh automotive environments. It includes wide operating temperature and input voltage ranges. The device is offered with adjustable voltage versions available in 3% output voltage accuracy. It has a high peak input voltage tolerance and reverse input voltage protection. It also provides overcurrent protection, overtemperature protection and enable for control of the state of the output voltage. The integrated current sense feature provides diagnosis and system protection functionality. The current limit of the device is adjustable by resistor connected to CSO pin. Voltage on CSO pin is proportional to output current.



### Evaluation/Development Tool Information

| Product         | Status | Compliance | Short Description                     | Parts Used      | Action                                                                                                 |
|-----------------|--------|------------|---------------------------------------|-----------------|--------------------------------------------------------------------------------------------------------|
| NV47701PDAJGEVB | Active | Pb-free    | 350 mA LDO Regulator Evaluation Board | NCV47701PDAJR2G | <a href="#">Buy Now</a><br><a href="#">» Contact Local Sales Office</a><br><a href="#">» Inventory</a> |

### Technical Documents

| Type                       | Document Title                                   | Document ID/Size                              | Rev |
|----------------------------|--------------------------------------------------|-----------------------------------------------|-----|
| Eval Board: BOM            | NV47701PDAJGEVB Bill of Materials ROHS Compliant | NV47701PDAJGEVB_BOM_ROHS.PDF - 59.0 KB        | 0   |
| Eval Board: Gerber         | NV47701PDAJGEVB Gerber Layout Files (Zip Format) | NV47701PDAJGEVB_GERBER.ZIP - 60.0 KB          | 0   |
| Eval Board: Schematic      | NV47701PDAJGEVB Schematic                        | NV47701PDAJGEVB_SCHEMATIC.PDF - 117.0 KB      | 0   |
| Eval Board: Test Procedure | NV47701PDAJGEVB Test Procedure                   | NV47701PDAJGEVB_TEST_PROCEDURE.PDF - 341.0 KB | 0   |

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