

# LDO Regulator - CMOS, Low Iq, Enable, TSOP-5

150 mA

## NCP699

The NCP699 series of fixed output LDO's are designed for handheld communication equipment and portable battery powered applications which require low quiescent current. The NCP699 series features a very low ground current of 40  $\mu$ A, independent of load current. Each device contains a voltage reference unit, an error amplifier, a PMOS power transistor, internal resistors for setting output voltage, current limit, and temperature limit protection circuits.

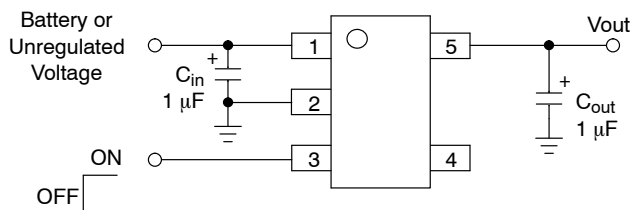
The NCP699 has been designed to be used with low cost capacitors. The device is housed in the micro-miniature TSOP-5 surface mount package. Standard voltage versions are 1.3, 1.4, 1.5, 1.8, 2.5, 2.8, 2.9, 3.0, 3.1, 3.3, 3.4, 4.5 and 5.0 V. Other voltages are available in 100 mV steps.

### Features

- Enable Control (Active High, Supports Sub 1 V Logic)
- Very Low Ground Current of 40  $\mu$ A Typical
- Low Dropout Voltage of 340 mV at 150 mA, and 3.0 V  $V_{out}$
- Multiple Fixed Output Voltage Option
- Output Voltage Accuracy of 2.0%
- Operating Temperature Range of  $-40^{\circ}\text{C}$  to  $85^{\circ}\text{C}$
- Stable with 1  $\mu$ F Ceramic or Tantalum Capacitors
- These are Pb-Free Devices

### Typical Applications

- Cellular Phones
- Battery Powered Consumer Products
- Hand-Held Instruments
- Camcorders and Cameras
- Printers and Office Equipment



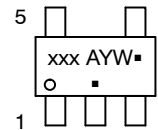
This device contains 86 active transistors

Figure 1. Typical Application Diagram



TSOP-5  
(SOT23-5, SC59-5)  
SN SUFFIX  
CASE 483

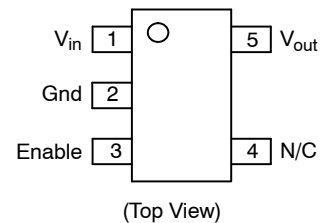
### MARKING DIAGRAM



xxx = Specific Device Code  
A = Assembly Location  
Y = Year  
W = Work Week  
▪ = Pb-Free Package

(Note: Microdot may be in either location)

### PIN CONNECTIONS



### ORDERING INFORMATION

See detailed ordering and shipping information on page 8 of this data sheet.

NOTE: Some of the devices on this data sheet have been **DISCONTINUED**. Please refer to the table on page 8.

## PIN FUNCTION DESCRIPTION

| Pin No. | Pin Name         | Description                                                                                                                                                                                         |
|---------|------------------|-----------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------|
| 1       | V <sub>in</sub>  | Positive power supply input voltage.                                                                                                                                                                |
| 2       | Gnd              | Power supply ground.                                                                                                                                                                                |
| 3       | Enable           | This input is used to place the device into low-power standby. When this input is pulled low, the device is disabled. If this function is not used, Enable should be connected to V <sub>in</sub> . |
| 4       | N/C              | No internal connection.                                                                                                                                                                             |
| 5       | V <sub>out</sub> | Regulated output voltage.                                                                                                                                                                           |

## MAXIMUM RATINGS

| Rating                         | Symbol              | Value                        | Unit |
|--------------------------------|---------------------|------------------------------|------|
| Input Voltage                  | V <sub>in</sub>     | 2.1 to 6.0                   | V    |
| Enable Voltage                 | Enable              | -0.3 to V <sub>in</sub> +0.3 | V    |
| Output Voltage                 | V <sub>out</sub>    | -0.3 to V <sub>in</sub> +0.3 | V    |
| Power Dissipation              | P <sub>D</sub>      | Internally Limited           | W    |
| Operating Junction Temperature | T <sub>J</sub>      | +150                         | °C   |
| Maximum Junction Temperature   | T <sub>J(max)</sub> | +150                         | °C   |
| Operating Ambient Temperature  | T <sub>A</sub>      | -40 to +85                   | °C   |
| Storage Temperature            | T <sub>stg</sub>    | -55 to +150                  | °C   |

Stresses exceeding those listed in the Maximum Ratings table may damage the device. If any of these limits are exceeded, device functionality should not be assumed, damage may occur and reliability may be affected.

- This device series contains ESD protection and exceeds the following tests:  
Human Body Model 2000 V per MIL-STD-883, Method 3015  
Machine Model Method 200 V
- Latch-up capability (85 °C) ±200 mA DC with trigger voltage.

## THERMAL CHARACTERISTICS

| Rating              | Symbol           | Test Conditions                            | Typical Value | Unit |
|---------------------|------------------|--------------------------------------------|---------------|------|
| Junction-to-Ambient | R <sub>θJA</sub> | 1 oz Copper Thickness, 100 mm <sup>2</sup> | 250           | °C/W |
| PSIJ-Lead 2         | Ψ <sub>JL2</sub> | 1 oz Copper Thickness, 100 mm <sup>2</sup> | 68            | °C/W |

NOTE: Single component mounted on an 80 x 80 x 1.5 mm FR4 PCB with stated copper head spreading area. Using the following boundary conditions as stated in EIA/JESD 51-1, 2, 3, 7, 12.



**ELECTRICAL CHARACTERISTICS** ( $V_{in} = V_{out(nom.)} + 1.0\text{ V}$ ,  $V_{enable} = V_{in}$ ,  $C_{in} = 1.0\text{ }\mu\text{F}$ ,  $C_{out} = 1.0\text{ }\mu\text{F}$ ,  $T_A = 25\text{ }^\circ\text{C}$ , unless otherwise noted.)

| Characteristic                                                                                                                                                                                                                     | Symbol             | Min                                                                                                               | Typ                                                                                     | Max                                                                                                               | Unit                  |
|------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------|--------------------|-------------------------------------------------------------------------------------------------------------------|-----------------------------------------------------------------------------------------|-------------------------------------------------------------------------------------------------------------------|-----------------------|
| Output Voltage ( $I_{out} = 10\text{ mA}$ , $T_A = -40\text{ }^\circ\text{C}$ to $85\text{ }^\circ\text{C}$ )<br>1.3 V<br>1.4 V<br>1.5 V<br>1.8 V<br>2.5 V<br>2.8 V<br>2.9 V<br>3.0 V<br>3.1 V<br>3.3 V<br>3.4 V<br>4.5 V<br>5.0 V | $V_{out}$          | 1.261<br>1.358<br>1.455<br>1.746<br>2.425<br>2.716<br>2.813<br>2.910<br>3.007<br>3.201<br>3.298<br>4.365<br>4.850 | 1.3<br>1.4<br>1.5<br>1.8<br>2.5<br>2.8<br>2.9<br>3.0<br>3.1<br>3.3<br>3.4<br>4.5<br>5.0 | 1.339<br>1.442<br>1.545<br>1.854<br>2.575<br>2.884<br>2.987<br>3.090<br>3.193<br>3.399<br>3.502<br>4.635<br>5.150 | V                     |
| Line Regulation ( $I_{out} = 10\text{ mA}$ )<br>1.3 V–4.4 V ( $V_{in} = V_{out(nom.)} + 1.0\text{ V}$ to $6.0\text{ V}$ )<br>4.5 V–5.0 V ( $V_{in} = 5.5\text{ V}$ to $6.0\text{ V}$ )                                             | $Reg_{line}$       | –<br>–                                                                                                            | 1.0<br>1.0                                                                              | 3.0<br>3.0                                                                                                        | mV/V                  |
| Load Regulation ( $I_{out} = 1.0\text{ mA}$ to $150\text{ mA}$ )                                                                                                                                                                   | $Reg_{load}$       | –                                                                                                                 | 0.3                                                                                     | 0.8                                                                                                               | mV/mA                 |
| Output Current Limit<br>1.3 V–3.9 V ( $V_{in} = V_{out(nom.)} + 2.0\text{ V}$ )<br>4.0 V–5.0 V ( $V_{in} = 6.0\text{ V}$ )                                                                                                         | $I_{o(nom.)}$      | 150<br>150                                                                                                        | 240<br>240                                                                              | –<br>–                                                                                                            | mA                    |
| Dropout Voltage ( $I_{out} = 150\text{ mA}$ ,<br>Measured at $V_{out} = V_{out(nom.)} - 3.0\%$ )<br>1.3 V<br>1.4 V<br>1.5 V<br>1.8 V<br>2.5 V<br>2.8 V<br>2.9 V<br>3.0 V<br>3.1 V<br>3.3 V<br>3.4 V<br>4.5 V – 5.0 V               | $V_{in} - V_{out}$ | –<br>–<br>–<br>–<br>–<br>–<br>–<br>–<br>–<br>–<br>–<br>–                                                          | 800<br>750<br>690<br>570<br>400<br>360<br>350<br>340<br>330<br>320<br>300<br>240        | 900<br>850<br>750<br>620<br>450<br>420<br>420<br>400<br>400<br>360<br>360<br>300                                  | mV                    |
| Disable Current ( $T_A = -40\text{ }^\circ\text{C}$ to $85\text{ }^\circ\text{C}$ )<br>(Enable Input = $0\text{ V}$ )                                                                                                              | DIS                | –                                                                                                                 | 0.03                                                                                    | 1.0                                                                                                               | $\mu\text{A}$         |
| Ground Current ( $T_A = -40\text{ }^\circ\text{C}$ to $85\text{ }^\circ\text{C}$ )<br>(Enable Input = $V_{in}$ , $I_{out} = 1.0\text{ mA}$ to $I_{o(nom.)}$ )                                                                      | $I_{GND}$          | –                                                                                                                 | 40                                                                                      | 90                                                                                                                | $\mu\text{A}$         |
| Output Short Circuit Current ( $V_{out} = 0\text{ V}$ )<br>1.3 V–3.9 V ( $V_{in} = V_{out(nom.)} + 2.0\text{ V}$ )<br>4.0 V–5.0 V ( $V_{in} = 6.0\text{ V}$ )                                                                      | $I_{out(max)}$     | 150<br>150                                                                                                        | 300<br>300                                                                              | 600<br>600                                                                                                        | mA                    |
| Output Voltage Noise ( $f = 100\text{ Hz}$ to $100\text{ kHz}$ )<br>$I_{out} = 30\text{ mA}$ , $C_{out} = 1\text{ }\mu\text{F}$                                                                                                    | $V_n$              | –                                                                                                                 | 100                                                                                     | –                                                                                                                 | $\mu\text{V}_{rms}$   |
| Ripple Rejection<br>( $f = 120\text{ Hz}$ , $15\text{ mA}$ )<br>( $f = 1.0\text{ kHz}$ , $15\text{ mA}$ )                                                                                                                          | RR                 | –<br>–                                                                                                            | 55<br>50                                                                                | –<br>–                                                                                                            | dB                    |
| Enable Input Threshold Voltage ( $T_A = -40\text{ }^\circ\text{C}$ to $85\text{ }^\circ\text{C}$ )<br>(Voltage Increasing, Output Turns On, Logic High)<br>(Voltage Decreasing, Output Turns Off, Logic Low)                       | $V_{th(en)}$       | 0.95<br>–                                                                                                         | –<br>–                                                                                  | –<br>0.3                                                                                                          | V                     |
| Output Voltage Temperature Coefficient                                                                                                                                                                                             | $T_C$              | –                                                                                                                 | $\pm 100$                                                                               | –                                                                                                                 | ppm/ $^\circ\text{C}$ |

Product parametric performance is indicated in the Electrical Characteristics for the listed test conditions, unless otherwise noted. Product performance may not be indicated by the Electrical Characteristics if operated under different conditions.

3. Maximum package power dissipation limits must be observed.

$$PD = \frac{T_{J(max)} - T_A}{R_{\theta JA}}$$

4. Low duty cycle pulse techniques are used during testing to maintain the junction temperature as close to ambient as possible.



TYPICAL CHARACTERISTICS

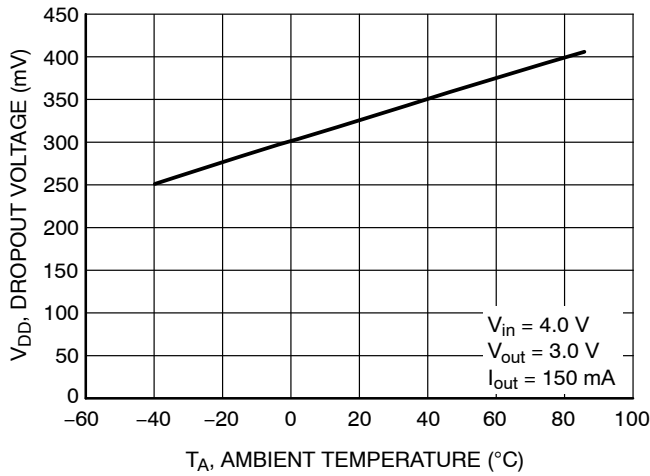


Figure 2. Dropout Voltage vs. Temperature

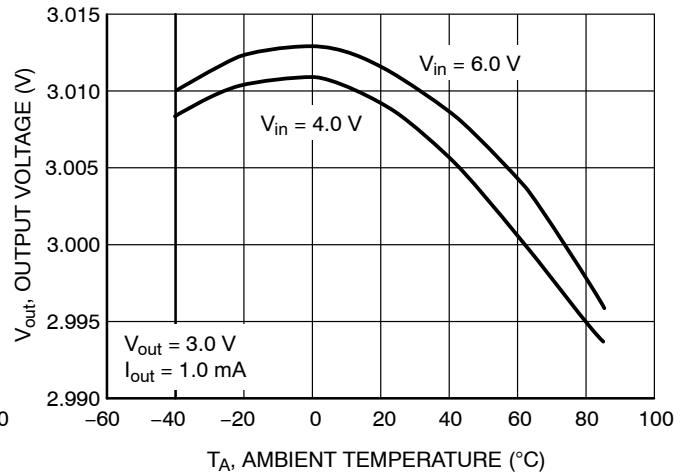


Figure 3. Output Voltage vs. Temperature

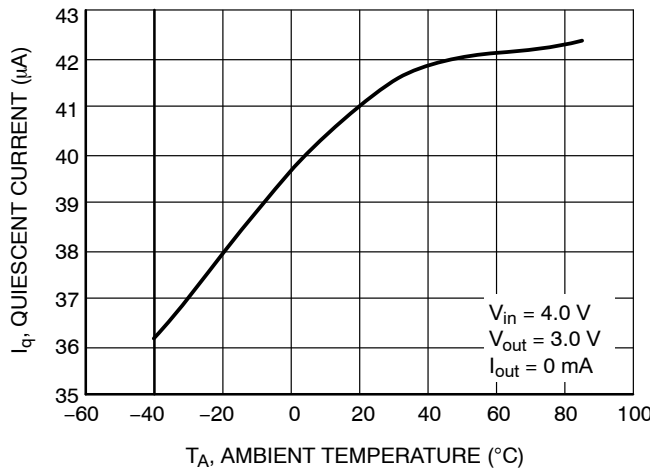


Figure 4. Quiescent Current vs. Temperature

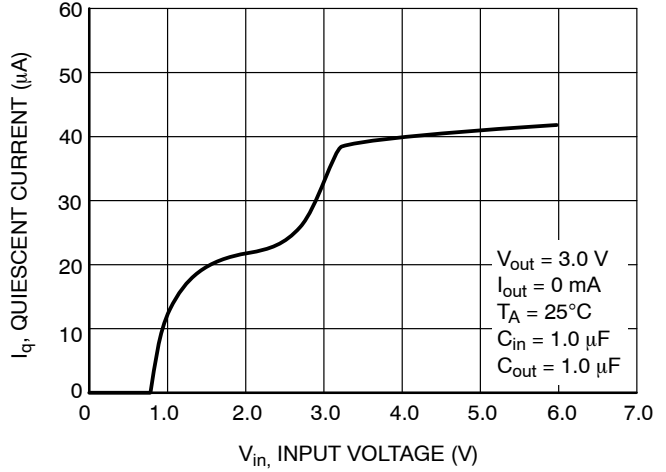


Figure 5. Quiescent Current vs. Input Voltage

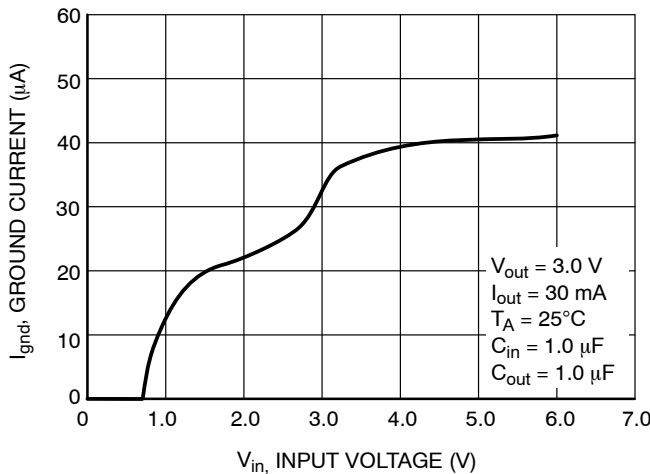


Figure 6. Ground Pin Current vs. Input Voltage

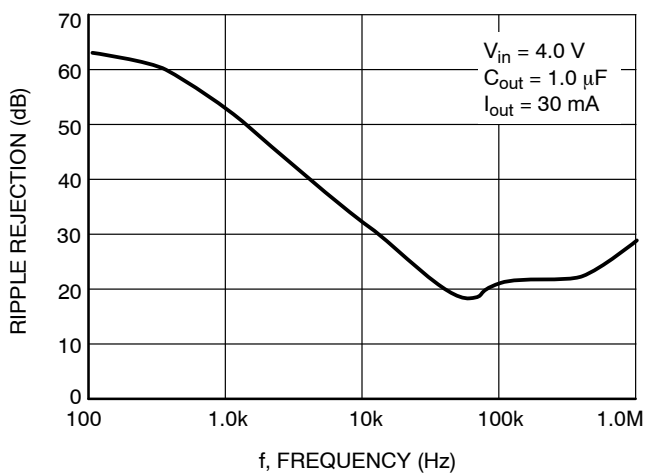


Figure 7. Ripple Rejection vs. Frequency

## TYPICAL CHARACTERISTICS

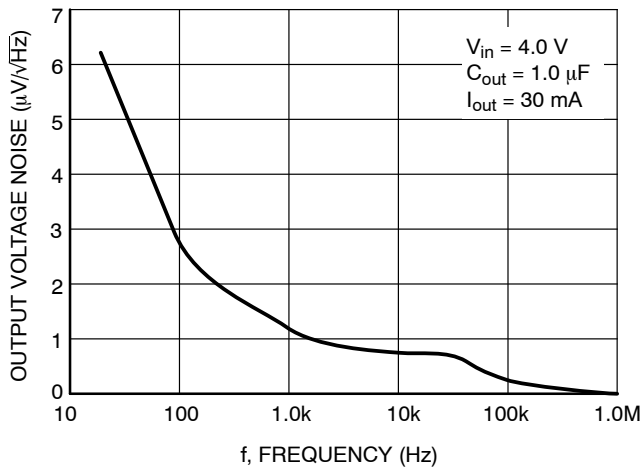


Figure 8. Output Noise Density

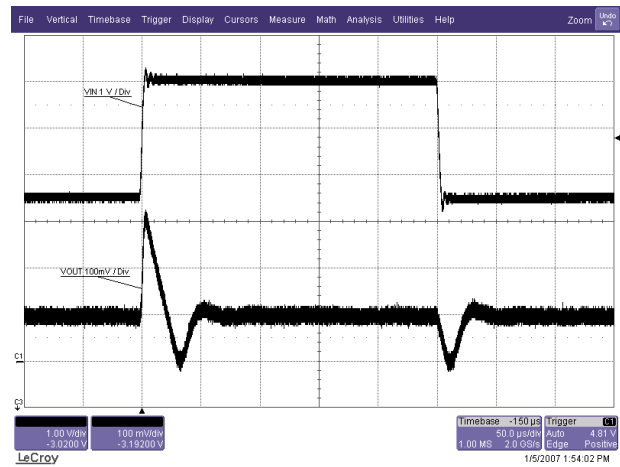


Figure 9. Line Transient Response

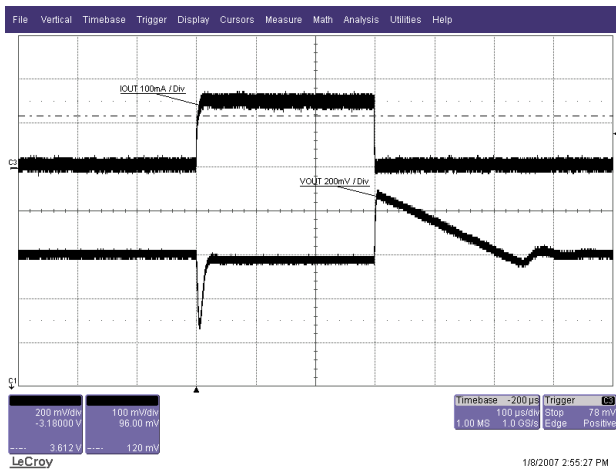


Figure 10. Load Transient Response

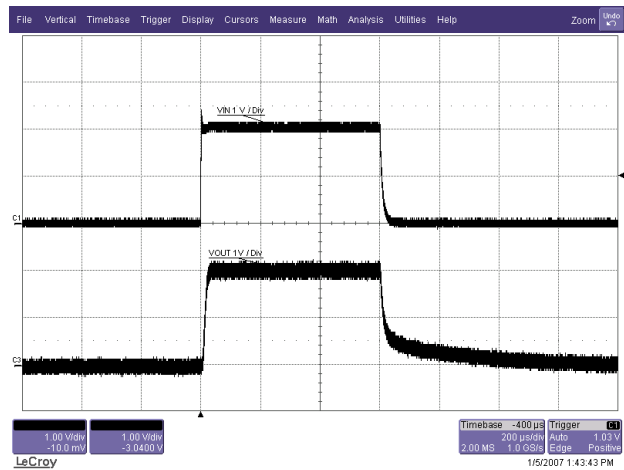


Figure 11. Turn-on Response

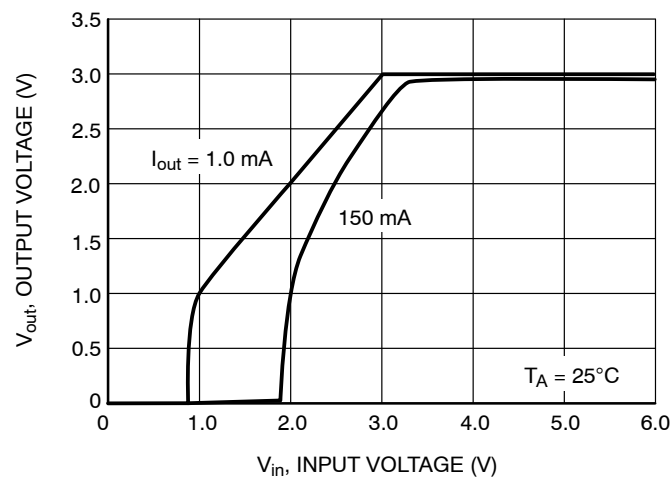


Figure 12. Output Voltage vs. Input Voltage

**DEFINITIONS****Load Regulation**

The change in output voltage for a change in output current at a constant temperature.

**Dropout Voltage**

The input/output differential at which the regulator output no longer maintains regulation against further reductions in input voltage. Measured when the output drops 3.0% below its nominal. The junction temperature, load current, and minimum input supply requirements affect the dropout level.

**Maximum Power Dissipation**

The maximum total dissipation for which the regulator will operate within its specifications.

**Quiescent and Ground Current**

The quiescent current is the current which flows through the ground when the LDO operates without a load on its output: internal IC operation, bias, etc. When the LDO becomes loaded, this term is called the Ground current. It is actually the difference between the input current (measured through the LDO input pin) and the output current.

**Line Regulation**

The change in output voltage for a change in input voltage. The measurement is made under conditions of low dissipation or by using pulse technique such that the average chip temperature is not significantly affected.

**Line Transient Response**

Typical over and undershoot response when input voltage is excited with a given slope.

**Thermal Protection**

Internal thermal shutdown circuitry is provided to protect the integrated circuit in the event that the maximum junction temperature is exceeded. When activated at typically 160 °C, the regulator turns off. This feature is provided to prevent failures from accidental overheating.

**Maximum Package Power Dissipation**

The maximum power package dissipation is the power dissipation level at which the junction temperature reaches its maximum operating value, i.e. 125 °C. Depending on the ambient power dissipation and thus the maximum available output current.



## APPLICATIONS INFORMATION

A typical application circuit for the NCP699 series is shown in Figure 1, front page.

**Input Decoupling ( $C_{in}$ )**

A 1.0  $\mu$ F capacitor either ceramic or tantalum is recommended and should be connected close to the NCP699 package. Higher values and lower ESR will improve the overall line transient response.

TDK capacitor: C2012X5R1C105K, or C1608X5R1A105K

**Output Decoupling ( $C_{out}$ )**

The NCP699 is a stable regulator and does not require any specific Equivalent Series Resistance (ESR) or a minimum output current. Capacitors exhibiting ESRs ranging from a few m $\Omega$  up to 5.0  $\Omega$  can thus safely be used. The minimum decoupling value is 1.0  $\mu$ F and can be augmented to fulfill stringent load transient requirements. The regulator accepts ceramic chip capacitors as well as tantalum capacitors. Larger values improve noise rejection and load regulation transient response.

TDK capacitor: C2012X5R1C105K, C1608X5R1A105K, or C3216X7R1C105K

**Enable Operation**

The enable pin will turn on the regulator when pulled high and turn off the regulator when pulled low. These limits of threshold are covered in the electrical specification section of this data sheet. If the enable is not used then the pin should be connected to  $V_{in}$ .

**Hints**

Please be sure the  $V_{in}$  and Gnd lines are sufficiently wide. When the impedance of these lines is high, there is a chance to pick up noise or cause the regulator to malfunction.

Set external components, especially the output capacitor, as close as possible to the circuit, and make leads as short as possible.

**Thermal**

As power across the NCP699 increases, it might become necessary to provide some thermal relief. The maximum power dissipation supported by the device is dependent upon board design and layout. Mounting pad configuration on the PCB, the board material and also the ambient temperature effect the rate of temperature rise for the part. This is stating that when the NCP699 has good thermal conductivity through the PCB, the junction temperature will be relatively low with high power dissipation applications.

The maximum dissipation the package can handle is given by:

$$PD = \frac{T_{J(max)} - T_A}{R_{\theta JA}}$$

If junction temperature is not allowed above the maximum 125 °C, then the NCP699 can dissipate up to 400 mW @ 25 °C.

The power dissipated by the NCP699 can be calculated from the following equation:

$$P_{tot} = [V_{in} * I_{gnd} (@I_{out})] + [V_{in} - V_{out}] * I_{out}$$

or

$$V_{inMAX} = \frac{P_{tot} + V_{out} * I_{out}}{I_{gnd} (@I_{out}) + I_{out}}$$

If an 150 mA output current is needed then the ground current from the data sheet is 40  $\mu$ A. For an NCP699 (3.0 V), the maximum input voltage will then be 5.65 V.



# NCP699

## ORDERING INFORMATION

| Device        | Nominal Output Voltage* | Marking | Package             | Shipping†                     |
|---------------|-------------------------|---------|---------------------|-------------------------------|
| NCP699SN18T1G | 1.8                     | LJS     | TSOP-5<br>(Pb-Free) | 3000 Units/<br>7" Tape & Reel |
| NCP699SN29T1G | 2.9                     | ACP     |                     |                               |
| NCP699SN30T1G | 3.0                     | LJV     |                     |                               |
| NCP699SN33T1G | 3.3                     | LJW     |                     |                               |
| NCP699SN34T1G | 3.4                     | ACF     |                     |                               |
| NCP699SN50T1G | 5.0                     | LJX     |                     |                               |

## DISCONTINUED (Note 5)

|               |     |     |                     |                               |
|---------------|-----|-----|---------------------|-------------------------------|
| NCP699SN13T1G | 1.3 | LJY | TSOP-5<br>(Pb-Free) | 3000 Units/<br>7" Tape & Reel |
| NCP699SN14T1G | 1.4 | AA4 | TSOP-5<br>(Pb-Free) | 3000 Units/<br>7" Tape & Reel |
| NCP699SN15T1G | 1.5 | LJP | TSOP-5<br>(Pb-Free) | 3000 Units/<br>7" Tape & Reel |
| NCP699SN25T1G | 2.5 | LJT | TSOP-5<br>(Pb-Free) | 3000 Units/<br>7" Tape & Reel |
| NCP699SN28T1G | 2.8 | LJU | TSOP-5<br>(Pb-Free) | 3000 Units/<br>7" Tape & Reel |
| NCP699SN31T1G | 3.1 | AAE | TSOP-5<br>(Pb-Free) | 3000 Units/<br>7" Tape & Reel |
| NCP699SN45T1G | 4.5 | ACQ | TSOP-5<br>(Pb-Free) | 3000 Units/<br>7" Tape & Reel |

† For information on tape and reel specifications, including part orientation and tape sizes, please refer to our Tape and Reel Packaging Specifications Brochure, [BRD8011/D](#).

\* Additional voltages in 100 mV steps are available upon request by contacting your **onsemi** representative.

5. **DISCONTINUED:** These devices are not available. Please contact your **onsemi** representative for information. The most current information on these devices may be available on [www.onsemi.com](#).

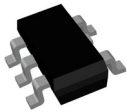
# NCP699

## REVISION HISTORY

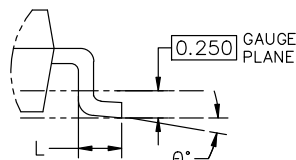
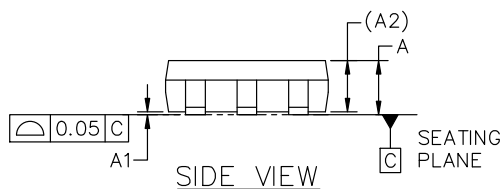
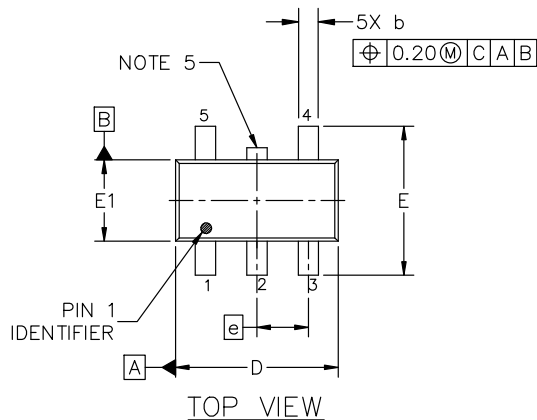
| Revision | Description of Changes                                                                                                                                                                       | Date     |
|----------|----------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------|----------|
| 9        | Rebranded the Data Sheet to <b>onsemi</b> format.<br>NCP699SN13T1G, NCP699SN14T1G, NCP699SN15T1G, NCP699SN25T1G,<br>NCP699SN28T1G, NCP699SN31T1G, NCP699SN45T1G OPNs Marked as Discontinued. | 2/4/2026 |

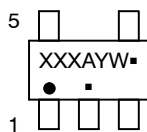
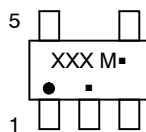
This document has undergone updates prior to the inclusion of this revision history table. The changes tracked here only reflect updates made on the noted approval dates.




**TSOP-5 3.00x1.50x0.95, 0.95P**  
**CASE 483**  
**ISSUE P**

DATE 01 APR 2024


DETAIL "A"  
SCALE 2:1

**GENERIC**  
**MARKING DIAGRAM\***

**Analog**

**Discrete/Logic**

XXX = Specific Device Code    XXX = Specific Device Code  
A = Assembly Location    M = Date Code  
Y = Year    ■ = Pb-Free Package  
W = Work Week  
■ = Pb-Free Package

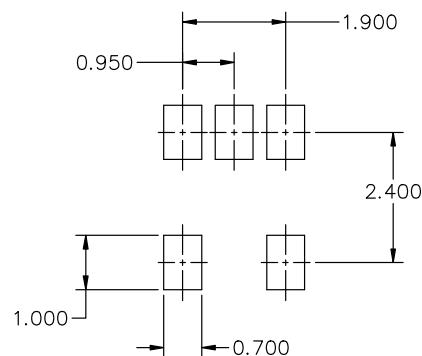
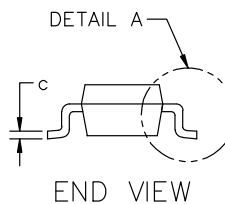
(Note: Microdot may be in either location)

\*This information is generic. Please refer to device data sheet for actual part marking. Pb-Free indicator, "G" or microdot "■", may or may not be present. Some products may not follow the Generic Marking.

## NOTES:

1. DIMENSIONING AND TOLERANCING CONFORM TO ASME Y14.5-2018.
2. ALL DIMENSION ARE IN MILLIMETERS (ANGLES IN DEGREES).
3. MAXIMUM LEAD THICKNESS INCLUDES LEAD FINISH THICKNESS. MINIMUM LEAD THICKNESS IS THE MINIMUM THICKNESS OF BASE MATERIAL.
4. DIMENSIONS D AND E1 DO NOT INCLUDE MOLD FLASH, PROTRUSIONS OF GATE BURRS. MOLD FLASH, PROTRUSIONS, OR GATE BURRS SHALL NOT EXCEED 0.15 PER SIDE. DIMENSION D.
5. OPTIONAL CONSTRUCTION: AN ADDITIONAL TRIMMED LEAD IS ALLOWED IN THIS LOCATION. TRIMMED LEAD NOT TO EXTEND MORE THAN 0.2 FROM BODY.

| DIM | MILLIMETERS |       |       |
|-----|-------------|-------|-------|
|     | MIN.        | NOM.  | MAX.  |
| A   | 0.900       | 1.000 | 1.100 |
| A1  | 0.010       | 0.055 | 0.100 |
| A2  | 0.950 REF.  |       |       |
| b   | 0.250       | 0.375 | 0.500 |
| c   | 0.100       | 0.180 | 0.260 |
| D   | 2.850       | 3.000 | 3.150 |
| E   | 2.500       | 2.750 | 3.000 |
| E1  | 1.350       | 1.500 | 1.650 |
| e   | 0.950 BSC   |       |       |
| L   | 0.200       | 0.400 | 0.600 |
| θ   | 0°          | 5°    | 10°   |


**RECOMMENDED MOUNTING FOOTPRINT\***

\* FOR ADDITIONAL INFORMATION ON OUR Pb-FREE STRATEGY AND SOLDERING DETAILS, PLEASE DOWNLOAD THE ON SEMICONDUCTOR SOLDERING AND MOUNTING TECHNIQUES REFERENCE MANUAL, SOLDERM/D.

|                         |                                     |                                                                                                                                                                                  |
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