



PBHV9540Z-Q

500 V, 0.5 A PNP high-voltage low V_{CEsat} transistor

17 July 2023

Product data sheet

1. General description

PNP high-voltage low V_{CEsat} transistor in a medium power SOT223 (SC-73) Surface-Mounted Device (SMD) plastic package.

NPN complement: PBHV8140Z-Q

2. Features and benefits

- High voltage
- Low collector-emitter saturation voltage V_{CEsat}
- High collector current capability I_C and I_{CM}
- High collector current gain (h_{FE}) at high I_C
- Medium power SMD plastic package
- Qualified according to AEC-Q101 and recommended for use in automotive applications

3. Applications

- LED driver for LED chain module
- LCD backlighting
- Automotive motor management
- Switch Mode Power Supply (SMPS)

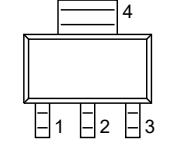
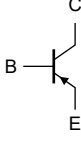
4. Quick reference data

Table 1. Quick reference data

Symbol	Parameter	Conditions	Min	Typ	Max	Unit
V _{CESM}	collector-emitter peak voltage	V _{BE} = 0 V	-	-	-500	V
V _{CEO}	collector-emitter voltage	open base	-	-	-400	V
I _C	collector current		-	-	-0.5	A
h _{FE}	DC current gain	V _{CE} = -10 V; I _C = -50 mA; pulsed; t _p ≤ 300 μs; δ ≤ 0.02; T _{amb} = 25 °C	100	155	-	

5. Pinning information

Table 2. Pinning information

Pin	Symbol	Description	Simplified outline	Graphic symbol
1	B	base	 SC-73 (SOT223)	 sym028
2	C	collector		
3	E	emitter		
4	C	collector		

6. Ordering information

Table 3. Ordering information

Type number	Package		
	Name	Description	Version
PBHV9540Z-Q	SC-73	plastic, surface-mounted package with increased heatsink; 4 leads; 2.3 mm pitch; 6.5 mm x 3.5 mm x 1.65 mm body	SOT223

7. Marking

Table 4. Marking codes

Type number	Marking code
PBHV9540Z-Q	V9540Z

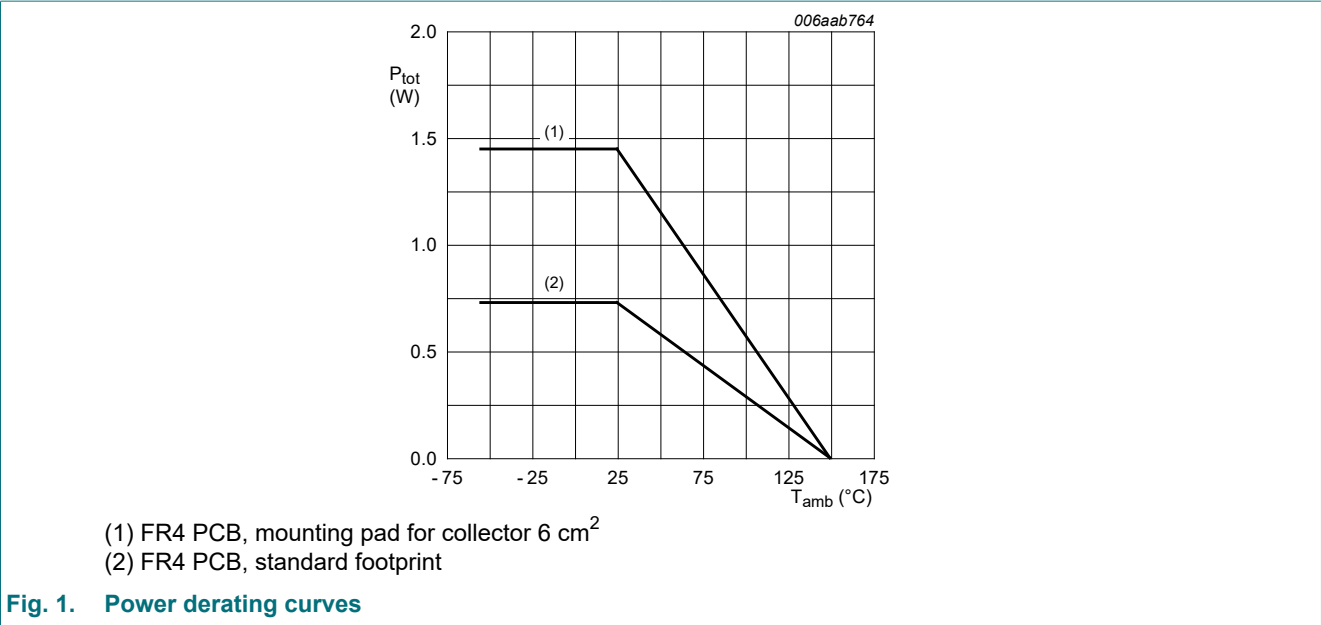
8. Limiting values

Table 5. Limiting values

In accordance with the Absolute Maximum Rating System (IEC 60134).

Symbol	Parameter	Conditions		Min	Max	Unit
V_{CBO}	collector-base voltage	open emitter		-	-500	V
V_{CEO}	collector-emitter voltage	open base		-	-400	V
V_{CESM}	collector-emitter peak voltage	$V_{BE} = 0\text{ V}$		-	-500	V
V_{EBO}	emitter-base voltage	open collector		-	-6	V
I_C	collector current	single pulse; $t_p \leq 1\text{ ms}$		-	-0.5	A
I_{CM}	peak collector current			-	-1	A
I_{BM}	peak base current			-	-200	mA
P_{tot}	total power dissipation	$T_{amb} \leq 25\text{ }^{\circ}\text{C}$	[1]	-	1.45	W
			[2]	-	0.73	W
T_j	junction temperature			-	150	$^{\circ}\text{C}$
T_{amb}	ambient temperature			-55	150	$^{\circ}\text{C}$
T_{stg}	storage temperature			-65	150	$^{\circ}\text{C}$

[1] Device mounted on an FR4 PCB, single-sided copper, tin-plated, mounting pad for collector 6 cm².
[2] Device mounted on an FR4 PCB, single-sided copper, tin-plated and standard footprint.

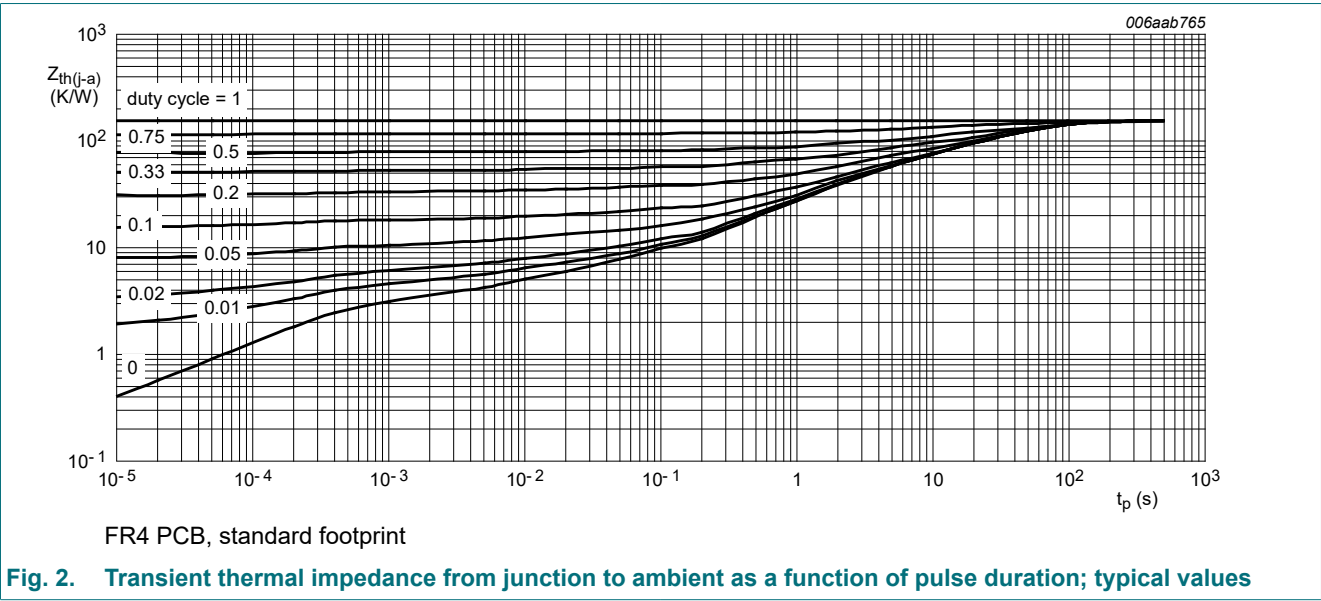


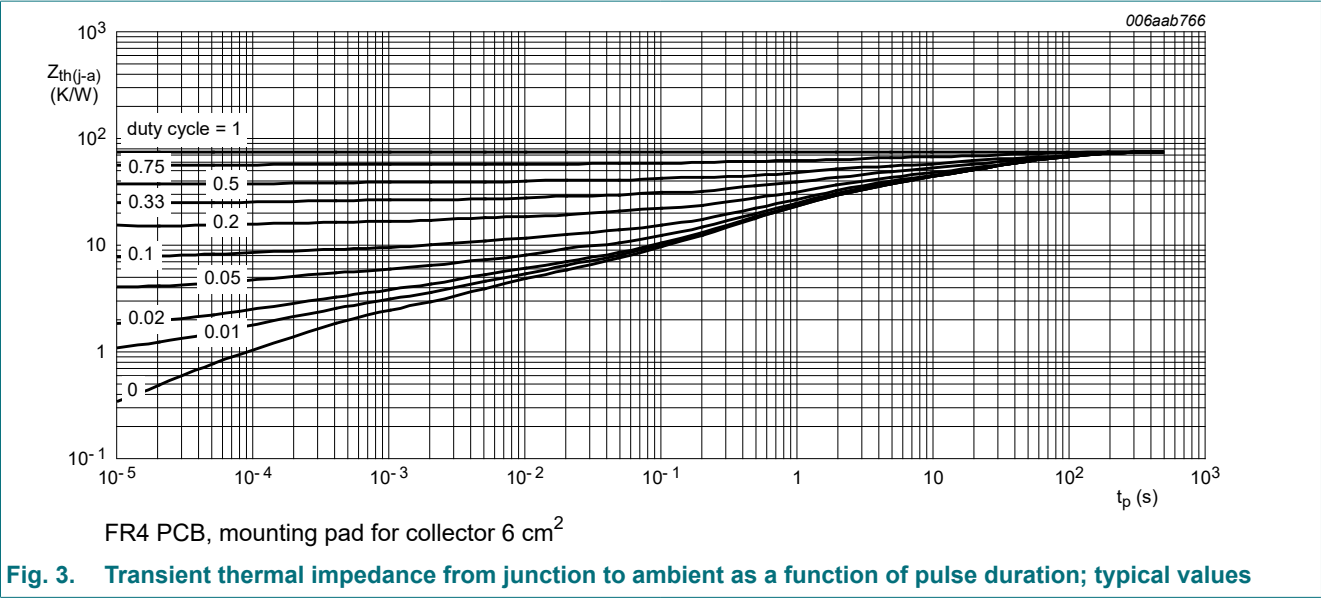
9. Thermal characteristics

Table 6. Thermal characteristics

Symbol	Parameter	Conditions		Min	Typ	Max	Unit
$R_{th(j-a)}$	thermal resistance from junction to ambient	in free air	[1]	-	-	170	K/W
			[2]	-	-	85	K/W
$R_{th(j-sp)}$	thermal resistance from junction to solder point			-	-	15	K/W

- [1] Device mounted on an FR4 PCB, single-sided copper, tin-plated and standard footprint.
[2] Device mounted on an FR4 PCB, single-sided copper, tin-plated, mounting pad for collector 6 cm².



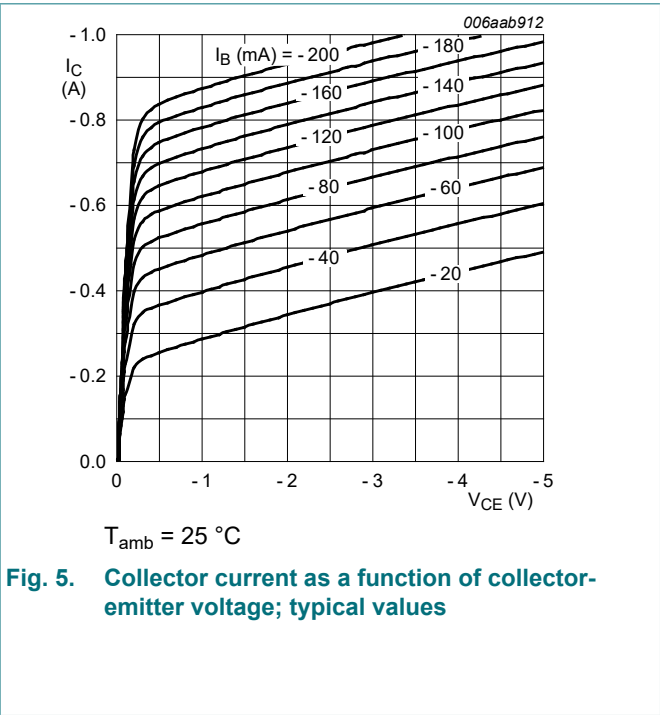
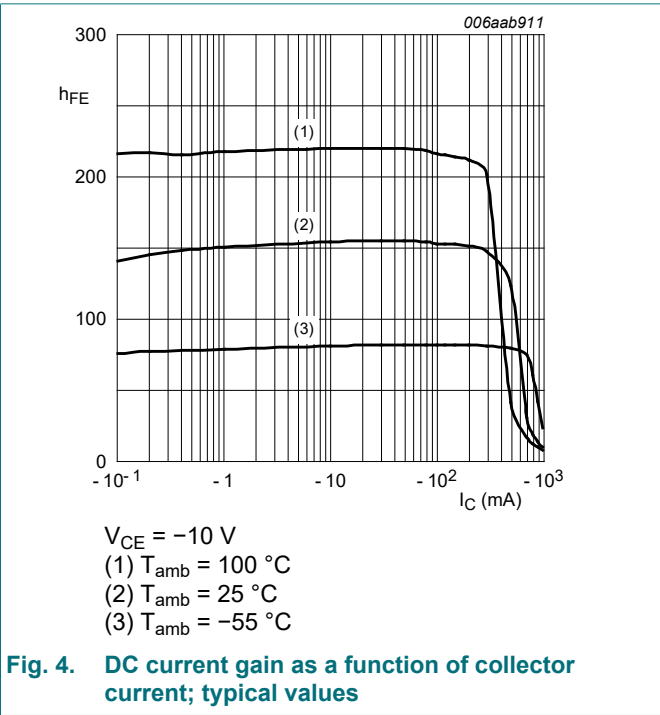


10. Characteristics

Table 7. Characteristics

Symbol	Parameter	Conditions	Min	Typ	Max	Unit
I_{CBO}	collector-base cut-off current	$V_{CB} = -320\text{ V}$; $I_E = 0\text{ A}$; $T_{amb} = 25\text{ °C}$	-	-	-100	nA
		$V_{CB} = -320\text{ V}$; $I_E = 0\text{ A}$; $T_j = 150\text{ °C}$	-	-	-10	μA
I_{EBO}	emitter-base cut-off current	$V_{EB} = -4\text{ V}$; $I_C = 0\text{ A}$; $T_{amb} = 25\text{ °C}$	-	-	-100	nA
I_{CES}	collector-emitter cut-off current	$V_{CE} = -320\text{ V}$; $V_{BE} = 0\text{ V}$; $T_{amb} = 25\text{ °C}$	-	-	-100	nA
h_{FE}	DC current gain	$V_{CE} = -10\text{ V}$; $I_C = -50\text{ mA}$; pulsed; $t_p \leq 300\text{ μs}$; $\delta \leq 0.02$; $T_{amb} = 25\text{ °C}$	100	155	-	
		$V_{CE} = -10\text{ V}$; $I_C = -100\text{ mA}$; pulsed; $t_p \leq 300\text{ μs}$; $\delta \leq 0.02$; $T_{amb} = 25\text{ °C}$	100	155	-	
		$V_{CE} = -10\text{ V}$; $I_C = -300\text{ mA}$; pulsed; $t_p \leq 300\text{ μs}$; $\delta \leq 0.02$; $T_{amb} = 25\text{ °C}$	80	145	-	
		$V_{CE} = -10\text{ V}$; $I_C = -500\text{ mA}$; pulsed; $t_p \leq 300\text{ μs}$; $\delta \leq 0.02$; $T_{amb} = 25\text{ °C}$	65	130	-	
V_{CEsat}	collector-emitter saturation voltage	$I_C = -100\text{ mA}$; $I_B = -10\text{ mA}$; $T_{amb} = 25\text{ °C}$	-	-100	-190	mV
		$I_C = -100\text{ mA}$; $I_B = -20\text{ mA}$; pulsed; $t_p \leq 300\text{ μs}$; $\delta \leq 0.02$; $T_{amb} = 25\text{ °C}$	-	-65	-110	mV
		$I_C = -300\text{ mA}$; $I_B = -60\text{ mA}$; pulsed; $t_p \leq 300\text{ μs}$; $\delta \leq 0.02$; $T_{amb} = 25\text{ °C}$	-	-110	-210	mV
		$I_C = -500\text{ mA}$; $I_B = -100\text{ mA}$; pulsed; $t_p \leq 300\text{ μs}$; $\delta \leq 0.02$; $T_{amb} = 25\text{ °C}$	-	-180	-320	mV
R_{CEsat}	collector-emitter saturation resistance		-	360	640	mΩ
V_{BEsat}	base-emitter saturation voltage	$I_C = -100\text{ mA}$; $I_B = -20\text{ mA}$; pulsed; $t_p \leq 300\text{ μs}$; $\delta \leq 0.02$; $T_{amb} = 25\text{ °C}$	-	-0.75	-0.9	V

Symbol	Parameter	Conditions	Min	Typ	Max	Unit
t_d	delay time	$V_{CC} = -2\text{ V}; I_C = -0.15\text{ A}; I_{B(on)} = -0.03\text{ A};$ $I_{B(off)} = 0.03\text{ A}; T_{amb} = 25\text{ }^{\circ}\text{C}$	-	55	-	ns
t_r	rise time		-	1775	-	ns
t_{on}	turn-on time		-	1830	-	ns
t_s	storage time		-	1545	-	ns
t_f	fall time		-	920	-	ns
t_{off}	turn-off time		-	2465	-	ns
f_T	transition frequency	$V_{CE} = -10\text{ V}; I_C = -10\text{ mA}; f = 100\text{ MHz}$	-	30	-	MHz
C_c	collector capacitance	$V_{CB} = -20\text{ V}; I_E = 0\text{ A}; i_e = 0\text{ A};$ $f = 1\text{ MHz}; T_{amb} = 25\text{ }^{\circ}\text{C}$	-	20	-	pF
C_e	emitter capacitance	$V_{EB} = -0.5\text{ V}; I_C = 0\text{ A}; i_c = 0\text{ A};$ $f = 1\text{ MHz}; T_{amb} = 25\text{ }^{\circ}\text{C}$	-	540	-	pF



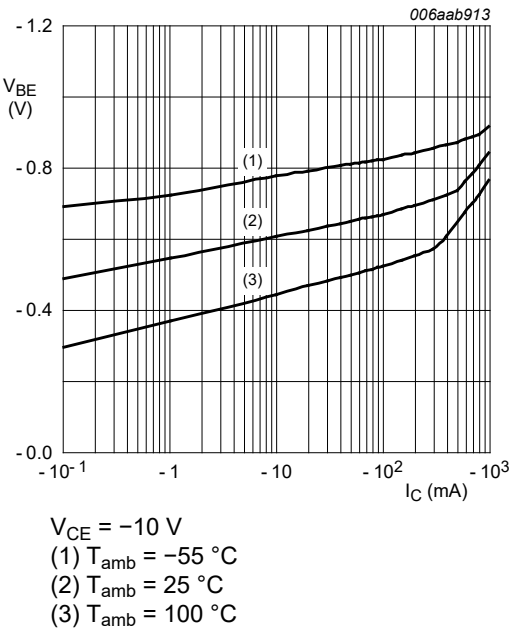


Fig. 6. Base-emitter voltage as a function of collector current; typical values

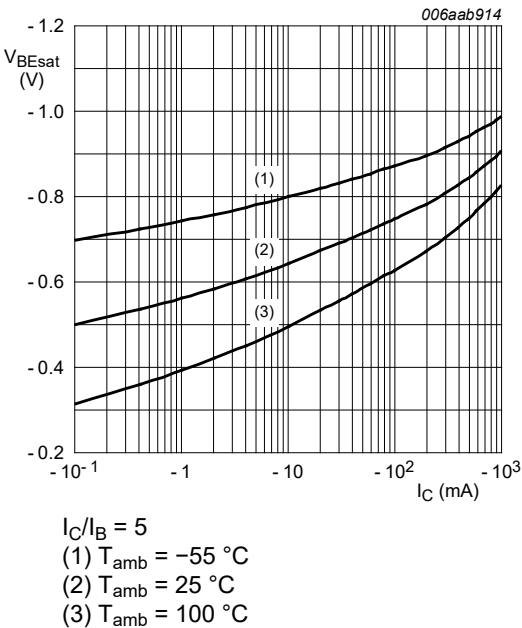


Fig. 7. Base-emitter saturation voltage as a function of collector current; typical values

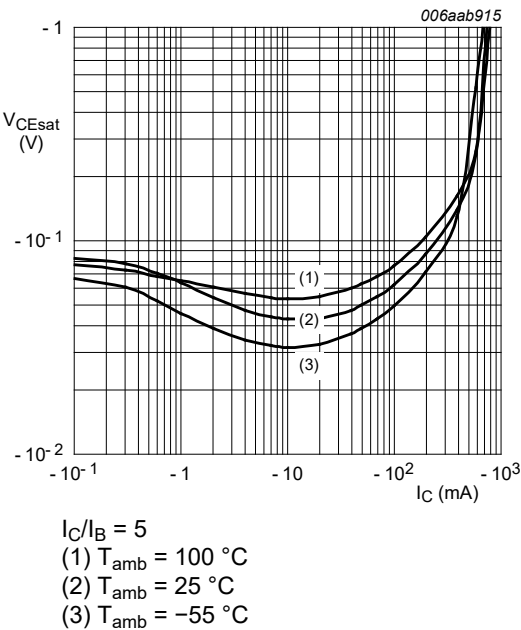


Fig. 8. Collector-emitter saturation voltage as a function of collector current; typical values

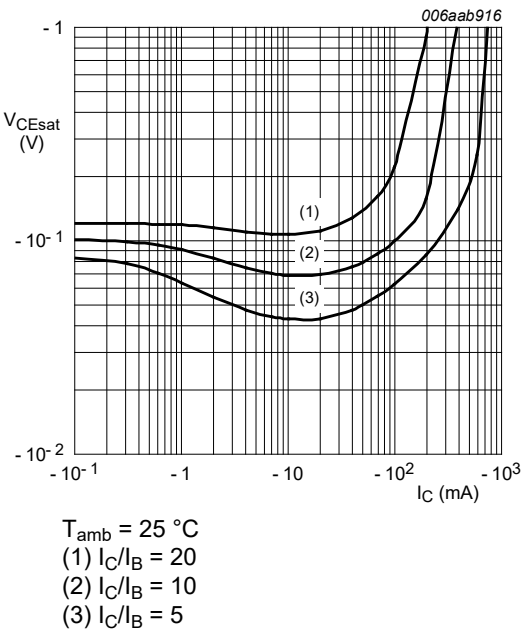
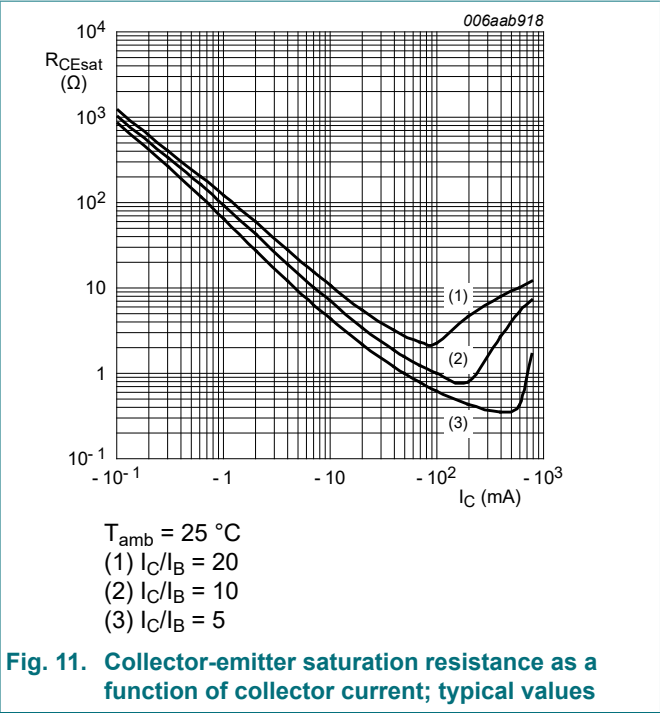
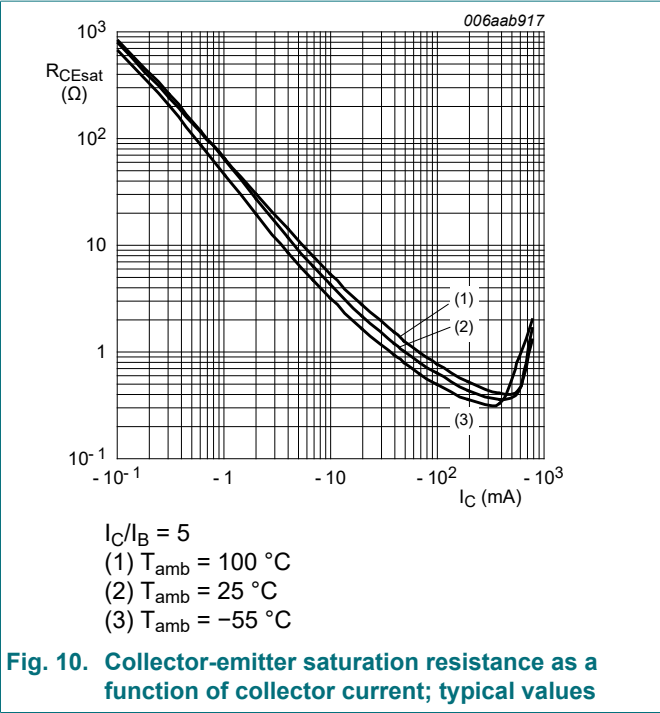
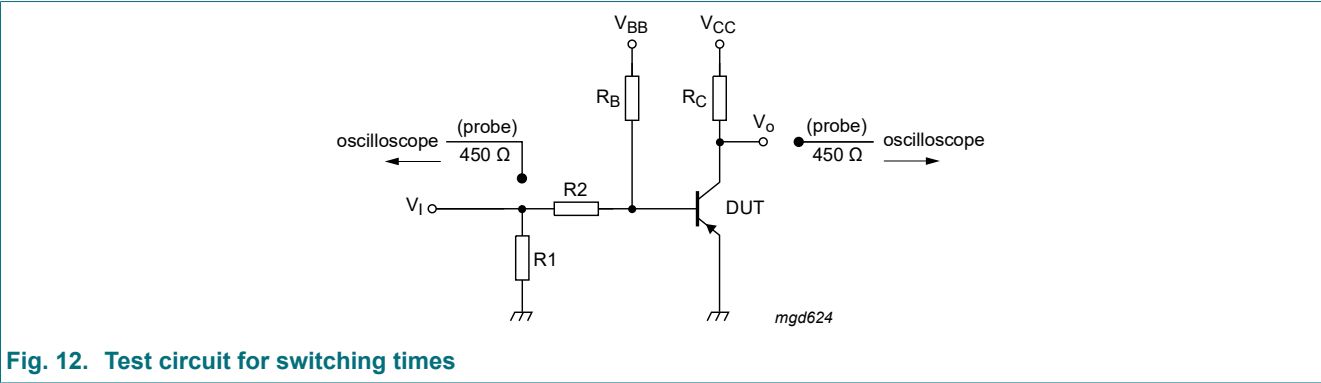


Fig. 9. Collector-emitter saturation voltage as a function of collector current; typical values



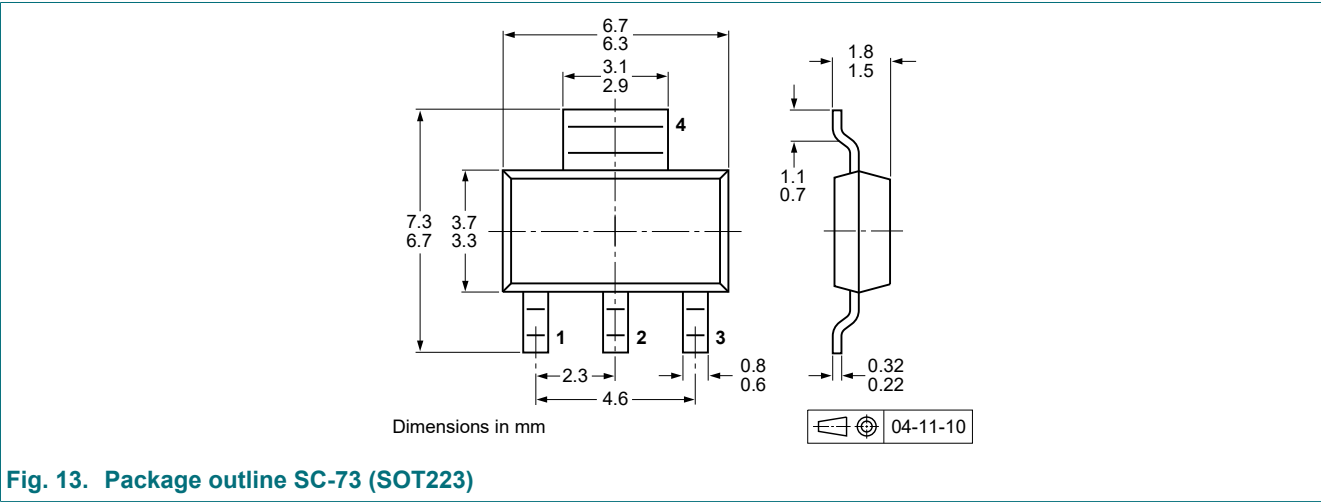
11. Test information



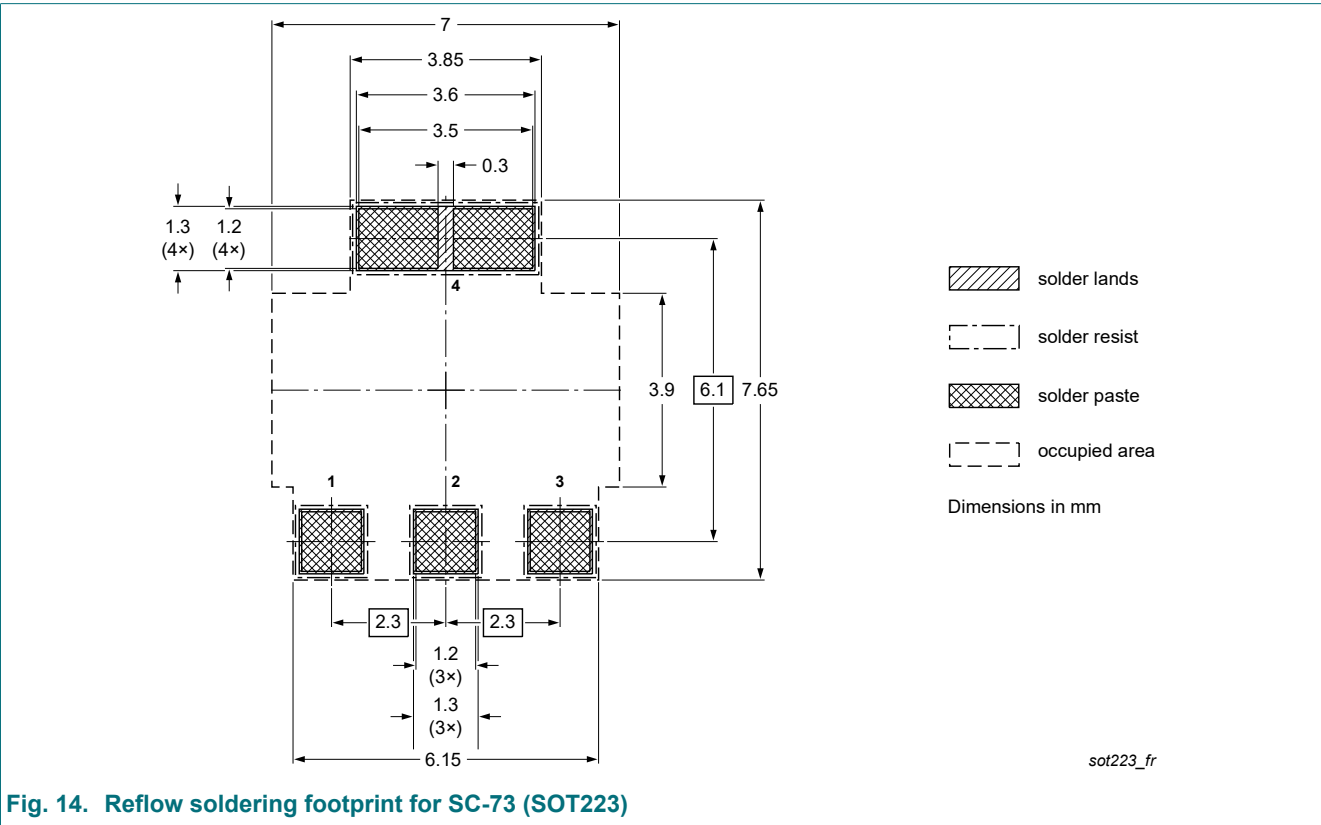
Quality information

This product has been qualified in accordance with the Automotive Electronics Council (AEC) standard *Q101 - Stress test qualification for discrete semiconductors*, and is suitable for use in automotive applications.

12. Package outline



13. Soldering



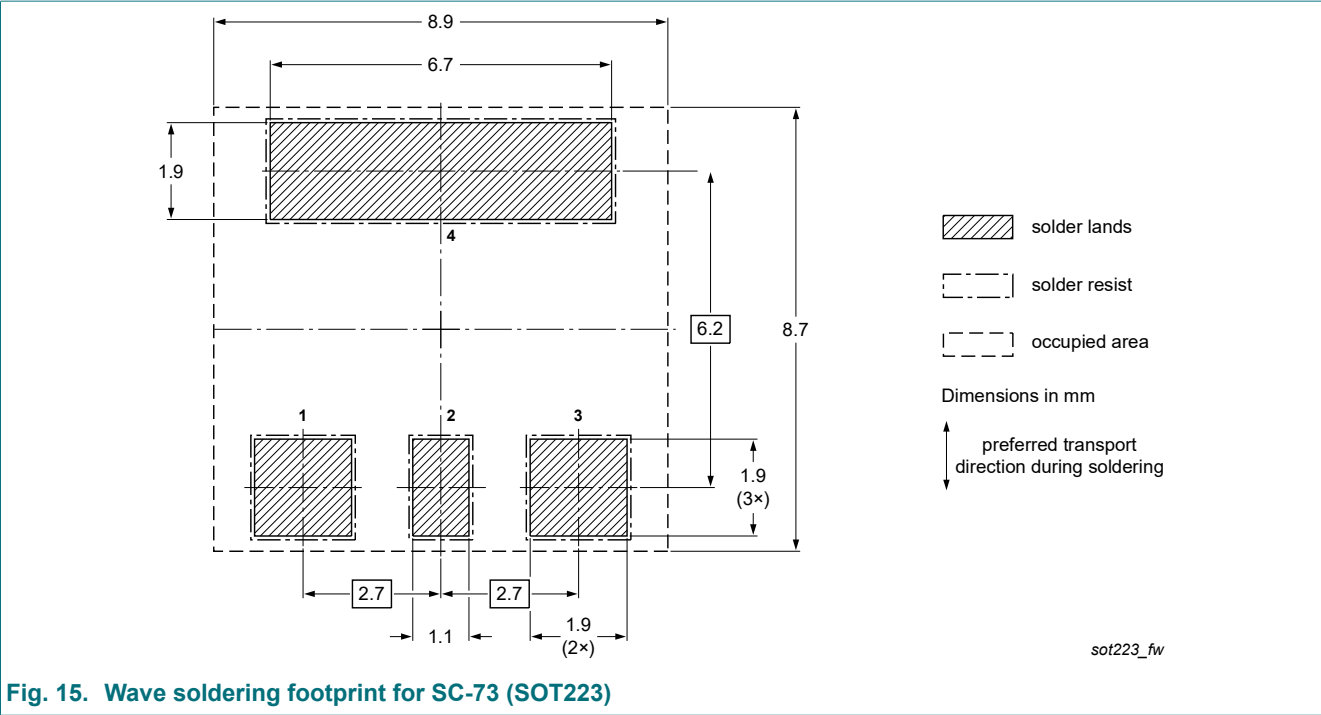


Fig. 15. Wave soldering footprint for SC-73 (SOT223)

14. Revision history

Table 8. Revision history

Data sheet ID	Release date	Data sheet status	Change notice	Supersedes
PBHV9540Z-Q v.1	20230717	Product data sheet	-	-

15. Legal information

Data sheet status

Document status [1][2]	Product status [3]	Definition
Objective [short] data sheet	Development	This document contains data from the objective specification for product development.
Preliminary [short] data sheet	Qualification	This document contains data from the preliminary specification.
Product [short] data sheet	Production	This document contains the product specification.

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- [2] The term 'short data sheet' is explained in section "Definitions".
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