

RoHS Compliant

Includes all homogeneous materials

INDIVIDUAL SPECIFICATION

NIC P/N: NMC0402X5R104K50TRPF

CERAMIC CHIP CAPACITOR

Specifications: (Test conditions Per EIA-198)	
Capacitance Value:	0.1uF (1KHz, 1.0Vrms±0.2, +25°C)
Capacitance Tolerance:	±10% (K)
Operating Temperature Range:	-55°C ~ +85°C
Capacitance Change Over Temperature Range:	±15% (0VDC)
Voltage Rating (WVDC):	50VDC
Dissipation Factor:	5% max. (1KHz, 1.0Vrms±0.2, +25°C)
Insulation Resistance:	500MegΩ (after 1 minute at rated voltage, 50mA max.)
Dielectric Withstanding Voltage:	125VDC for 1 ~ 5 seconds max.
Dimensions (mm):	
Length (L):	1.00 ±0.05
Width (W):	0.50 ±0.05
Height (T):	0.55 max.
Termination Overlap (P):	0.20 min.
Termination: Lead-Free 100% Tin (Sn)	
Packaging:	
Per EIA-481-1-A; 8.0mm width punched carrier tape, 2.0mm component pitch, 10K/reel	

