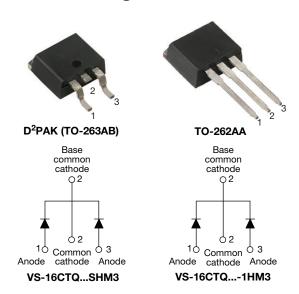


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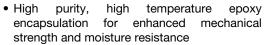
## High Performance Schottky Rectifier, 2 x 8 A



PRIMARY CHARACTERISTICS						
I <sub>F(AV)</sub>	2 x 8 A					
$V_{R}$	60 V to 100 V					
V <sub>F</sub> at I <sub>F</sub>	0.58 V					
I <sub>RM</sub>	7.0 mA at 125 °C					
T <sub>J</sub> max.	175 °C					
E <sub>AS</sub>	7.5 mJ					
Package D <sup>2</sup> PAK (TO-263AB), T						
Circuit configuration	Common cathode					

#### **FEATURES**

- 175 °C T<sub>J</sub> operation
- Center tap configuration
- Low forward voltage drop





FREE

- High frequency operation
- Guard ring for enhanced ruggedness and long term reliability
- Meets MSL level 1, per J-STD-020, LF maximum peak of 260 °C
- AEC-Q101 qualified, meets JESD 201 class 1 whisker test
- Material categorization: for definitions of compliance please see <a href="https://www.vishay.com/doc?99912">www.vishay.com/doc?99912</a>

#### **DESCRIPTION**

This center tap Schottky rectifier series has been optimized for low reverse leakage at high temperature. The proprietary barrier technology allows for reliable operation up to 175 °C junction temperature. Typical applications are in switching power supplies, converters, freewheeling diodes, and reverse battery protection.

MAJOR RATINGS AND CHARACTERISTICS								
SYMBOL CHARACTERISTICS VALUES								
I <sub>F(AV)</sub>	Rectangular waveform	16	Α					
V <sub>RRM</sub>		60 to 100	V					
I <sub>FSM</sub>	t <sub>p</sub> = 5 μs sine	850	Α					
V <sub>F</sub>	8 A <sub>pk</sub> , T <sub>J</sub> = 125 °C (per leg)	0.58	V					
T <sub>J</sub>	Range	-55 to +175	°C					

VOLTAGE RATINGS									
PARAMETER	SYMBOL	VS-16CTQ060SHM3 VS-16CTQ060-1HM3	VS-16CTQ080SHM3 VS-16CTQ080-1HM3	VS-16CTQ100SHM3 VS-16CTQ100-1HM3	UNITS				
Maximum DC reverse voltage	$V_R$	60	80	100	V				
Maximum working peak reverse voltage	$V_{RWM}$	00	00	100	V				

ABSOLUTE MAXIMUM RATINGS								
PARAMETER SYMBOL TEST CONDITIONS					UNITS			
Maximum average forward current per leg	1	50 % duty cycle at T <sub>C</sub> = 148 °C	rootongular wayoform	8	۸			
See fig. 5 per device	I <sub>F(AV)</sub>	30 % duty cycle at 1 <sub>C</sub> = 146 C	16	A				
Maximum peak one cycle non-repetitive surge current per leg	1	5 μs sine or 3 μs rect. pulse Following any rated load condition and with rated		850	Α			
See fig. 7	IFSM	10 ms sine or 6 ms rect. pulse	275	^				
Non-repetitive avalanche energy per leg	E <sub>AS</sub>	$T_J = 25  ^{\circ}\text{C},  I_{AS} = 0.50  \text{A},  L = 60  \text{mH}$		7.50	mJ			
Repetitive avalanche current per leg	I <sub>AR</sub>	Current decaying linearly to zer Frequency limited by T <sub>J</sub> maxim	0.50	Α				

Revision: 03-Aug-2023 1 Document Number: 95861



# VS-16CTQ...SHM3, VS-16CTQ...-1HM3 Series

## Vishay Semiconductors

ELECTRICAL SPECIFICATIONS								
PARAMETER	SYMBOL	TEST	CONDITIONS	VALUES	UNITS			
		8 A	T 05 °C	0.72	V			
Maximum forward voltage drop per leg See fig. 1	V (1)	16 A	T <sub>J</sub> = 25 °C	0.88				
	V <sub>FM</sub> <sup>(1)</sup>	8 A	T 105 %C	0.58				
		16 A	T <sub>J</sub> = 125 °C	0.69				
Maximum reverse leakage current per leg	I <sub>RM</sub> <sup>(1)</sup>	T <sub>J</sub> = 25 °C	\/to.d\/	0.55	mA			
See fig. 2		T <sub>J</sub> = 125 °C	$V_R = \text{rated } V_R$	7.0				
Threshold voltage	V <sub>F(TO)</sub>	T T	·	0.415	V			
Forward slope resistance	r <sub>t</sub>	$T_J = T_J$ maximum		11.07	mΩ			
Maximum junction capacitance per leg	C <sub>T</sub>	V <sub>R</sub> = 5 V <sub>DC</sub> (test signal	500	pF				
Typical series inductance per leg	L <sub>S</sub>	Measured lead to lead	8.0	nH				
Maximum voltage rate of change	dV/dt	Rated V <sub>R</sub>	10 000	V/µs				

### Note

 $<sup>^{(1)}\,</sup>$  Pulse width < 300  $\mu s,$  duty cycle < 2 %

THERMAL - MECHANICAL SPECIFICATIONS								
PARAMETER		SYMBOL	TEST CONDITIONS	VALUES	UNITS			
Maximum junction and storage temperature range		T <sub>J</sub> , T <sub>Stg</sub>		-55 to +175	°C			
Maximum thermal resistance, junction to case per leg		D	DC operation	3.25				
Maximum thermal resistance, junction to case per package		R <sub>thJC</sub>	DC operation	1.63	°C/W			
Typical thermal resistance, case to heatsink		R <sub>thCS</sub>	Mounting surface, smooth and greased	0.50				
Approximate weight				2	g			
Approximate weight				0.07	OZ.			
Mounting toward	minimum			6 (5)	kgf · cm			
Mounting torque	maximum			12 (10)	(lbf $\cdot$ in)			
Marking device			Case style D <sup>2</sup> PAK (TO-263AB)	16CTC	SH			
			Case style TO-262AA	16CTC	)1H			

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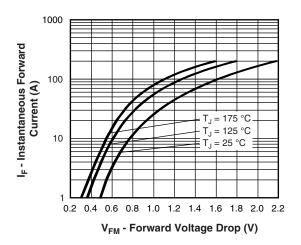


Fig. 1 - Maximum Forward Voltage Drop Characteristics (Per Leg)

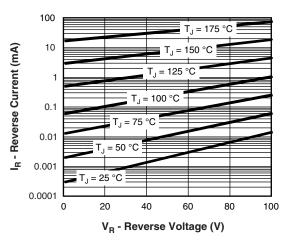


Fig. 2 - Typical Values of Reverse Current vs. Reverse Voltage (Per Leg)

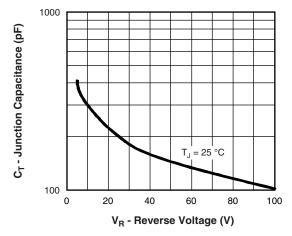


Fig. 3 - Typical Junction Capacitance vs. Reverse Voltage (Per Leg)

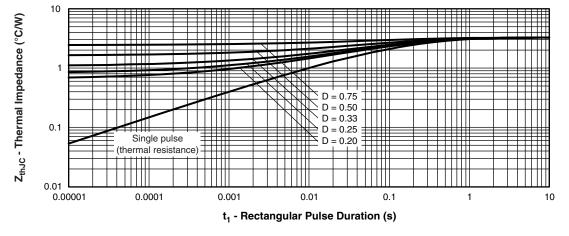


Fig. 4 - Maximum Thermal Impedance Z<sub>thJC</sub> Characteristics (Per Leg)

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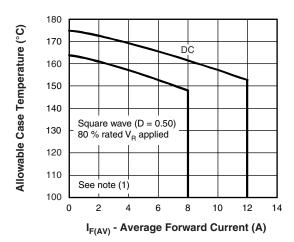


Fig. 5 - Maximum Allowable Case Temperature vs. Average Forward Current (Per Leg)

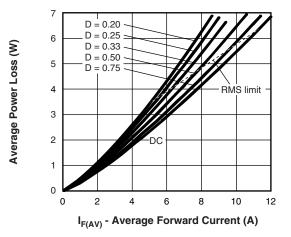


Fig. 6 - Forward Power Loss Characteristics (Per Leg)

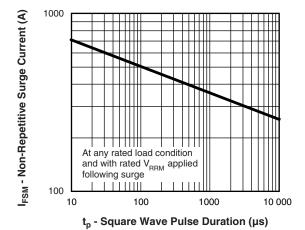


Fig. 7 - Maximum Non-Repetitive Surge Current (Per Leg)

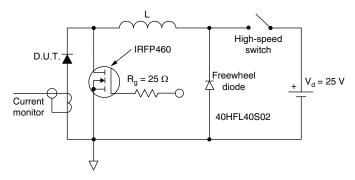


Fig. 8 - Unclamped Inductive Test Circuit

### Note

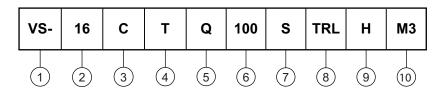
 $^{(1)}$  Formula used:  $T_C = T_J - (Pd + Pd_{REV}) \times R_{thJC};$  Pd = forward power loss =  $I_{F(AV)} \times V_{FM}$  at  $(I_{F(AV)}/D)$  (see fig. 6);  $Pd_{REV} =$  inverse power loss =  $V_{R1} \times I_R$  (1 - D);  $I_R$  at  $V_{R1} = 80~\%$  rated  $V_R$  applied

## VS-16CTQ...SHM3, VS-16CTQ...-1HM3 Series

Vishay Semiconductors

### **ORDERING INFORMATION TABLE**

**Device code** 



Vishay Semiconductors product

Current rating (16 A)

Circuit configuration: C = Common cathode

T = TO-220

Schottky "Q" series

060 = 60 V

Voltage ratings

080 = 80 V

•  $S = D^2PAK$ 

100 = 100 V

• -1 = TO-262

8 • None = tube

• TRL = tape and reel (left oriented - for D<sup>2</sup>PAK only)

• TRR = tape and reel (right oriented - for D<sup>2</sup>PAK only)

H = AEC-Q101 qualified

M3 = halogen-free, RoHS-compliant and termination lead (Pb)-free

ORDERING INFORMATI	ORDERING INFORMATION									
PREFERRED P/N	QUANTITY PER T/R	MINIMUM ORDER QUANTITY	PACKAGING DESCRIPTION							
VS-16CTQ060SHM3	50	1000	Antistatic plastic tubes							
VS-16CTQ060STRRHM3	800	800	13" diameter reel							
VS-16CTQ060STRLHM3	800	800	13" diameter reel							
VS-16CTQ060-1HM3	50	1000	Antistatic plastic tubes							
VS-16CTQ080SHM3	50	1000	Antistatic plastic tubes							
VS-16CTQ080STRRHM3	800	800	13" diameter reel							
VS-16CTQ080STRLHM3	800	800	13" diameter reel							
VS-16CTQ080-1HM3	50	1000	Antistatic plastic tubes							
VS-16CTQ100SHM3	50	1000	Antistatic plastic tubes							
VS-16CTQ100STRRHM3	800	800	13" diameter reel							
VS-16CTQ100STRLHM3	800	800	13" diameter reel							
VS-16CTQ100-1HM3	50	1000	Antistatic plastic tubes							

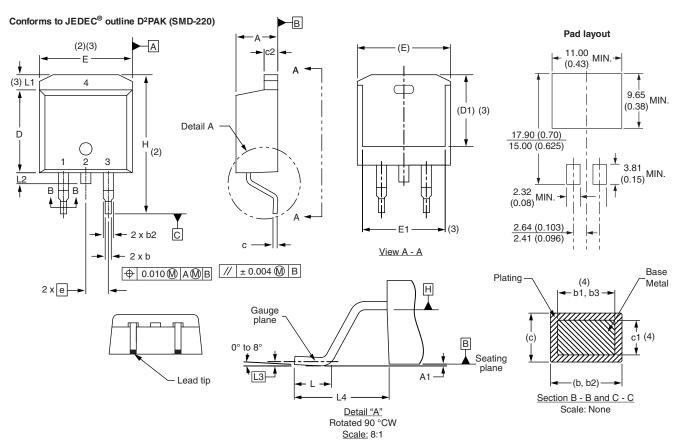
LINKS TO RELATED DOCUMENTS						
Dimensions	TO-263AB (D <sup>2</sup> PAK)	www.vishay.com/doc?95046				
Dimensions	TO-262AA	www.vishay.com/doc?95419				
Doub as auditors information	TO-263AB (D <sup>2</sup> PAK)	www.vishay.com/doc?95444				
Part marking information	TO-262AA	www.vishay.com/doc?95443				
Packaging information		www.vishay.com/doc?95032				
SPICE model		www.vishay.com/doc?95279				



### Vishay Semiconductors

### D<sup>2</sup>PAK

#### **DIMENSIONS** in millimeters and inches



SYMBOL	MILLIM	MILLIMETERS		HES	NOTES	NOTES SYMBOL	MILLIM	ETERS	INC	HES	NOTES	
STIVIBUL	MIN.	MAX.	MIN.	MAX.	NOIES	NOTES	STWIDOL	MIN.	MAX.	MIN.	MAX.	NOTES
Α	4.06	4.83	0.160	0.190			D1	6.86	8.00	0.270	0.315	3
A1	0.00	0.254	0.000	0.010			Е	9.65	10.67	0.380	0.420	2, 3
b	0.51	0.99	0.020	0.039			E1	7.90	8.80	0.311	0.346	3
b1	0.51	0.89	0.020	0.035	4		е	2.54	BSC	0.100	) BSC	
b2	1.14	1.78	0.045	0.070			Н	14.61	15.88	0.575	0.625	
b3	1.14	1.73	0.045	0.068	4		L	1.78	2.79	0.070	0.110	
С	0.38	0.74	0.015	0.029			L1	-	1.65	-	0.066	3
c1	0.38	0.58	0.015	0.023	4		L2	1.27	1.78	0.050	0.070	
c2	1.14	1.65	0.045	0.065			L3	0.25	BSC	0.010	BSC	
D	8.51	9.65	0.335	0.380	2		L4	4.78	5.28	0.188	0.208	

#### Notes

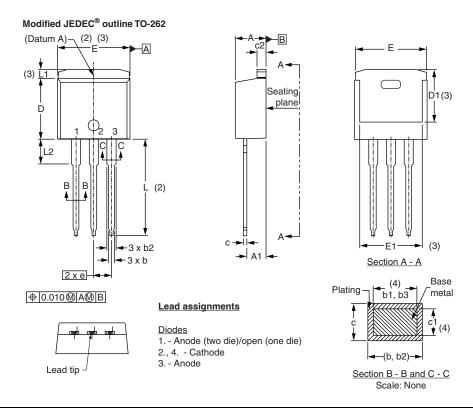
- (1) Dimensioning and tolerancing per ASME Y14.5 M-1994
- (2) Dimension D and E do not include mold flash. Mold flash shall not exceed 0.127 mm (0.005") per side. These dimensions are measured at the outmost extremes of the plastic body
- (3) Thermal pad contour optional within dimension E, L1, D1 and E1
- (4) Dimension b1 and c1 apply to base metal only
- (5) Datum A and B to be determined at datum plane H
- (6) Controlling dimension: inch
- (7) Outline conforms to JEDEC® outline TO-263AB

Revision: 08-Jul-15 1 Document Number: 95046

### Vishay Semiconductors

### **TO-262**

#### **DIMENSIONS** in millimeters and inches



SYMBOL	MILLIN	METERS	INC	INCHES			
STINIDOL	MIN.	MAX.	MIN.	MAX.	NOTES		
Α	4.06	4.83	0.160	0.190			
A1	2.03	3.02	0.080	0.119			
b	0.51	0.99	0.020	0.039			
b1	0.51	0.89	0.020	0.035	4		
b2	1.14	1.78	0.045	0.070			
b3	1.14	1.73	0.045	0.068	4		
С	0.38	0.74	0.015	0.029			
c1	0.38	0.58	0.015	0.023	4		
c2	1.14	1.65	0.045	0.065			
D	8.51	9.65	0.335	0.380	2		
D1	6.86	8.00	0.270	0.315	3		
E	9.65	10.67	0.380	0.420	2, 3		
E1	7.90	8.80	0.311	0.346	3		
е	2.54	2.54 BSC		) BSC			
L	13.46	14.10	0.530	0.555			
L1	-	1.65	-	0.065	3		
L2	3.36	3.71	0.132	0.146			

#### **Notes**

- (1) Dimensioning and tolerancing as per ASME Y14.5M-1994
- (2) Dimension D and E do not include mold flash. Mold flash shall not exceed 0.127 mm (0.005") per side. These dimensions are measured at the outmost extremes of the plastic body
- (3) Thermal pad contour optional within dimension E, L1, D1 and E1
- (4) Dimension b1 and c1 apply to base metal only
- 5) Controlling dimension: inches
- (6) Outline conform to JEDEC TO-262 except A1 (maximum), b (minimum), D1 (minimum) and L2 where dimensions derived the actual package outline

Revision: 11-Jul-2019 1 Document Number: 95419





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