



#### **50V P-CHANNEL ENHANCEMENT MODE MOSFET**

### **Product Summary**

BV <sub>DSS</sub>	R <sub>DS(ON)</sub> Max	I <sub>D</sub> Max T <sub>A</sub> = +25°C
-50V	10.0Ω @ V <sub>GS</sub> = -5V	-174mA

### **Description and Applications**

This MOSFET has been designed to minimize the on-state resistance (RDS(ON)) and yet maintain superior switching performance, making it ideal for high efficiency power management applications.

- Motor Control
- Power Management Functions
- Backlighting

### **Features and Benefits**

- Low On-Resistance
- Low Input Capacitance
- Fast Switching Speed
- Low Input/Output Leakage
- Totally Lead-Free & Fully RoHS Compliant (Notes 1 & 2)
- Halogen and Antimony Free. "Green" Device (Note 3)
- For automotive applications requiring specific change control (i.e. parts qualified to AEC-Q100/101/200, PPAP capable, and manufactured in IATF 16949 certified facilities), please <u>contact us</u> or your local Diodes representative. <a href="https://www.diodes.com/quality/product-definitions/">https://www.diodes.com/quality/product-definitions/</a>

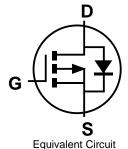
#### **Mechanical Data**

- Case: SOT323
- Case Material: Molded Plastic, "Green" Molding Compound. UL Flammability Classification Rating 94V-0
- Moisture Sensitivity: Level 1 per J-STD-020
- Terminals: Finish Matte Tin Annealed over Alloy 42 Leadframe. Solderable per MIL-STD-202, Method 208 (3)
- Weight: 0.006 grams (Approximate)











Top View

G

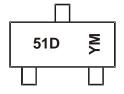
### Ordering Information (Note 4)

Part Number	Case	Packaging
DMP510DLW-7	SOT323	3000/Tape & Reel
DMP510DLW-13	SOT323	10000/Tape & Reel

Notes:

- 1. No purposely added lead. Fully EU Directive 2002/95/EC (RoHS), 2011/65/EU (RoHS 2) & 2015/863/EU (RoHS 3) compliant.
- 2. See https://www.diodes.com/quality/lead-free/ for more information about Diodes Incorporated's definitions of Halogen- and Antimony-free, "Green" and Lead-free.
- 3. Halogen- and Antimony-free "Green" products are defined as those which contain <900ppm bromine, <900ppm chlorine (<1500ppm total Br + Cl) and <1000ppm antimony compounds.
- 4. For packaging details, go to our website at https://www.diodes.com/design/support/packaging/diodes-packaging/.

# Marking Information



 $\begin{array}{l} \underline{51} \text{D= Product Type Marking Code} \\ \overline{Y} \text{M} = \text{Date Code Marking} \\ \text{Y or } \overline{Y} = \text{Year (ex: H = 2020)} \\ \text{M} = \text{Month (ex: 9 = September)} \end{array}$ 

Date Code Key

Year	2017		2020	2021	2022	2023	2024	2025	2026	2027	2028	2029
Code	Е		Н	ı	J	K	L	М	N	0	Р	R
<u> </u>												
Month	Jan	Feb	Mar	Apr	May	Jun	Jul	Aug	Sep	Oct	Nov	Dec



## **Maximum Ratings** (@ $T_A = +25$ °C, unless otherwise specified.)

Characteristic		Symbol	Value	Unit	
Drain-Source Voltage		$V_{DSS}$	-50	V	
Gate-Source Voltage			Vgss	±20	V
Continuous Drain Current (Note 6) Vgs = -5V	Steady State	$T_A = +25^{\circ}C$ $T_A = +70^{\circ}C$	lo	-174 -139	mA
Maximum Continuous Body Diode Forward Current	t (Note 6)	Is	-65	mA	
Pulsed Drain Current (10µs Pulse, Duty Cycle = 1%	6) (Note 6	)	IDM	-1.0	А

# Thermal Characteristics (@TA = +25°C, unless otherwise specified.)

Characteristic		Symbol	Value	Unit
Total Power Dissipation (Note 5)		PD	320	mW
Thermal Resistance, Junction to Ambient (Note 5)	Steady State	Reja	398	°C/W
Total Power Dissipation (Note 6)		P <sub>D</sub>	470	mW
Thermal Resistance, Junction to Ambient (Note 6)	Steady State	R <sub>0JA</sub>	273	°C/W
Operating and Storage Temperature Range		TJ, TSTG	-55 to +150	°C

# Electrical Characteristics (@TA = +25°C, unless otherwise specified.)

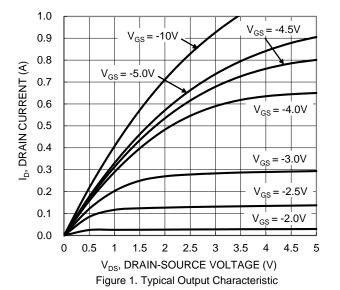
Characteristic	Symbol	Min	Тур	Max	Unit	Test Condition		
OFF CHARACTERISTICS (Note 7)								
Drain-Source Breakdown Voltage	BVDSS	-50	_	_	V	V <sub>G</sub> S = 0V, I <sub>D</sub> = -250µA		
Zero Gate Voltage Drain Current	I <sub>DSS</sub>	_	_	-1.0	μΑ	V <sub>DS</sub> = -50V, V <sub>GS</sub> = 0V		
Gate-Source Leakage	Igss		_	±100	nA	$V_{GS} = \pm 16V, V_{DS} = 0V$		
ON CHARACTERISTICS (Note 7)								
Gate Threshold Voltage	V <sub>GS(TH)</sub>	-0.8		-2.0	V	$V_{DS} = V_{GS}$ , $I_D = -1mA$		
Static Drain-Source On-Resistance	RDS(ON)	_		10	Ω	$V_{GS} = -5V, I_{D} = -0.1A$		
Diode Forward Voltage	VsD	_	-0.78	-1.5	V	V <sub>G</sub> S = 0V, I <sub>S</sub> = -100mA		
DYNAMIC CHARACTERISTICS (Note 8)		•		•				
Input Capacitance	Ciss		24.6	_	рF			
Output Capacitance	Coss	_	4.8	_	pF	V <sub>DS</sub> = -25V, V <sub>GS</sub> = 0V, f = 1.0MHz		
Reverse Transfer Capacitance	Crss	_	2.8	_	pF			
Gate Resistance	Rg	_	2000	_	Ω	$f = 1.0MHz$ , $V_{GS} = 0V$ , $V_{DS} = 0V$		
Total Gate Charge (V <sub>GS</sub> = -4.5V)	Qg	_	280	_	рС			
Total Gate Charge (V <sub>GS</sub> = -10V)	Qg	_	560	_	рС	V <sub>G</sub> S = -4.5V, V <sub>D</sub> S = -10V,		
Gate-Source Charge	Qgs	_	90	_	рС	I <sub>D</sub> = -100mA		
Gate-Drain Charge	Q <sub>gd</sub>	_	77	_	рС			
Turn-On Delay Time	t <sub>D(ON)</sub>	_	2.8	_	ns			
Turn-On Rise Time	t <sub>R</sub>	_	2.6	_	ns	$V_{DD} = -30V, I_{D} = -0.27A,$		
Turn-Off Delay Time	tD(OFF)	_	11.1	_	ns	RGEN = $50\Omega$ , VGS = $-10V$		
Turn-Off Fall Time	tF	_	7.2	_	ns			

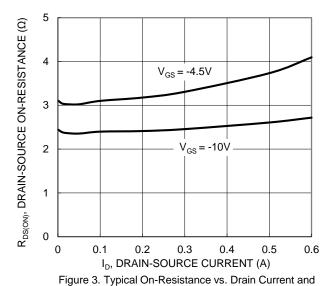
5. Device mounted on FR-4 PCB, with minimum recommended pad layout. Notes:

<sup>6.</sup> Device mounted on 1" x 1" FR-4 PCB with high coverage 2oz. Copper, single sided.
7. Short duration pulse test used to minimize self-heating effect.

<sup>8.</sup> Guaranteed by design. Not subject to product testing.







Gate Voltage

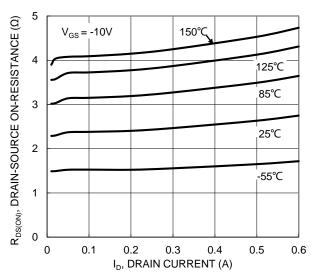


Figure 5. Typical On-Resistance vs. Drain Current and Temperature

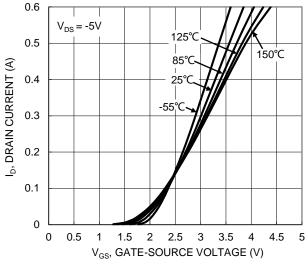


Figure 2. Typical Transfer Characteristic

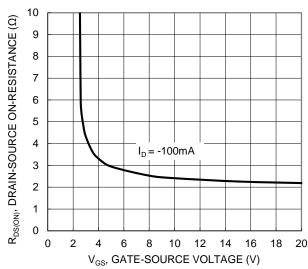


Figure 4. Typical Transfer Characteristic

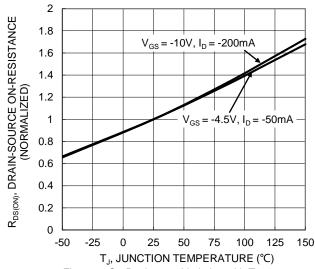


Figure 6. On-Resistance Variation with Temperature



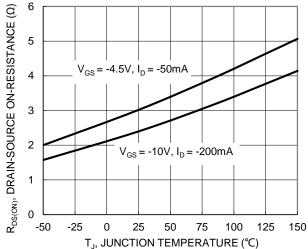
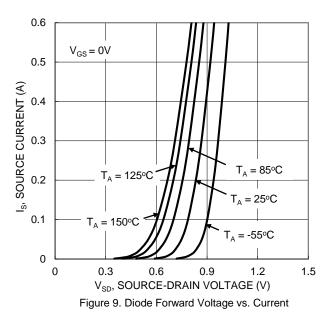
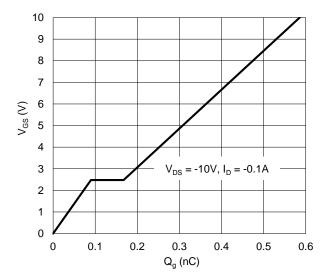


Figure 7. On-Resistance Variation with Temperature





 $V_{GS(TH)}, GATE THRESHOLD VOLTAGE (V)$ 1.8  $I_D = -1mA$ 1.6 1.4  $I_D = -250 \mu A$ 1.2 -25 25 50 75 100 125 -50 150  $T_J$ , JUNCTION TEMPERATURE (°C)

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Figure 8. Gate Threshold Variation vs. Junction Temperature

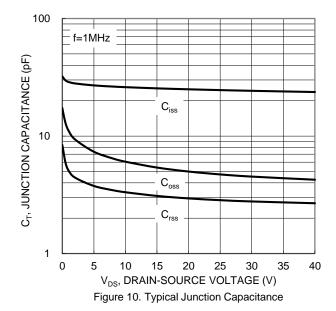


Figure 11. Gate Charge



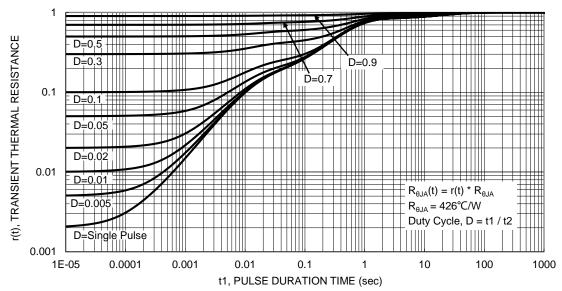


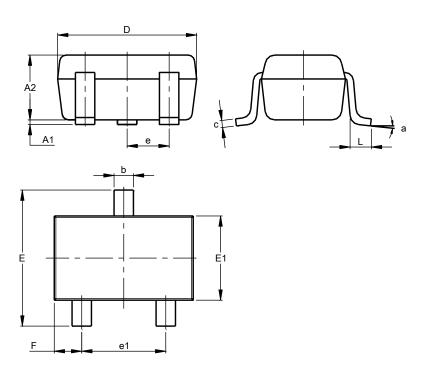
Figure 12. Transient Thermal Resistance



## **Package Outline Dimensions**

Please see http://www.diodes.com/package-outlines.html for the latest version.

#### **SOT323**

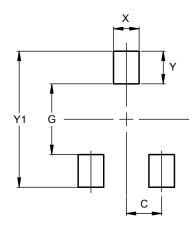


SOT323							
Dim	Min	Max	Тур				
A1	0.00	0.10	0.05				
A2	0.90	1.00	0.95				
b	0.25	0.40	0.30				
С	0.10	0.18	0.11				
D	1.80	2.20	2.15				
Е	2.00	2.20	2.10				
E1	1.15	1.35	1.30				
е	0.650 BSC						
e1	1.20	1.40	1.30				
F	0.375	0.475	0.425				
١	0.25	0.40	0.30				
а	0°	8°					
All Dimensions in mm							

# **Suggested Pad Layout**

Please see http://www.diodes.com/package-outlines.html for the latest version.

#### SOT323



Dimensions	Value (in mm)
С	0.650
G	1.300
Х	0.470
Y	0.600
Y1	2.500



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