

CMLSH2-4T

SURFACE MOUNT SILICON
TRIPLE ISOLATED
LOW V_F SCHOTTKY DIODE



SOT-563 CASE



www.centrasemi.com

DESCRIPTION:

The CENTRAL SEMICONDUCTOR CMLSH2-4T consists of three (3) isolated silicon Schottky diodes, manufactured in an SOT-563 surface mount package. This device is designed for switching applications requiring a low forward voltage drop.

MARKING CODE: CT4

• Device is **Halogen Free** by design

MAXIMUM RATINGS: ($T_A=25^{\circ}\text{C}$)

	SYMBOL		UNITS
Peak Repetitive Reverse Voltage	V_{RRM}	40	V
Continuous Forward Current	I_F	200	mA
Peak Repetitive Forward Current	I_{FRM}	300	mA
Peak Forward Surge Current, $t_p=10\text{ms}$	I_{FSM}	600	mA
Power Dissipation (Note 1)	P_D	350	mW
Power Dissipation (Note 2)	P_D	300	mW
Power Dissipation (Note 3)	P_D	150	mW
Operating and Storage Junction Temperature	T_J, T_{stg}	-65 to +150	$^{\circ}\text{C}$
Thermal Resistance (Note 1)	Θ_{JA}	357	$^{\circ}\text{C/W}$

ELECTRICAL CHARACTERISTICS PER DIODE: ($T_A=25^{\circ}\text{C}$ unless otherwise noted)

SYMBOL	TEST CONDITIONS	MIN	TYP	MAX	UNIT
I_R	$V_R=25\text{V}$		100	500	nA
BV_R	$I_R=100\mu\text{A}$	40	55		V
V_F	$I_F=100\mu\text{A}$		0.24	0.26	V
V_F	$I_F=500\mu\text{A}$		0.29	0.31	V
V_F	$I_F=1.0\text{mA}$		0.31	0.33	V
V_F	$I_F=50\text{mA}$		0.54	0.60	V
V_F	$I_F=100\text{mA}$		0.67	0.75	V
C_J	$V_R=1.0\text{V}$, $f=1.0\text{MHz}$			10	pF
t_{rr}	$I_F=I_R=10\text{mA}$, $I_{rr}=1.0\text{mA}$, $R_L=100\Omega$			5.0	ns

Notes: (1) Ceramic or aluminum core PC Board with copper mounting pad area of 4.0mm^2
(2) FR-4 Epoxy PC Board with copper mounting pad area of 4.0mm^2
(3) FR-4 Epoxy PC Board with copper mounting pad area of 1.4mm^2

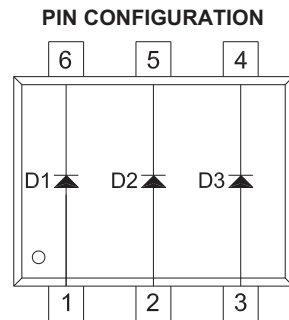
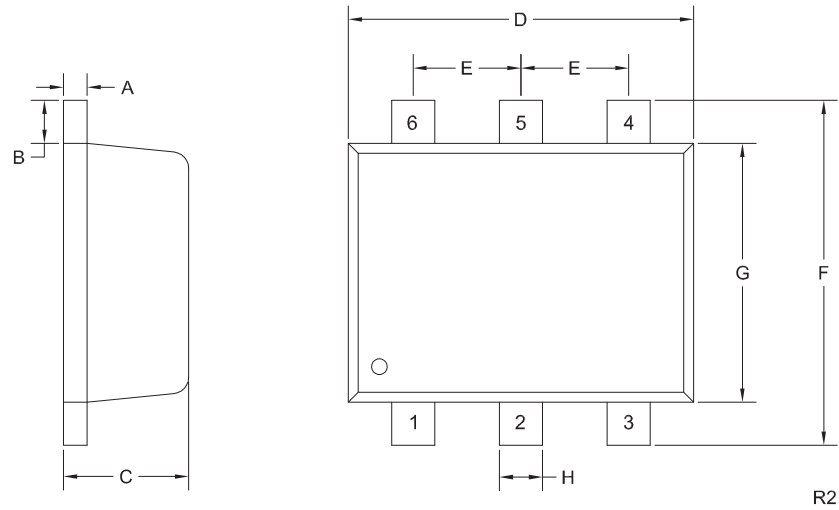
R5 (1-July 2015)

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SOT-563 CASE - MECHANICAL OUTLINE



DIMENSIONS				
SYMBOL	INCHES		MILLIMETERS	
	MIN	MAX	MIN	MAX
A	0.0027	0.007	0.07	0.18
B	0.008		0.20	
C	0.017	0.024	0.45	0.60
D	0.059	0.067	1.50	1.70
E	0.020		0.50	
F	0.059	0.067	1.50	1.70
G	0.043	0.051	1.10	1.30
H	0.006	0.012	0.15	0.30

SOT-563 (REV: R2)

LEAD CODE:

- 1) Anode D1
- 2) Anode D2
- 3) Anode D3
- 4) Cathode D3
- 5) Cathode D2
- 6) Cathode D1

MARKING CODE: CT4

R5 (1-July 2015)

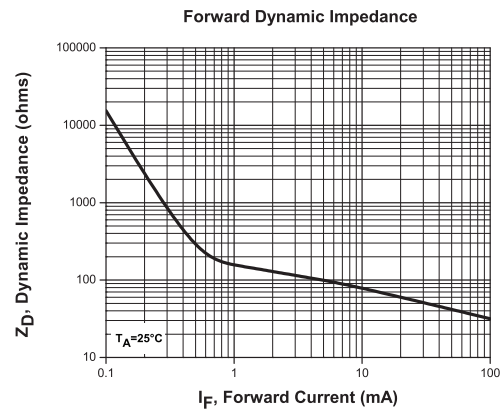
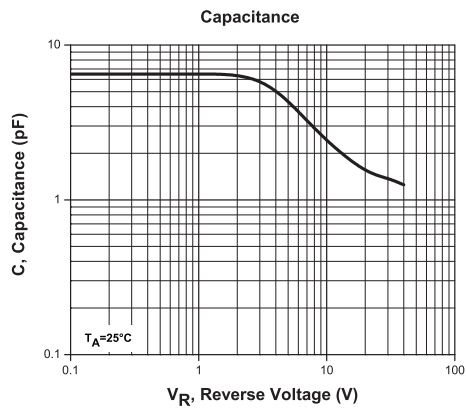
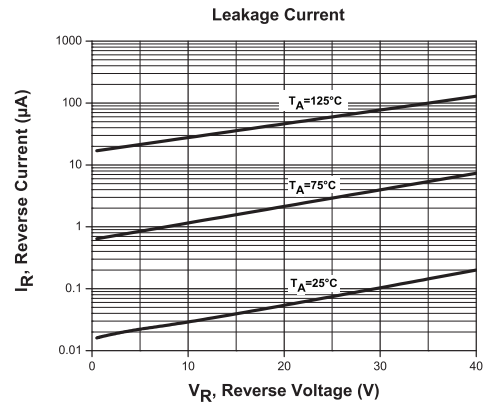
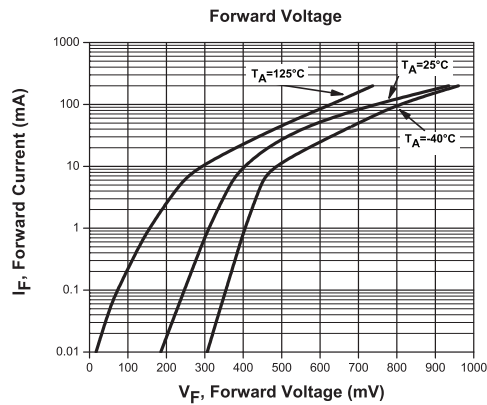
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TYPICAL ELECTRICAL CHARACTERISTICS



R5 (1-July 2015)

OUTSTANDING SUPPORT AND SUPERIOR SERVICES



PRODUCT SUPPORT

Central's operations team provides the highest level of support to insure product is delivered on-time.

- Supply management (Customer portals)
- Inventory bonding
- Consolidated shipping options
- Custom bar coding for shipments
- Custom product packing

DESIGNER SUPPORT/SERVICES

Central's applications engineering team is ready to discuss your design challenges. Just ask.

- Free quick ship samples (2nd day air)
- Online technical data and parametric search
- SPICE models
- Custom electrical curves
- Environmental regulation compliance
- Customer specific screening
- Up-screening capabilities
- Special wafer diffusions
- PbSn plating options
- Package details
- Application notes
- Application and design sample kits
- Custom product and package development

REQUESTING PRODUCT PLATING

1. If requesting Tin/Lead plated devices, add the suffix "TIN/LEAD" to the part number when ordering (example: 2N2222A TIN/LEAD).
2. If requesting Lead (Pb) Free plated devices, add the suffix "PBFREE" to the part number when ordering (example: 2N2222A PBFREE).

CONTACT US

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