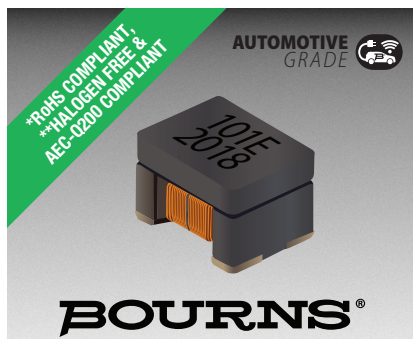




Features

- Shielded construction – low radiation
- Bifilar wound
- Designed to meet 2-wire UTP (Unshielded Twisted Pair) cable automotive audio bus system technology applications
- AEC-Q200 compliant
- RoHS compliant* and halogen free**



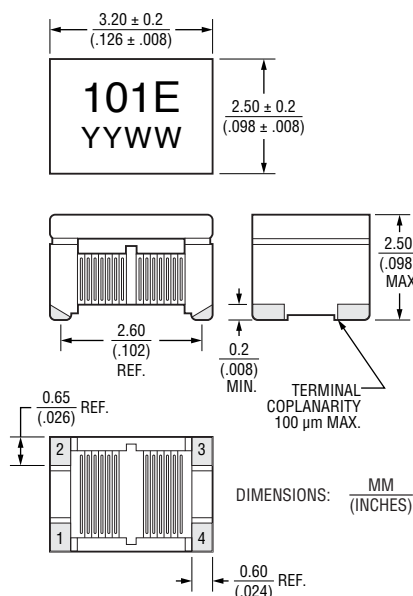
This model is not recommended for new designs. Contact your local Bourns representative for alternative solutions.

SRF3225AB – Common Mode Chip Inductor

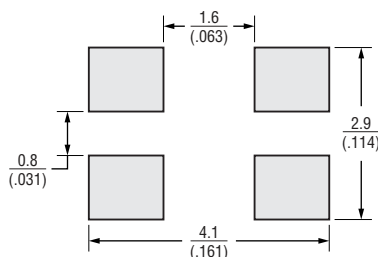
Electrical Specifications @ 25 °C

Bourns Part Number	Inductance @ 100 KHz / 0.1 V		Leakage Inductance (μH) Typ. @ 100 kHz / 0.1 V	DCR (Ω) Max.	IDC (A) Typ.	Common Mode Impedance (Ω) @ 10 MHz	
	L (μH)	Tol. (%)				Typ.	Min.
SRF3225AB-101Y	100	± 50	0.60	3.5	0.1	5500	2000

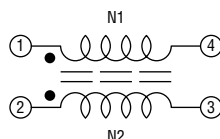
Product Dimensions



Recommended Layout



Electrical Schematic



Additional Information

Click these links for more information:



PRODUCT
SELECTOR



TECHNICAL
LIBRARY



INVENTORY



SAMPLES



CONTACT

General Specifications

Rated Voltage 50 VDC
 Insulation Resistance 10 MΩ (min.) @ 50 VDC
 Operating Temperature -40 °C to +125 °C
 (Temperature rise included)
 Storage Temperature -40 °C to +125 °C
 Temperature Rise 40 °C at rated IDC

Materials

Core Ferrite
 Wire Enameled copper
 Terminal Finish Sn
 Packaging 1200 pcs. per 7-inch reel

How to Order

SRF3225AB - 101 Y

Model _____
 Value Code _____
 Tolerance _____

Typical Part Marking

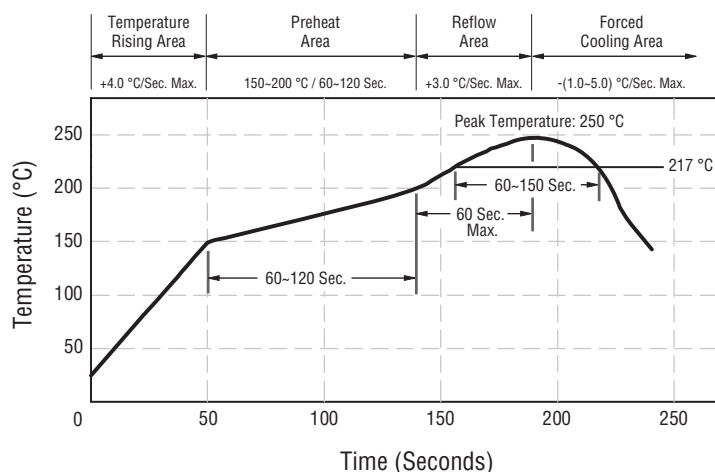


WARNING
 Cancer and Reproductive Harm
www.P65Warnings.ca.gov

* RoHS Directive 2015/863, Mar 31, 2015 and Annex.
 ** Bourns considers a product to be "halogen free" if (a) the Bromine (Br) content is 900 ppm or less; (b) the Chlorine (Cl) content is 900 ppm or less; and (c) the total Bromine (Br) and Chlorine (Cl) content is 1500 ppm or less.

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Soldering Profile

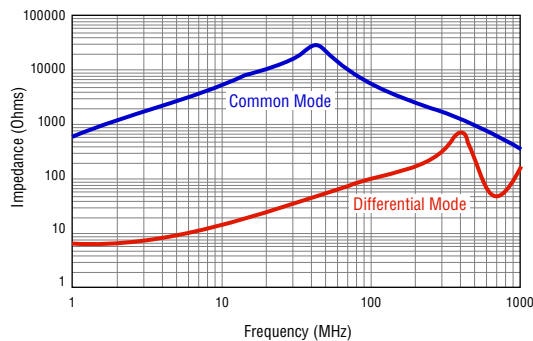


Peak Temperature: 250 °C max.
 Max. Peak Temperature -5 °C: 30 sec. max.
 Time Above 217 °C: 60-150 sec. max.

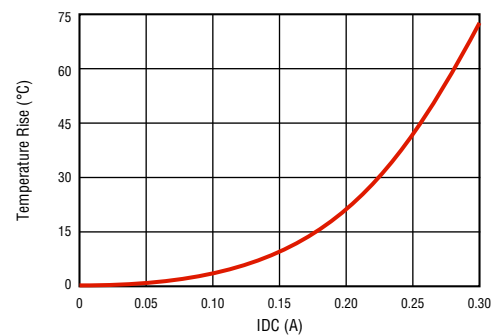
SRF3225AB Series – Common Mode Chip Inductor

BOURNS®

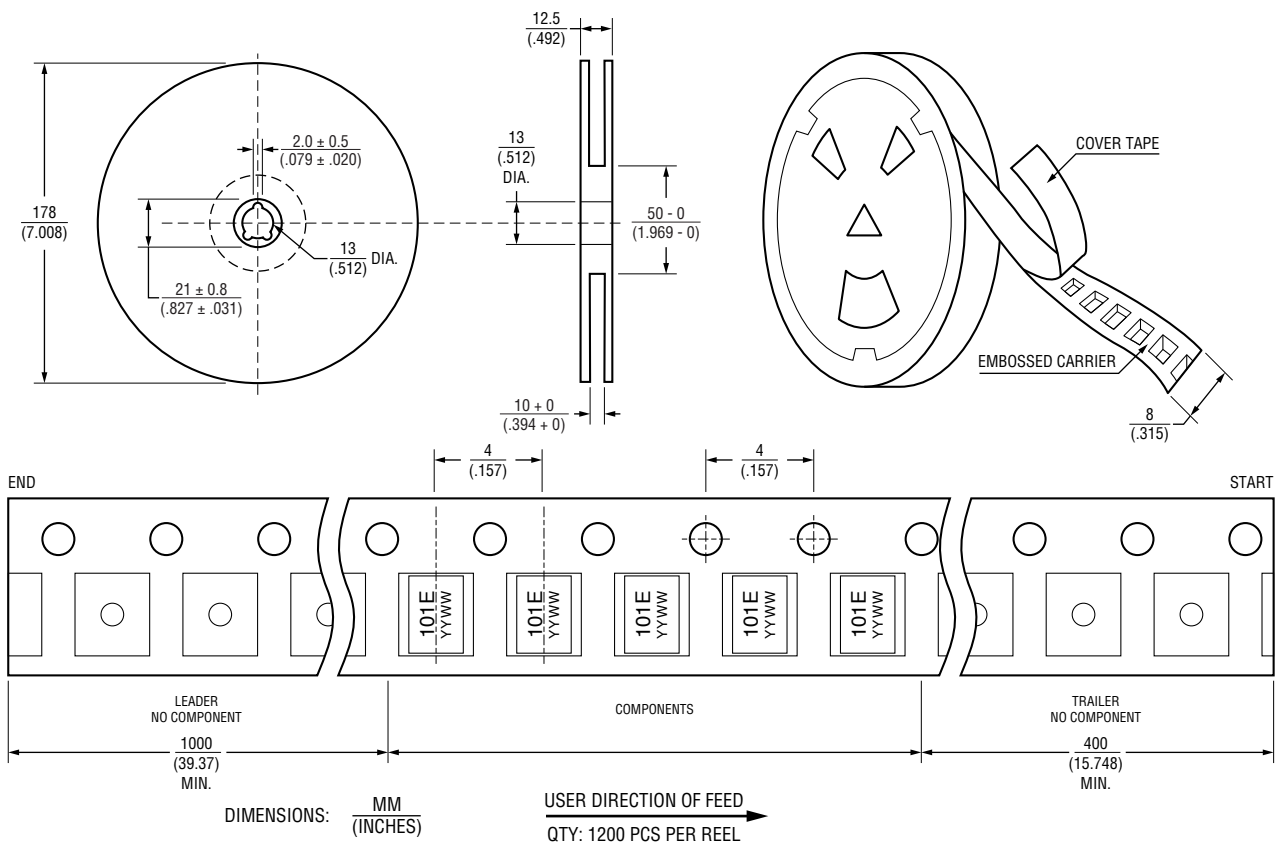
Typical Impedance vs. Frequency Curves



Temperature Rise Plot



Packaging Specifications



REV. 02/22

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Users should verify actual device performance in their specific applications.
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