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ON Semiconductor®

FDMC0310AS

N-Channel PowerTrench® SyncFETTM **30 V, 21 A, 4.4 m** Ω

Features

- Max $r_{DS(on)} = 4.4 \text{ m}\Omega$ at $V_{GS} = 10 \text{ V}$, $I_D = 19 \text{ A}$
- Max $r_{DS(on)} = 5.2 \text{ m}\Omega$ at $V_{GS} = 4.5 \text{ V}$, $I_D = 17.5 \text{ A}$
- Advanced package and silicon combination for low r_{DS(on)} and high efficiency
- SyncFETTM Schottky Body Diode
- MSL1 robust package design
- 100% UIL tested
- RoHS Compliant

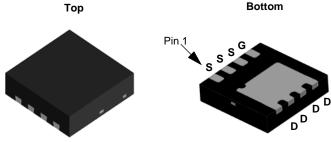


General Description

The FDMC0310AS has been designed to minimize losses in power conversion application. Advancements in both silicon and package technologies have been combined to offer the lowest r_{DS(on)} while maintaining excellent switching performance. This device has the added benefit of an efficient monolithic schottky body diode.

Applications

- Synchronous Rectifier for DC/DC Converters
- Notebook Vcore/GPU low side switch
- Networking Point of Load low side switch
- Telecom secondary side rectification





G S 3 6 2 S 1 S 8

MOSFET Maximum Ratings T_A = 25 °C unless otherwise noted

Symbol	Parameter		Ratings	Units	
V _{DS}	Drain to Source Voltage			30	V
V _{DSt}	Drain to Source Transient Voltage (t Transie	nt < 100 ns)		33	V
V_{GS}	Gate to Source Voltage		(Note 4)	±20	V
	Drain Current -Continuous	T _C = 25°C		21	
I _D	-Continuous	T _A = 25°C	(Note 1a)	19	Α
	-Pulsed			100	
E _{AS}	Single Pulse Avalance Energy		(Note 3)	66	mJ
Б	Power Dissipation	T _C = 25°C		36	W
P_{D}	Power Dissipation	T _A = 25°C	(Note 1a)	2.4	VV
T _J , T _{STG}	Operating and Storage Junction Temperature Range			-55 to +150	°C

Thermal Characteristics

$R_{\theta JC}$	Thermal Resistance, Junction to Case	3.4	°C/W
$R_{\theta JA}$	Thermal Resistance, Junction to Ambient (Note 1a)	53	C/VV

Package Marking and Ordering Information

Device Marking	Device	Package	Reel Size	Tape Width	Quantity
FDMC0310AS	FDMC0310AS	MLP 3.3X3.3	13 "	12 mm	3000 units

Electrical Characteristics $T_J = 25$ °C unless otherwise noted

Symbol	Parameter	Test Conditions	Min	Тур	Max	Units		
Off Characteristics								
BV_{DSS}	Drain to Source Breakdown Voltage	I _D = 1 mA, V _{GS} = 0 V	30			V		
$\frac{\Delta BV_{DSS}}{\Delta T_{J}}$	Breakdown Voltage Temperature Coefficient	I _D = 10 mA, referenced to 25 °C		26		mV/°C		
I _{DSS}	Zero Gate Voltage Drain Current	V _{DS} = 24 V, V _{GS} = 0 V			500	μΑ		
I _{GSS}	Gate to Source Leakage Current, Forward	V _{GS} = 20 V, V _{DS} = 0 V			100	nA		

On Characteristics

V _{GS(th)}	Gate to Source Threshold Voltage	$V_{GS} = V_{DS}$, $I_D = 1 \text{ mA}$	1.2	1.6	3.0	V
$\frac{\Delta V_{GS(th)}}{\Delta T_J}$	Gate to Source Threshold Voltage Temperature Coefficient	I _D = 10 mA, referenced to 25 °C		-5		mV/°C
r _{DS(on)}	Static Drain to Source On Resistance	V _{GS} = 10 V, I _D = 19 A		3.8	4.4	- mΩ
		$V_{GS} = 4.5 \text{ V}, I_D = 17.5 \text{ A}$		4.5	5.2	
		$V_{GS} = 10 \text{ V, } I_{D} = 19 \text{ A,}$ $T_{J} = 125 ^{\circ}\text{C}$		4.5	5.8	
g _{FS}	Forward Transconductance	V _{DS} = 5 V, I _D = 19 A		106		S

Dynamic Characteristics

C _{iss}	Input Capacitance	V _{DS} = 15 V, V _{GS} = 0 V, f = 1 MHz		2380	3165	pF
Coss	Output Capacitance			885	1175	pF
C _{rss}	Reverse Transfer Capacitance			100	150	pF
R_q	Gate Resistance		0.1	0.7	2.5	Ω

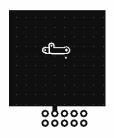
Switching Characteristics

t _{d(on)}	Turn-On Delay Time		11	20	ns
t _r	Rise Time	V _{DD} = 15 V, I _D = 19 A,	5	10	ns
t _{d(off)}	Turn-Off Delay Time	$V_{GS} = 10 \text{ V}, R_{GEN} = 6 \Omega$	30	48	ns
t _f	Fall Time		4	10	ns
Q_g	Total Gate Charge	V _{GS} = 0 V to 10 V	37	52	nC
Qg	Total Gate Charge	$V_{GS} = 0 \text{ V to } 4.5 \text{ V} V_{DD} = 15 \text{ V},$	18	25	nC
Q _{gs}	Gate to Source Charge	I _D = 19 A	6		nC
Q_{gd}	Gate to Drain "Miller" Charge		6		nC

Drain-Source Diode Characteristics

V _{SD}	Source to Drain Diode Forward Voltage	$V_{GS} = 0 V, I_{S} = 2 A$ (Note 2)	0.6	6.0	V
		$V_{GS} = 0 \text{ V, } I_S = 19 \text{ A}$ (Note 2)	0.8	3 1.2	_ v
t _{rr}	Reverse Recovery Time	$I_{E} = 19 \text{ A, di/dt} = 300 \text{ A/µs}$	29	47	ns
Q _{rr}	Reverse Recovery Charge	1F = 19 A, α/αι = 300 A/μs	33	53	nC

^{1.} R_{8JA} is determined with the device mounted on a 1 in² pad 2 oz copper pad on a 1.5 x 1.5 in. board of FR-4 material. R_{8JC} is guaranteed by design while R_{8CA} is determined by the user's board design.



a. 53 °C/W when mounted on a 1 in² pad of 2 oz copper.



b. 125 °C/W when mounted on a minimum pad of 2 oz copper.

- 2. Pulse Test: Pulse Width < 300 μ s, Duty cycle < 2.0%. 3. E_{AS} of 66 mJ is based on starting T_J = 25 °C, L = 0.3 mH, I_{AS} = 21 A, V_{DD} = 27 V, V_{GS} = 10 V. 100% tested at L= 3 mH, I_{AS} = 10.2 A. 4. As an N-ch device, the negative Vgs rating is for low duty cycle pulse occurrence only. No continuous rating is implied.

Typical Characteristics T_J = 25 °C unless otherwise noted

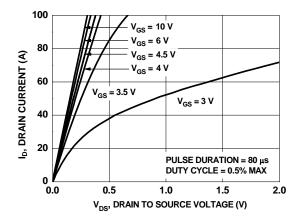


Figure 1. On Region Characteristics

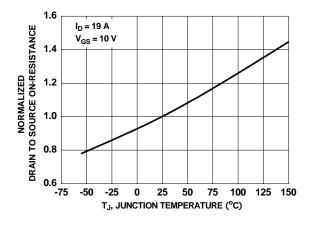


Figure 3. Normalized On Resistance vs. Junction Temperature

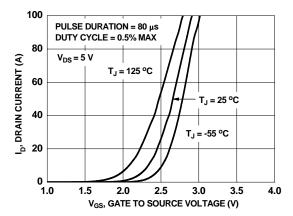


Figure 5. Transfer Characteristics

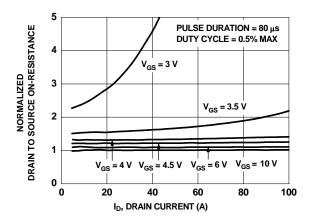


Figure 2. Normalized On-Resistance vs. Drain Current and Gate Voltage

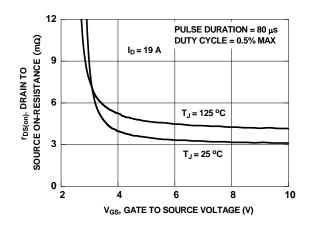


Figure 4. On-Resistance vs. Gate to Source Voltage

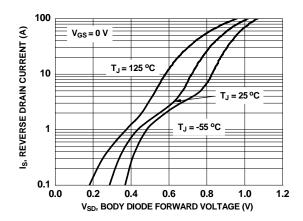


Figure 6. Source to Drain Diode Forward Voltage vs. Source Current

Typical Characteristics $T_J = 25$ °C unless otherwise noted

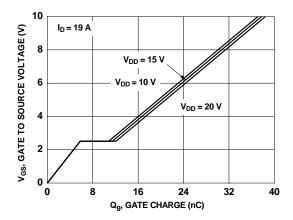


Figure 7. Gate Charge Characteristics

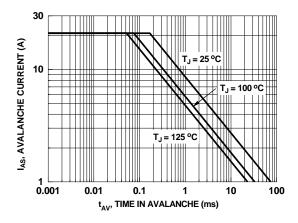


Figure 9. Unclamped Inductive Switching Capability

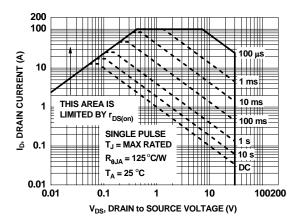


Figure 11. Forward Bias Safe Operating Area

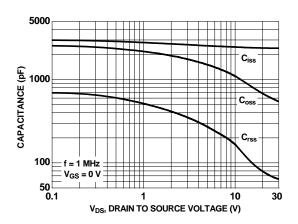


Figure 8. Capacitance vs. Drain to Source Voltage

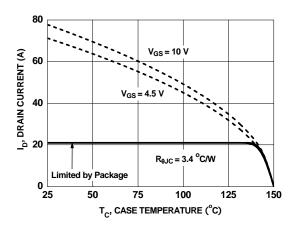


Figure 10. Maximum Continuous Drain Current vs. Case Temperature

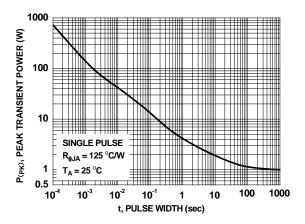


Figure 12. Single Pulse Maximum Power Dissipation

Typical Characteristics $T_J = 25$ °C unless otherwise noted

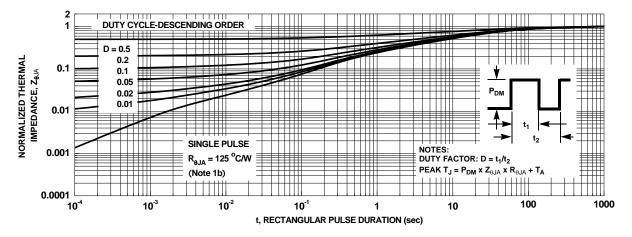


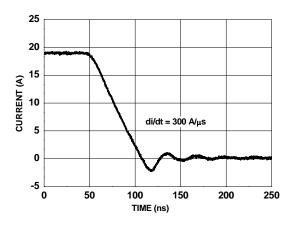
Figure 13. Junction-to-Ambient Transient Thermal Response Curve

Typical Characteristics (continued)

SyncFETTM Schottky body diode Characteristics

ON Semiconductor SyncFETTM process embeds a Schottky diode in parallel with PowerTrench MOSFET. This diode exhibits similar characteristics to a discrete external Schottky diode in parallel with a MOSFET. Figure 14 shows the reverse recovery characteristic of the FDMC0310AS.

Schottky barrier diodes exhibit significant leakage at high temperature and high reverse voltage. This will increase the power in the device.



0.001 T_J = 125 °C T_J = 100 °C T_J = 100 °C T_J = 25 °C V_{DS}, REVERSE VOLTAGE (V)

Figure 14. SyncFETTM Body Diode Reverse Recovery Characteristic

Figure 15. SyncFETTM Body Diode Reverse Leakage vs. Drain-Source Voltage

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