AUTOMOTIVE

COMPLIANT

FREE

GREEN

(5-2008)



# Vishay Semiconductors

# Standard SMD LED PLCC-2



#### **DESCRIPTION**

This device has been designed to meet the increasing demand for blue SMD LED.

The package of the VLMB41.. is the PLCC-2.

It consists of a lead frame which is embedded in a white thermoplast. The reflector inside this package is filled with clear epoxy.

#### PRODUCT GROUP AND PACKAGE DATA

Product group: LED
Package: SMD PLCC-2
Product series: standard
Angle of half intensity: ± 60°

#### **FEATURES**

- · High efficient InGaN technology
- EIA and ICE standard package
- Compatible with IR reflow, vapor phase and wave solder processes according to CECC 00802 and J-STD-020
- Available in 8 mm tape reel
- Preconditioning according to JEDEC<sup>®</sup> level 2 a
- ESD withstand voltage: up to 2 kV according to JESD22-A114-B
- AEC-Q101 qualified
- Material categorization: for definitions of compliance please see <a href="https://www.vishay.com/doc?99912">www.vishay.com/doc?99912</a>

#### **APPLICATIONS**

- Automotive: backlighting in dashboards and switches
- Telecommunication: indicator and backlighting in telephone and fax
- · Backlighting for audio and video equipment
- Backlighting in office equipment
- · Indoor and outdoor message boards
- · Flat backlight for LCDs, switches, and symbols
- Illumination purposes, alternative to incandescent lamps
- · General use

PARTS TABLE														
PART	COLOR	LUMINOUS INTENSITY (mcd)				at I <sub>F</sub>	FORWARD VOLTAGE (V)		at I <sub>F</sub> (mA)	TECHNOLOGY				
		MIN.	TYP.	MAX.		MIN.	TYP.	MAX.		MIN.	TYP.	MAX.		
VLMB41P1Q2-GS08	Blue	45	90	112	10	462	469	476	10	-	3.2	4.2	20	InGaN on Sapphire
VLMB41P1Q2-GS18	Blue	45	90	112	10	462	469	476	10	-	3.2	4.2	20	InGaN on Sapphire
VLMB41P2Q2-GS08	Blue	56	90	112	10	462	469	476	10	-	3.2	4.2	20	InGaN on Sapphire

ABSOLUTE MAXIMUM RATINGS (T <sub>amb</sub> = 25 °C, unless otherwise specified) VLMB41							
PARAMETER	TEST CONDITION	SYMBOL	VALUE	UNIT			
DC forward current	T <sub>amb</sub> ≤ 80 °C	I <sub>F</sub>	20	mA			
Surge forward current	t <sub>p</sub> ≤ 10 μs	I <sub>FSM</sub>	0.1	Α			
Power dissipation		Pv	84	mW			
Junction temperature		Tj	110	°C			
Operating temperature range		T <sub>amb</sub>	-40 to +100	°C			
Storage temperature range		T <sub>stg</sub>	-40 to +100	°C			
Thermal resistance junction to ambient	Mounted on PC board (pad size > 16 mm <sup>2</sup> )	R <sub>thJA</sub>	360	K/W			



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<b>OPTICAL AND ELECTRICAL CHARACTERISTICS</b> ( $T_{amb} = 25  ^{\circ}\text{C}$ , unless otherwise specified) <b>VLMB41, BLUE</b>							
PARAMETER	TEST CONDITION	PART	SYMBOL	MIN.	TYP.	MAX	UNIT
Luminous intensity	I 10 m 1	VLMB41P1Q2	Ι <sub>V</sub>	45	90	112	mcd
Luminous intensity	$I_F = 10 \text{ mA}$	VLMB41P2Q2	Ι <sub>V</sub>	56	90	112	mcd
Dominant wavelength	I <sub>F</sub> = 10 mA		$\lambda_{d}$	462	469	476	nm
Peak wavelength	I <sub>F</sub> = 10 mA		$\lambda_{p}$	=	464	-	nm
Angle of half intensity	I <sub>F</sub> = 10 mA		φ	-	± 60	-	0
Forward voltage	I <sub>F</sub> = 20 mA		$V_{F}$	=	3.2	4.2	V
Temperature coefficient of V <sub>F</sub>	I <sub>F</sub> = 10 mA		TC <sub>VF</sub>	-	-3	-	mV/K
Temperature coefficient of I <sub>V</sub>	I <sub>F</sub> = 10 mA		TC <sub>IV</sub>	-	-0.4	-	%/K

#### Note

· Not designed for reverse operation

LUMINOUS INTENSITY CLASSIFICATION							
GROUP	LIGHT INTENSITY (mcd)						
GROUP	OPTIONAL	MIN.	MAX.				
Р	1	45	56				
	2	56	71				
Q	1	71	90				
	2	90	112				

#### Note

 Luminous intensity is tested at a current pulse duration of 25 ms and an accuracy of ± 11 %.

These type numbers represent the order groups which include only a few brightness groups. Only one group will be shipped on each reel (there will be no mixing of two groups on each reel). In order to ensure availability, single brightness groups are not be orderable.

In a similar manner for colors where wavelength groups are measured and binned, single wavelength groups will be shipped on any one reel.

In order to ensure availability, single wavelength groups are not be orderable

COLOR CLASSIFICATION						
	BLUE					
GROUP	DOM. WAVELENGTH (nm)					
<u> </u>	MIN.	MAX.				
3	462	468				
4	466	472				
5	470	476				

#### Note

• Wavelengths are tested at a current pulse duration of 25 ms

CROSSING TABLE					
VISHAY	OSRAM				
VLMB41	LBT67C				

### TYPICAL CHARACTERISTICS (T<sub>amb</sub> = 25 °C, unless otherwise specified)

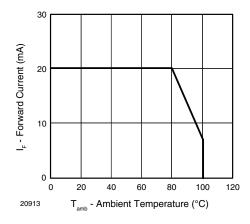


Fig. 1 - Forward Current vs. Ambient Temperature

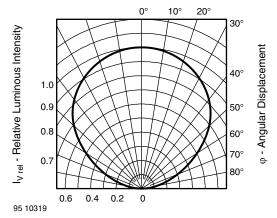


Fig. 2 - Relative Luminous Intensity vs. Angular Displacement



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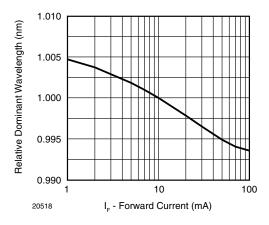


Fig. 3 - Relative Dominant Wavelength vs. Forward Current

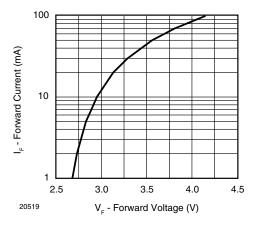


Fig. 4 - Forward Current vs. Forward Voltage

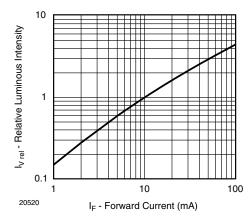


Fig. 5 - Relative Luminous Intensity vs. Forward Current

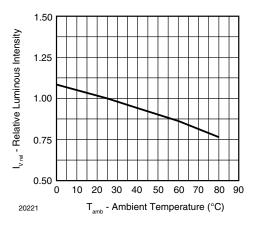


Fig. 6 - Relative Luminous Intensity vs. Ambient Temperature

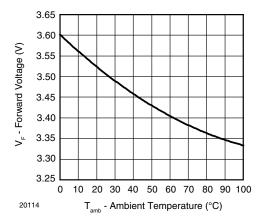
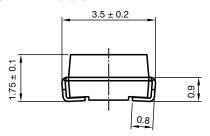
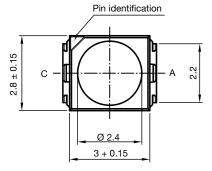


Fig. 7 - Forward Voltage vs. Ambient Temperature

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### **PACKAGE DIMENSIONS** in millimeters

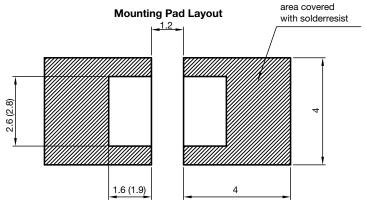




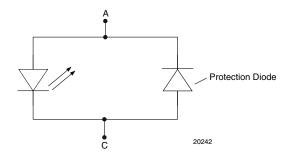


Dimensions in mm

Drawing-No.: 6.541-5089.01-4 Issue: 2; 23.09.13



Dimensions: reflow and vapor phase (wave soldering)

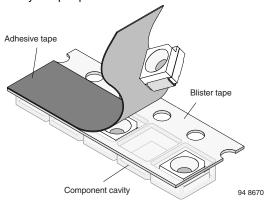




#### METHOD OF TAPING / POLARITY AND TAPE AND REEL

### SMD LED (VLM.3..., VLM.4...-SERIES)

Vishay's LEDs in SMD packages are available in an antistatic 8 mm blister tape (in accordance with DIN IEC 40 (CO) 564) for automatic component insertion. The blister tape is a plastic strip with impressed component cavities, covered by a top tape.



#### TAPING OF VLM.3..., VLM.4...

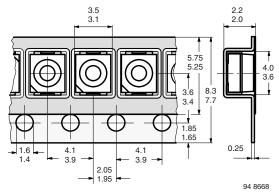


Fig. 8 - Tape Dimensions in mm for PLCC-2

### REEL PACKAGE DIMENSION IN MILLIMETERS FOR SMD LEDS, TAPE OPTION GS08 (= 1500 PCS.)

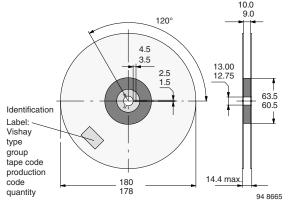


Fig. 9 - Reel Dimensions - GS08

### REEL PACKAGE DIMENSION IN MILLIMETERS FOR SMD LEDS, TAPE OPTION GS18 (= 8000 PCS.) PREFERRED

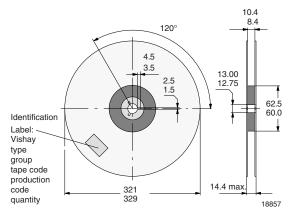


Fig. 10 - Reel Dimensions - GS18

#### SOLDERING PROFILE

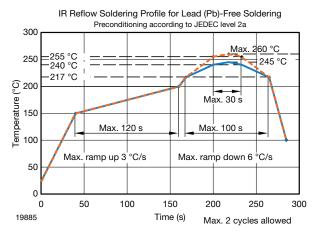


Fig. 11 - Vishay Lead (Pb)-free Reflow Soldering Profile (according to J-STD-020)

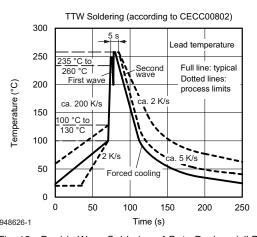


Fig. 12 - Double Wave Soldering of Opto Devices (all Packages)

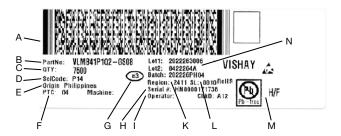
Rev. 1.9, 05-Jul-2022 5 Document Number: 81641

For technical questions, contact: LED@vishay.com



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### **BAR CODE PRODUCT LABEL** (example)



A. 2D barcode

B. Part No: Vishay part number

C. QTY: quantity

D. SelCode: selection bin code

E. Country of origin

F. PTC: production plant code

G. Termination finish

H. Region code

I. Serial#: serial number

K. Batch number: year, week, country code, plant code

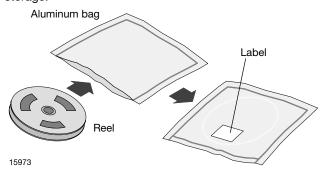
L. SL: storage location

M. Environmental symbols: RoHS, lead (Pb)-free,

halogen-free N. Lot numbers

#### **DRY PACKING**

The reel is packed in an anti-humidity bag to protect the devices from absorbing moisture during transportation and storage.



#### FINAL PACKING

The sealed reel is packed into a cardboard box. A secondary cardboard box is used for shipping purposes.

#### RECOMMENDED METHOD OF STORAGE

Dry box storage is recommended as soon as the aluminum bag has been opened to prevent moisture absorption. The following conditions should be observed, if dry boxes are not available:

- Storage temperature 10 °C to 30 °C
- Storage humidity ≤ 60 % RH max.

After more than 672 h under these conditions moisture content will be too high for reflow soldering.

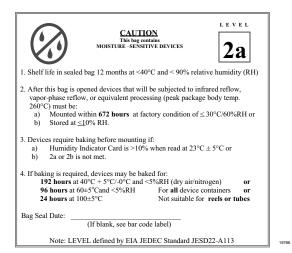
In case of moisture absorption, the devices will recover to the former condition by drying under the following condition:

192 h at 40 °C + 5 °C / - 0 °C and < 5 % RH (dry air / nitrogen) or

96 h at 60 °C + 5 °C and < 5 % RH for all device containers or

24 h at 100 °C + 5 °C not suitable for reel or tubes.

An EIA JEDEC standard JESD22-A112 level 2a label is included on all dry bags.



Example of JESD22-A112 level 2a label

### **ESD PRECAUTION**

Proper storage and handling procedures should be followed to prevent ESD damage to the devices especially when they are removed from the antistatic shielding bag. Electrostatic sensitive devices warning labels are on the packaging.

# VISHAY SEMICONDUCTORS STANDARD BAR CODE LABEL

The Vishay Semiconductors standard bar code labels are printed at final packing areas. The labels are on each packing unit and contain Vishay Semiconductors specific data.





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