

MOSFET – P-Channel, QFET

-60 V, -27 A, 70 mΩ

FQP27P06

Description

This P-Channel enhancement mode power MOSFET is produced using onsemi's proprietary planar stripe and DMOS technology. This advanced MOSFET technology has been especially tailored to reduce on-state resistance, and to provide superior switching performance and high avalanche energy strength. These devices are suitable for switched mode power supplies, audio amplifier, DC motor control, and variable switching power applications.

Features

- 27 A, -60 V, $R_{DS(on)} = 70 \text{ m}\Omega$ (Max.) @ $V_{GS} = -10 \text{ V}$, $I_D = -13.5 \text{ A}$
- Low Gate Charge (Typ. 33 nC)
- Low C_{rss} (Typ. 120 pF)
- 100% Avalanche Tested
- 175°C Maximum Junction Temperature Rating

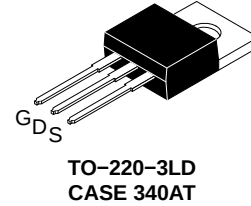
ABSOLUTE MAXIMUM RATINGS ($T_C = 25^\circ\text{C}$, unless otherwise noted)

Symbol	Parameter		FQP27P06	Unit
V _{DSS}	Drain-Source Voltage		-60	V
I _D	Drain Current	- Continuous (T _C = 25°C)	-27	A
		- Continuous (T _C = 100°C)	-19.1	
I _{DM}	Drain Current	- Pulsed (Note 1)	-108	A
V _{GSS}	Gate-Source Voltage		±25	V
E _{AS}	Single Pulsed Avalanche Energy (Note 2)		560	mJ
I _{AR}	Avalanche Current (Note 1)		-27	A
E _{AR}	Repetitive Avalanche Energy (Note 1)		12	mJ
dv/dt	Peak Diode Recovery dv/dt (Note 3)		-7.0	V/ns
P _D	Power Dissipation	(T _C = 25°C)	120	W
		- Derate Above 25°C	0.8	W/°C
T _J , T _{STG}	Operating and Storage Temperature Range		-55 to +175	°C
T _L	Maximum Lead Temperature for Soldering, 1/8" from Case for 5 Seconds		300	°C

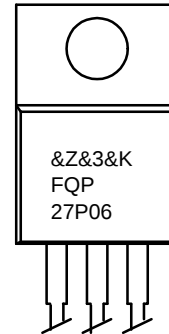
Stresses exceeding those listed in the Maximum Ratings table may damage the device. If any of these limits are exceeded, device functionality should not be assumed, damage may occur and reliability may be affected.

1. Repetitive Rating: Pulse width limited by maximum junction temperature
2. $L = 0.9 \text{ mH}$, $I_{AS} = -27 \text{ A}$, $V_{DD} = -25 \text{ V}$, $R_G = 25 \Omega$, Starting $T_J = 25^\circ\text{C}$
3. $I_{SD} \leq -27 \text{ A}$, $di/dt \leq 300 \text{ A}/\mu\text{s}$, $V_{DD} \leq BV_{DSS}$, starting $T_J = 25^\circ\text{C}$.

V_{DSS}	$R_{DS(on)}$ MAX	I_D MAX
-60 V	70 mΩ @ -10 V	-27 A

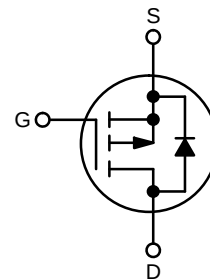


MARKING DIAGRAM



&Z = Assembly Plant Code
&3 = 3-Digit Date Code
&K = 2-Digits Lot Run Traceability Code
FQP27P06 = Specific Device Code

P-CHANNEL MOSFET



ORDERING INFORMATION

Part Number	Package	Shipping
FCP380N60	TO-220-3LD (Pb-Free, Halide Free)	1000 Units / Tube

FQP27P06

THERMAL CHARACTERISTICS

Symbol	Parameter	FQP27P06	Unit
$R_{\theta JC}$	Thermal Resistance, Junction-to-Case, Max.	1.25	°C/W
$R_{\theta CS}$	Thermal Resistance, Case-to-Sink, Typ.	0.5	°C/W
$R_{\theta JA}$	Thermal Resistance, Junction-to-Ambient, Max.	62.5	°C/W

ELECTRICAL CHARACTERISTICS ($T_C = 25^\circ\text{C}$ unless otherwise noted)

Symbol	Parameter	Test Conditions	Min	Typ	Max	Unit
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OFF CHARACTERISTICS

BV_{DSS}	Drain-Source Breakdown Voltage	$V_{GS} = 0\text{ V}, I_D = -250\text{ }\mu\text{A}$	-60	-	-	V
$\Delta BV_{DSS} / \Delta T_J$	Breakdown Voltage Temperature Coefficient	$I_D = -250\text{ }\mu\text{A}$, Referenced to 25°C	-	-0.06	-	V/°C
I_{DSS}	Zero Gate Voltage Drain Current	$V_{DS} = -60\text{ V}, V_{GS} = 0\text{ V}$	-	-	-1	μA
		$V_{DS} = -48\text{ V}, T_C = 150^\circ\text{C}$	-	-	-10	
I_{GSSF}	Gate-Body Leakage Current, Forward	$V_{GS} = -25\text{ V}, V_{DS} = 0\text{ V}$	-	-	-100	nA
I_{GSSR}	Gate-Body Leakage Current, Reverse	$V_{GS} = 25\text{ V}, V_{DS} = 0\text{ V}$	-	-	100	nA

ON CHARACTERISTICS

$V_{GS(th)}$	Gate Threshold Voltage	$V_{DS} = V_{GS}, I_D = -250\text{ }\mu\text{A}$	-2.0	-	-4.0	V
$R_{DS(on)}$	Static Drain-Source On Resistance	$V_{GS} = -10\text{ V}, I_D = -13.5\text{ A}$	-	0.055	0.07	Ω
g_{FS}	Forward Transconductance	$V_{DS} = -30\text{ V}, I_D = -13.5\text{ A}$	-	12.4	-	S

DYNAMIC CHARACTERISTICS

C_{iss}	Input Capacitance	$V_{DS} = -25\text{ V}, V_{GS} = 0\text{ V}, f = 1.0\text{ MHz}$	-	1100	1400	pF
C_{oss}	Output Capacitance		-	510	660	pF
C_{rss}	Reverse Transfer Capacitance		-	120	155	pF

SWITCHING CHARACTERISTICS

$t_{d(on)}$	Turn-On Delay Time	$V_{DD} = -30\text{ V}, I_D = -13.5\text{ A}, R_G = 25\text{ }\Omega$ (Note 4)	-	18	45	ns
t_r	Turn-On Rise Time		-	185	380	ns
$t_{d(off)}$	Turn-Off Delay Time		-	30	70	ns
t_f	Turn-Off Fall Time		-	90	190	ns
Q_g	Total Gate Charge	$V_{DS} = -48\text{ V}, I_D = -27\text{ A}, V_{GS} = -10\text{ V}$ (Note 4)	-	33	43	nC
Q_{gs}	Gate-Source Charge		-	6.8	-	nC
Q_{gd}	Gate-Drain Charge		-	18	-	nC

DRAIN-SOURCE DIODE CHARACTERISTICS AND MAXIMUM RATINGS

I _S	Maximum Continuous Drain–Source Diode Forward Current		–	–	–27	A
I _{SM}	Maximum Pulsed Drain–Source Diode Forward Current		–	–	–108	A
V _{SD}	Drain–Source Diode Forward Voltage	V _{GS} = 0 V, I _{SD} = –27 A	–	–	–4.0	V
t _{rr}	Reverse Recovery Time	V _{GS} = 0 V, I _{SD} = –27 A, dI _F /dt = 100 A/μs	–	105	–	ns
Q _{rr}	Reverse Recovery Charge		–	0.41	–	μC

Product parametric performance is indicated in the Electrical Characteristics for the listed test conditions, unless otherwise noted. Product performance may not be indicated by the Electrical Characteristics if operated under different conditions.

4. Essentially independent of operating temperature



TYPICAL CHARACTERISTICS

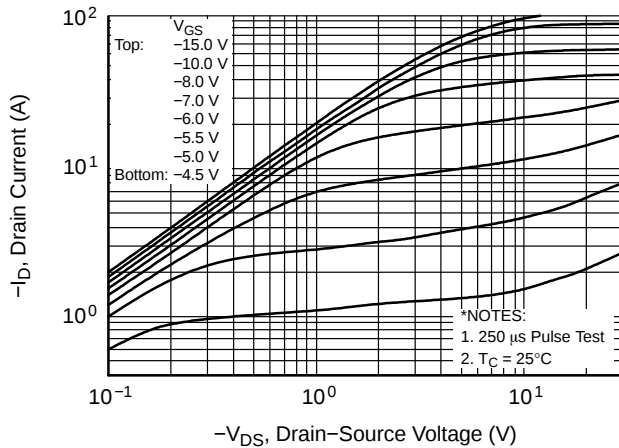


Figure 1. On-Region Characteristics

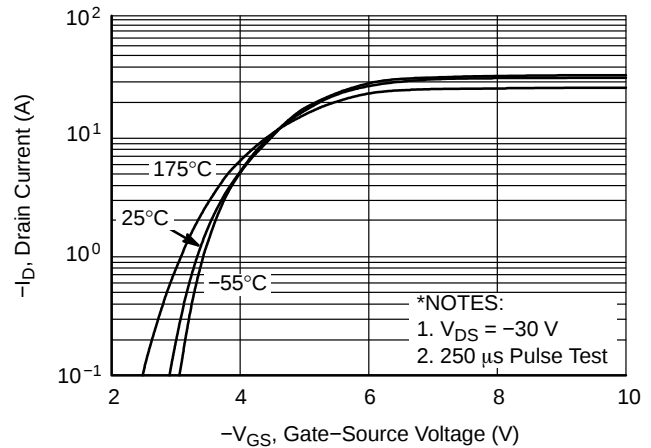


Figure 2. Transfer Characteristics

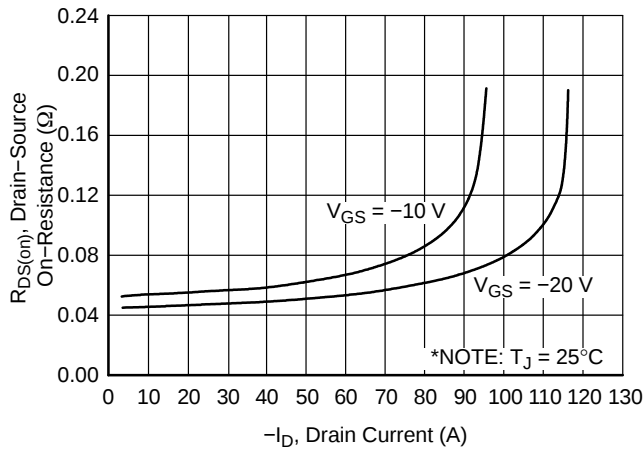


Figure 3. On-Resistance Variation vs. Drain Current and Gate Voltage

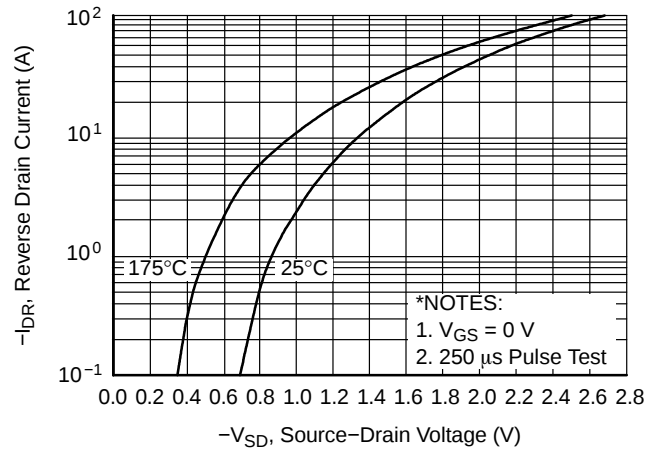


Figure 4. Body Diode Forward Voltage Variation vs. Source Current and Temperature

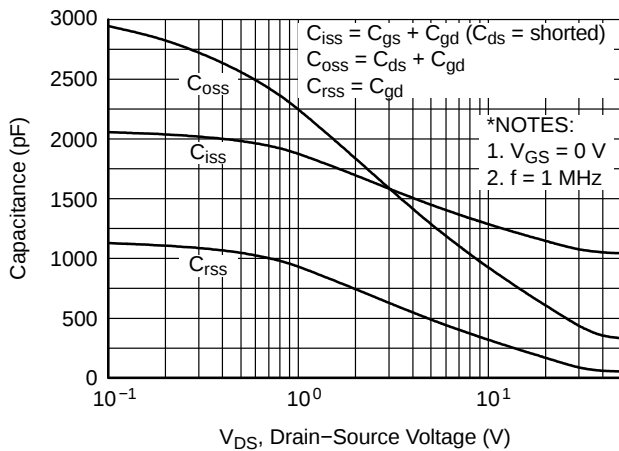


Figure 5. Capacitance Characteristics

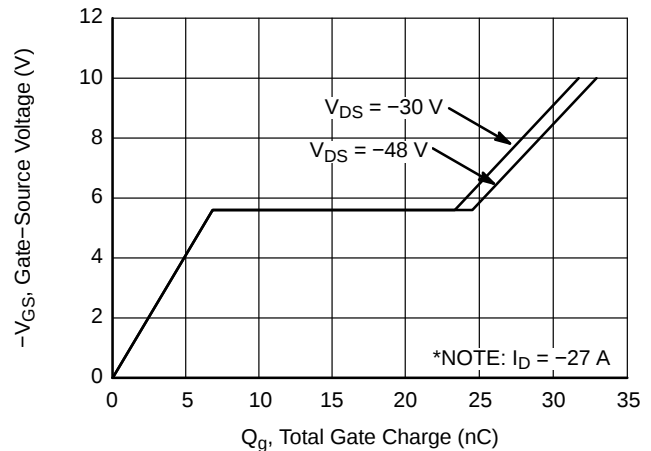


Figure 6. Gate Charge Characteristics

TYPICAL CHARACTERISTICS (CONTINUED)

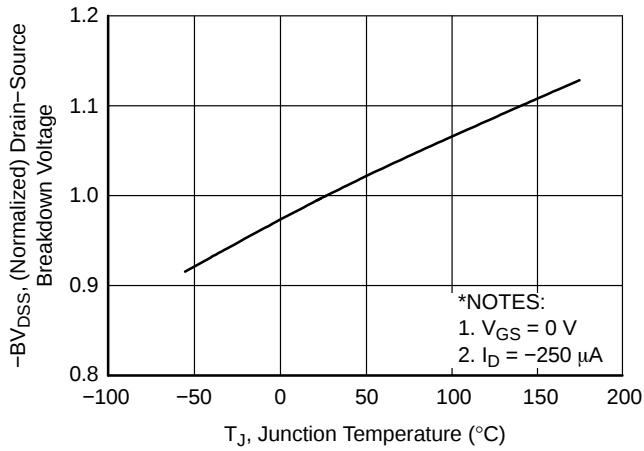


Figure 7. Breakdown Voltage Variation vs. Temperature

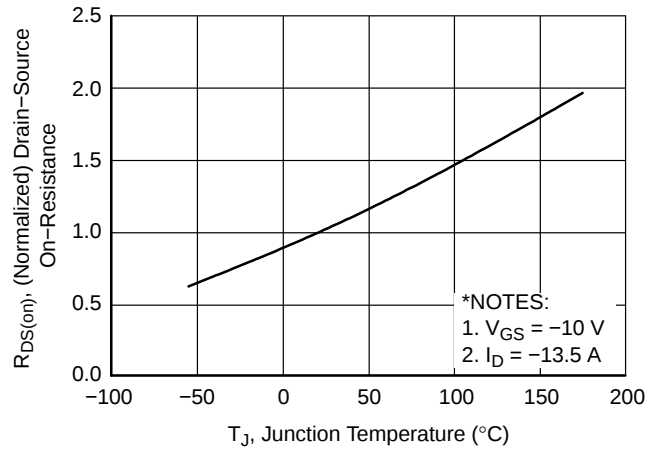


Figure 8. On-Resistance Variation vs. Temperature

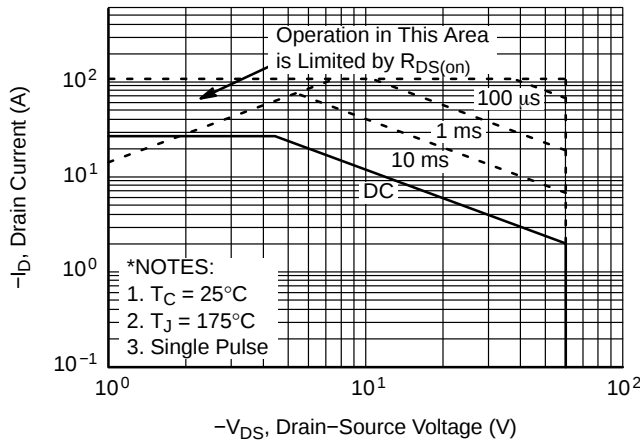


Figure 9. Maximum Safe Operating Area

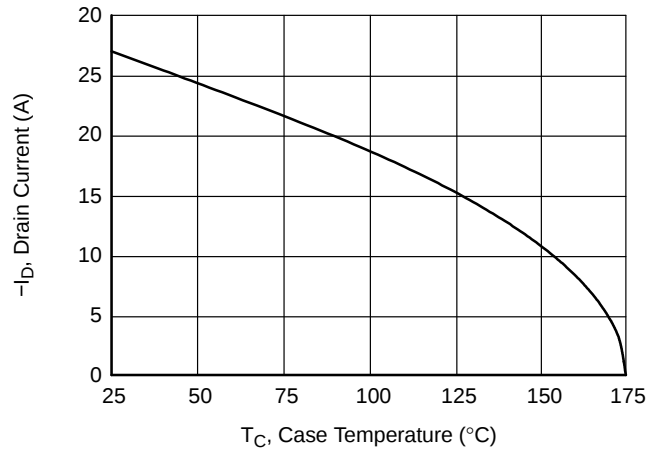


Figure 10. Maximum Drain Current vs. Case Temperature

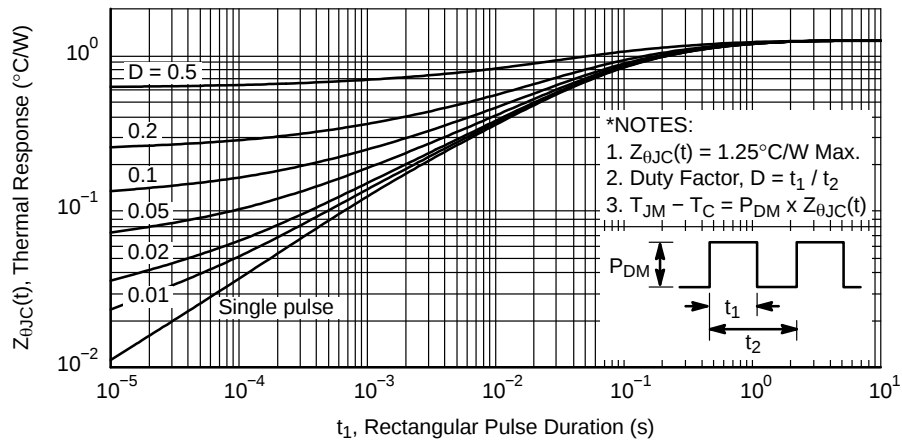


Figure 11. Transient Thermal Response Curve

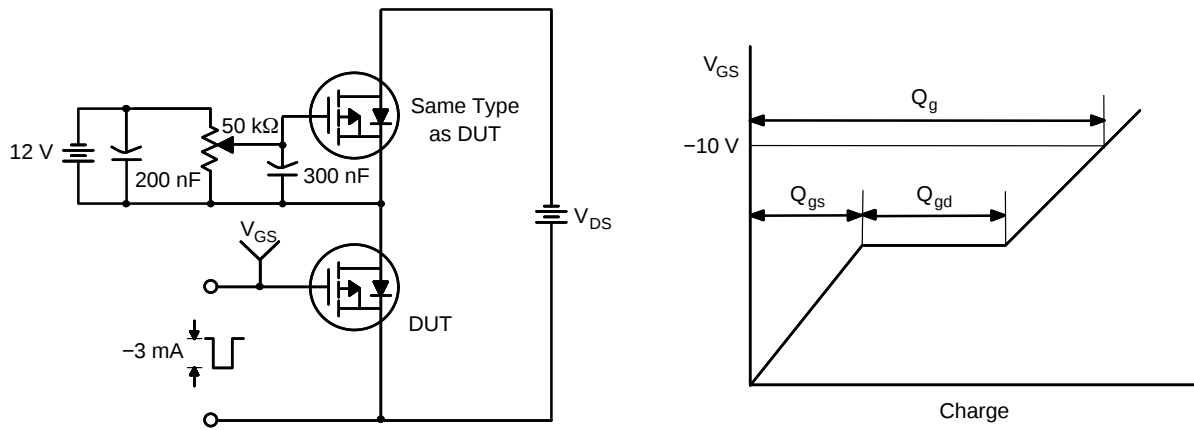


Figure 12. Gate Charge Test Circuit & Waveform

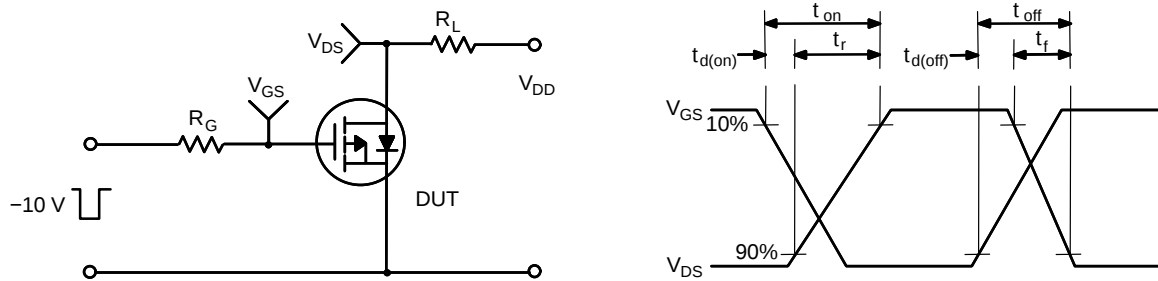


Figure 13. Resistive Switching Test Circuit & Waveforms

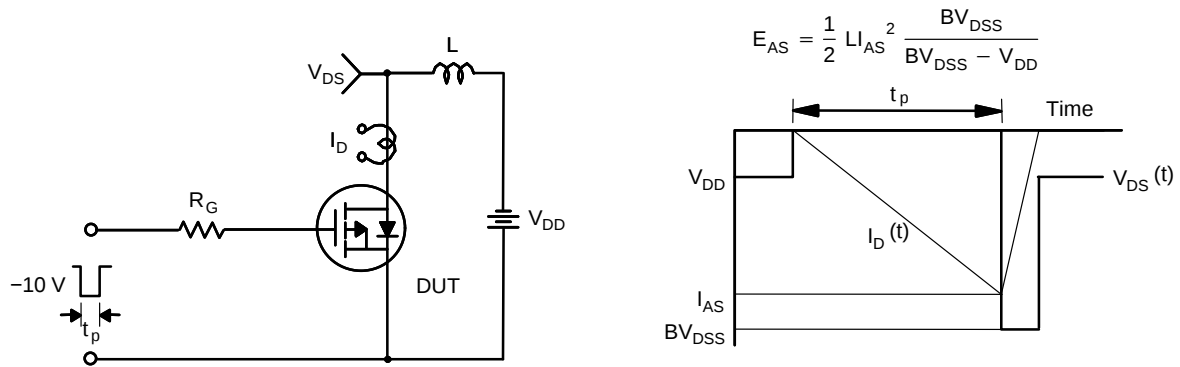


Figure 14. Unclamped Inductive Switching Test Circuit & Waveforms

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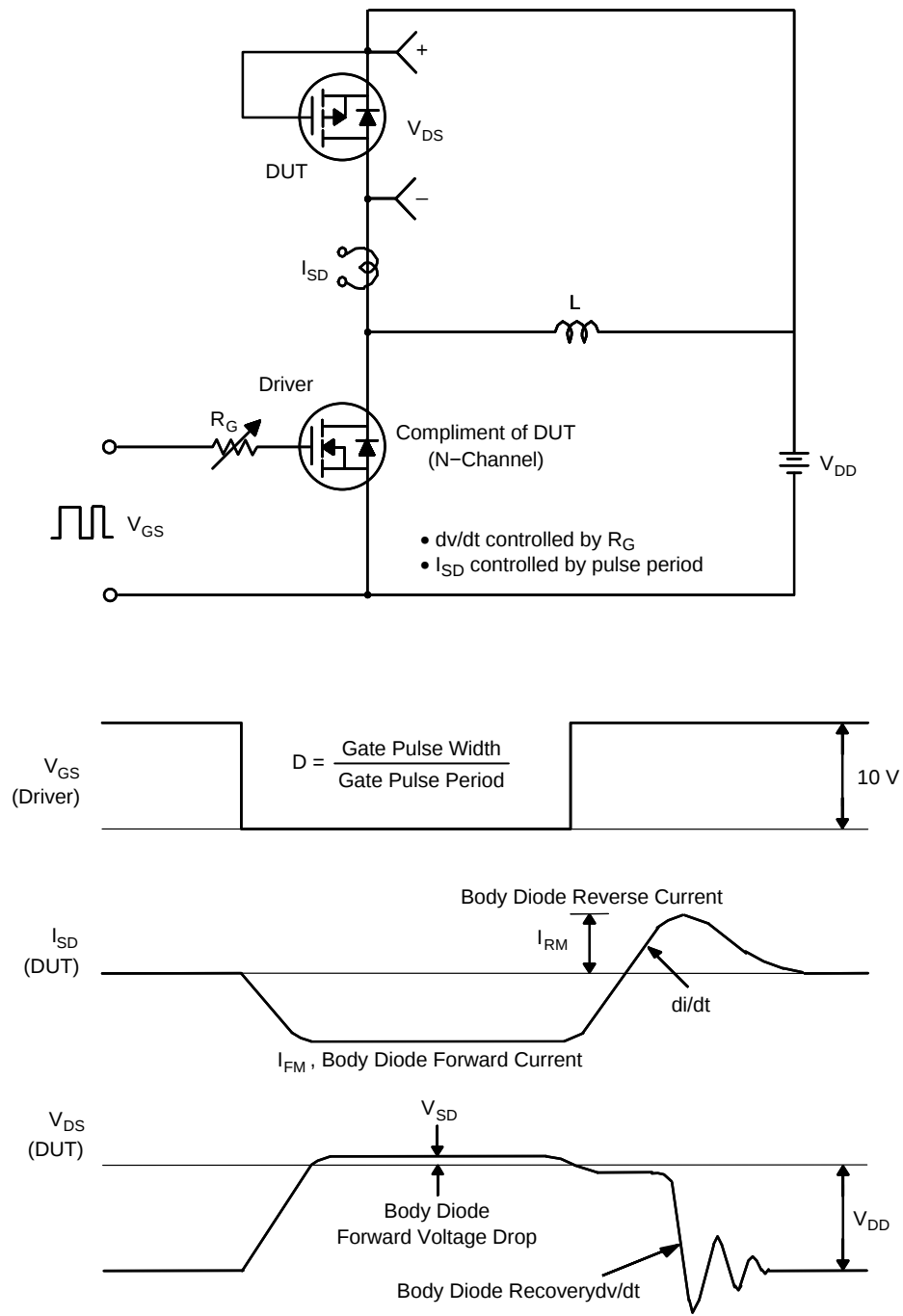
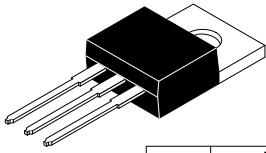


Figure 15. Peak Diode Recovery dv/dt Test Circuit & Waveforms



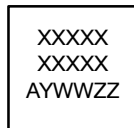
TO-220-3LD CASE 340AT ISSUE B

DATE 08 AUG 2022

DIM	MILLIMETERS		
	MIN.	NOM.	MAX.
A	4.00	--	4.70
A1	SEE NOTE "F"		
A2	2.10	--	2.85
b	0.55	--	1.00
b2	1.10	--	1.62
b4	1.42	--	1.62
c	0.36	--	0.60
D	13.90	--	16.30
D1	8.13	--	9.40
D2	11.50	--	14.30
D3	15.42	--	16.51
E	9.65	--	10.67
E1	7.59	--	8.65
e	2.40	--	2.67
H1	6.06	--	6.69
L	12.70	--	14.04
L1	2.70	--	4.10
P	3.50	--	4.00
Q	2.50	--	3.40
z	2.13 REF		
z1	2.06 REF		
θ	3°	--	5°

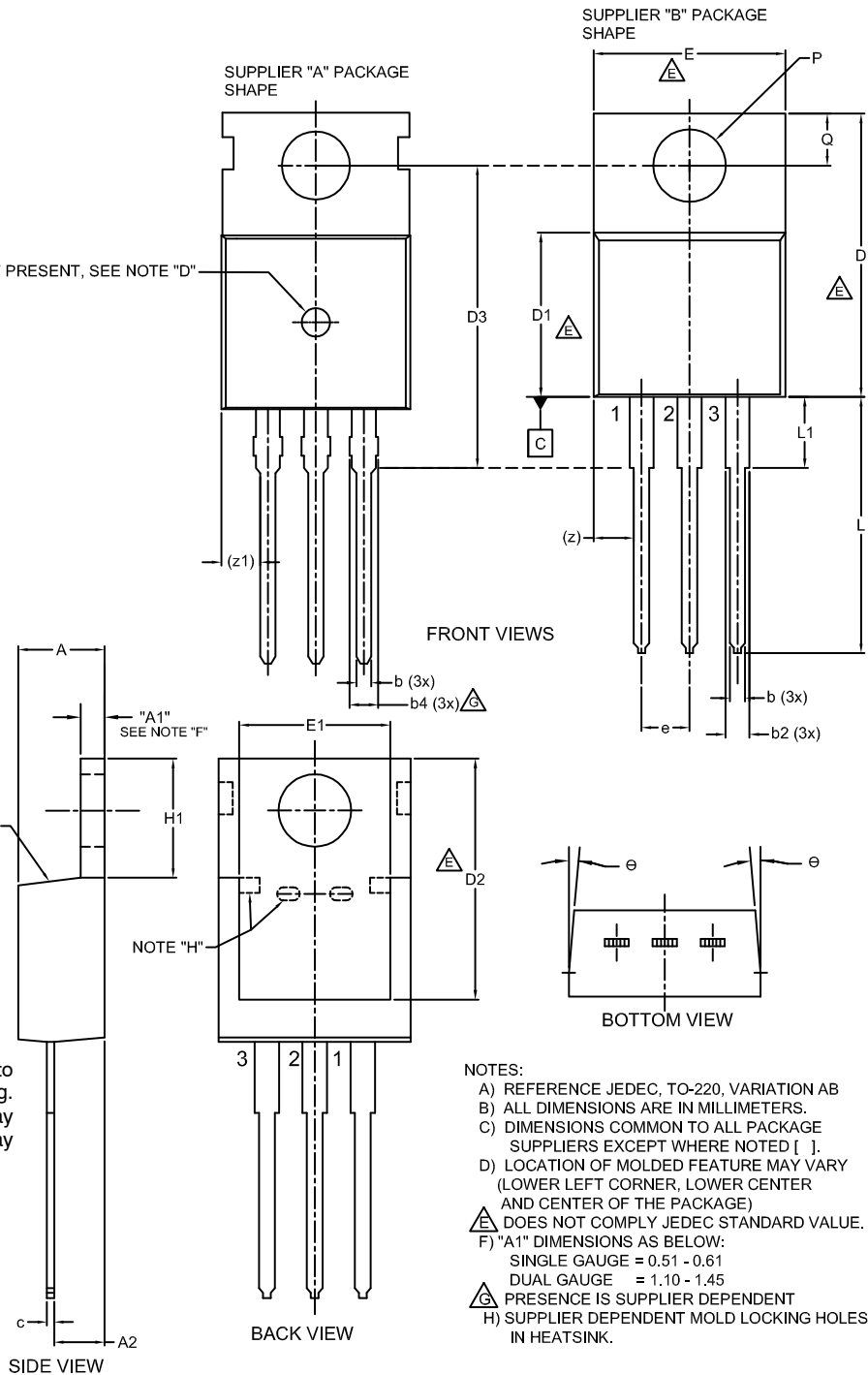
IF PRESENT, SEE NOTE "D"

GENERIC MARKING DIAGRAM*



XXXX = Specific Device Code
A = Assembly Location
Y = Year
WW = Work Week
ZZ = Assembly Lot Code

*This information is generic. Please refer to device data sheet for actual part marking. Pb-Free indicator, "G" or microdot "▪", may or may not be present. Some products may not follow the Generic Marking.



NOTES:

- A) REFERENCE JEDEC, TO-220, VARIATION AB
- B) ALL DIMENSIONS ARE IN MILLIMETERS.
- C) DIMENSIONS COMMON TO ALL PACKAGE SUPPLIERS EXCEPT WHERE NOTED [].
- D) LOCATION OF MOLDED FEATURE MAY VARY (LOWER LEFT CORNER, LOWER CENTER AND CENTER OF THE PACKAGE)
- △ DOES NOT COMPLY JEDEC STANDARD VALUE.
- F) "A1" DIMENSIONS AS BELOW:
SINGLE GAUGE = 0.51 - 0.61
DUAL GAUGE = 1.10 - 1.45
- △ PRESENCE IS SUPPLIER DEPENDENT
- H) SUPPLIER DEPENDENT MOLD LOCKING HOLES IN HEATSINK.

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