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November 2010

NC7SVL04 TinyLogic[®] Low-I_{CCT} Inverter

Features

- 0.9V to 3.6V V_{CC} Supply Operation
- 3.6V Over-Voltage Tolerant I/Os at V_{CC} from 0.9V to 3.6V
- Power-Off High Impedance Inputs and Outputs
- Proprietary Quiet SeriesTM Noise / EMI Reduction Circuitry
- Ultra-Small MicroPak[™] Package
- Ultra-Low Dynamic Power

Description

The NC7SVL04 is a single inverter with a low-l_{CCT} input design from Fairchild's Ultra-Low Power (ULP-A) series of TinyLogic®. The NC7SVL04 features very low quiescent current, even when the input voltage is lower than the $V_{\rm CC}$ supply. This feature services mobile handset applications very well, allowing for direct interface with baseband processor general-purpose I/Os. Since mobile devices rely on a battery supply, the NC7SVL04 facilitates lower power consumption in mixed-voltage rail environments.

This product is designed on an advanced CMOS technology for a wide low-voltage operating range (0.9V to 3.6V $V_{\rm CC}$), high drive needs (up to 24mA), and speed (maximum propagation delay of 3.5ns, $V_{\rm CC}$ =3.3V). It achieves this performance while maintaining low CMOS power dissipation.

Ordering Information

Part Number	Top Mark	Package	Packing Method
NC7SVL04P5X	L04	5-Lead SC70, EIAJ SC-88a, 1.25mm Wide	3000 Units on Tape & Reel
NC7SVL04L6X	CD	6-Lead MicroPak™, 1.00mm Wide	5000 Units on Tape & Reel
NC7SVL04FHX	CD	6-Lead, MicroPak2™, 1x1mm Body, .35mm Pitch	5000 Units on Tape & Reel

Connection Diagram

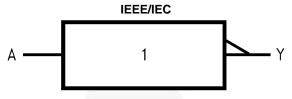


Figure 1. Logic Symbol

Pin Configurations

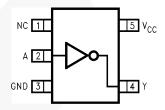


Figure 2. SC70 (Top View)

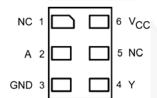


Figure 3. MicroPak™ (Top Through View)

Pin Definitions

Pin # SC70	Pin # MicroPak™	Name	Description	
1	1	NC	No Connect	
2	2	A	Input	
3	3	GND	Ground	
4	4	Y	Output	
5	6	V _{CC}	Supply Voltage	
	5	NC	No Connect	

Function Table

Y=/A

Inputs	Output
Α	Υ
L	Н
Н	L

H = HIGH Logic Level L = LOW Logic Level

Absolute Maximum Ratings

Stresses exceeding the absolute maximum ratings may damage the device. The device may not function or be operable above the recommended operating conditions and stressing the parts to these levels is not recommended. In addition, extended exposure to stresses above the recommended operating conditions may affect device reliability. The absolute maximum ratings are stress ratings only.

Symbol	Para	Min.	Max.	Unit	
V _{CC}	Supply Voltage		-0.5	4.6	V
V _{IN}	DC Input Voltage		-0.5	4.6	V
V	DC Output Valtage	HIGH or LOW State ⁽¹⁾	-0.5	V _{CC} + 0.5	\/
V_{OUT}	DC Output Voltage	V _{CC} =0V	-0.5	4.6	V
I _{IK}	DC Input Diode Current	V _{IN} < 0V		-50	mA
,	DC Output Diede Current	V _{OUT} < 0V		-50	A
l _{OK}	DC Output Diode Current	$V_{OUT} > V_{CC}$		+50	mA
I _{OH} / I _{OL}	DC Output Source/Sink Current		±50	mA	
I _{CC} or I _{GND}	DC V _{CC} or Ground Current per Su	ıpply Pin		±50	mA
T _{STG}	Storage Temperature Range		-65	+150	°C
TJ	Junction Temperature Under Bias	3		+150	°C
TL	Junction Lead Temperature, Sold	ering 10 Seconds		+260	°C
		SC70-5		150	
P_D	Power Dissipation at +85°C	MicroPak™-6		130	mW
	/2	MicroPak2™-6		120	
ECD	Human Body Model, JEDEC:JES		4000	V	
ESD	Charge Device Model, JEDEC:JE	SD22-C101		2000	V

Note:

Recommended Operating Conditions

The Recommended Operating Conditions table defines the conditions for actual device operation. Recommended operating conditions are specified to ensure optimal performance to the datasheet specifications. Fairchild does not recommend exceeding them or designing to Absolute Maximum Ratings.

Symbol	Parameter	Conditions	Min.	Max.	Unit	
V _{CC}	Supply Voltage		0.9	3.6	V	
V _{IN}	Input Voltage		0	3.6	V	
V	Output Voltage	V _{CC} =0V	0	3.6	V	
V _{OUT}	Output Voltage	HIGH or LOW State	0	V _{CC}	\ \ \	
		V _{CC} =3.0V to 3.6V		±24.0		
	Output Current in I _{OH} /I _{OL}	V _{CC} =2.3V to 2.7V		±18.0		
1 /1		V _{CC} =1.65V to 1.95V		±6.0	mΛ	
I _{OH} /I _{OL}		V _{CC} =1.4V to 1.6V		±4.0	mA	
		V _{CC} =1.1V to 1.3V		±2.0		
		V _{CC} =0.9V		±0.1		
T_A	Operating Temperature, Free Air		-40	+85	°C	
Δt/ΔV	Minimum Input Edge Rate	V _{IN} =0.8V to 2.0V, V _{CC} =3.0V		10	ns/V	
		SC70-5		425		
$\theta_{\sf JA}$	Thermal Resistance	MicroPak™-6		500	°C/W	
		MicroPak2™-6		560		

Note:

2. Unused inputs must be held HIGH or LOW. They may not float.

^{1.} Io absolute maximum ratings must be observed.

DC Electrical Characteristics

0		.,	0 1111	T _A =2	5°C	T _A =-40	to +85°C	
Symbol	Parameter	V _{cc}	Conditions	Min.	Max.	Min.	Max.	Units
		0.90		.65 x V _{CC}		.65 x V _{CC}		
		$1.10 \le V_{CC} \le 1.30$.65 x V _{CC}		.65 x V _{CC}		
V	HIGH Level Input	$1.40 \le V_{CC} \le 1.60$.65 x V _{CC}		.65 x V _{CC}		V
V _{IH}	Voltage	$1.65 \le V_{CC} \le 1.95$		0.90		.0.90] V
		$2.30 \leq V_{CC} \leq 2.70$		1.50		1.50		
		$2.70 \leq V_{CC} \leq 3.60$		1.50		1.50		
		0.90			.25 x V _{CC}		.25 x V _{CC}	
		$1.10 \le V_{CC} \le 1.30$.25 x V _{CC}		.25 x V _{CC}	
V	LOW Level Input	$1.40 \le V_{CC} \le 1.60$.25 x V _{CC}		.25 x V _{CC}	V
V_{IL}	Voltage	$1.65 \leq V_{CC} \leq 1.95$.25 x V _{CC}		.25 x V _{CC}	V
		$2.30 \leq V_{CC} \leq 2.70$			0.70		0.70	
		$2.70 \leq V_{CC} \leq 3.60$			0.80		0.80	
7		0.90		V _{CC} -0.10		V _{CC} -0.10		
		$1.10 \le V_{CC} \le 1.30$		V _{CC} -0.10		V _{CC} -0.10		
		$1.40 \leq V_{CC} \leq 1.60$	I _{OH} =-100μΑ	V _{CC} -0.20		V _{CC} -0.20		
		$1.65 \leq V_{CC} \leq 1.95$	10H=-100μΑ	V _{CC} -0.20		V _{CC} -0.20		
		$2.30 \leq V_{CC} \leq 2.70$		V _{CC} -0.20		V _{CC} -0.20		
		$2.70 \leq V_{CC} \leq 3.60$		V _{CC} -0.20		V _{CC} -0.20		
		$1.10 \le V_{CC} \le 1.30$	I _{OH} =-2mA	.75 x V _{CC}		.75 x V _{CC}		
V_{OH}	HIGH Level Output Voltage	$1.40 \leq V_{CC} \leq 1.60$	I _{OH} =-4mA	.75 x V _{CC}		.75 x V _{CC}		V
	l	$1.65 \leq V_{CC} \leq 1.95$	I _{OH} =-6mA	1.25		1.25		
		$2.30 \leq V_{CC} \leq 2.70$	IOH=-OITIA	2.00		2.00		-
		$2.30 \leq V_{CC} \leq 2.70$	I – 12mΛ	1.80		1.80		
		2.70≤ V _{CC} ≤ 3.60	I _{OH} =-12mA	2.20		2.20		
		$2.30 \leq V_{CC} \leq 2.70$	19m Λ	1.70		1.70		
		$2.70 \leq V_{CC} \leq 3.60$	I _{OH} =-18mA	2.40		2.40		
		$2.70 \leq V_{CC} \leq 3.60$	I _{OH} =-24mA	2.20	7	2.20		

Continued on the following page...

DC Electrical Characteristics (Continued)

Comple of	Donomoton	V	Conditions	T _A =	25°C	T _A =-40	to 85°C	l linita
Symbol	Parameter	V _{cc}	Conditions	Min.	Max.	Min.	Max.	Units
		0.90			0.10		0.10	
		$1.10 \leq V_{CC} \leq 1.30$			0.10		0.10	
		$1.40 \le V_{CC} \le 1.60$	Ι _{ΟL} =100μΑ		0.20		0.20	
		$1.65 \leq V_{CC} \leq 1.95$	I _{OL} =100µA		0.20		0.20	
		$2.30 \leq V_{CC} \leq 2.70$			0.20		0.20	
		$2.70 \leq V_{CC} \leq 3.60$			0.20		0.20	
	LOW Level	$1.10 \leq V_{CC} \leq 1.30$	I _{OL} =2mA		0.25 x V _{CC}		0.25 x V _{CC}	V
V _{OL}	Output Voltage	$1.40 \le V_{CC} \le 1.60$	I _{OL} =4mA		0.25 x V _{CC}		0.25 x V _{CC}	V
		$1.65 \leq V_{CC} \leq 1.95$	I _{OL} =6mA		0.30		0.30	
		$2.30 \leq V_{CC} \leq 2.70$	I _{OL} =12mA		0.40		0.40	- -
		$2.70 \leq V_{CC} \leq 3.60$	I _{OL} =12IIIA		0.40		0.40	
		$2.30 \leq V_{CC} \leq 2.70$	I _{OL} =18mA		0.60		0.60	
		$2.70 \leq V_{CC} \leq 3.60$	IOL=TOTTIA		0.40		0.40	
		$2.70 \leq V_{CC} \leq 3.60$	I _{OL} =24mA		0.55		0.55	
I _{IN}	Input Leakage Current	0.90 to 3.60	$0 \leq V_{IN} \leq 3.60V$		±0.1		±0.5	μA
I _{OFF}	Power Off Leakage Current	0	$0 \le (V_{IN}, V_O) \le 3.60V$		0.5		0.5	μΑ
	, Quiescent	0.00 to 2.60	V _{IN} =V _{CC} or GND		0.9		0.9	
Icc	Supply Current	0.90 to 3.60	$V_{CC} \leq V_{IN} \leq 3.60 V$				±0.9	μA
Ісст	Increase in I _{CC}	1.95	V _{IN} =0.9V		6		8	πΔ
ICCI	per Input				6		8 μΑ	

AC Electrical Characteristics

Symbol	Parameter	V _{CC} Conditions		T _A =25°C		С	T _A =-40	to 85°C	Units	Figure	
Symbol Parameter	Vcc	Conditions	Min.	Тур.	Max.	Min.	Max.	Ullits	rigure		
		0.90	$C_L=15pF, R_L=1M\Omega$		34.0						
	1.10 ≤ V _{CC} ≤ 1.30	$1.10 \le V_{CC} \le 1.30$	C 15nF D 2kO	3.5	8.1	16.5	3.0	27.8			
	Propagation	$1.40 \le V_{CC} \le 1.60$	$C_L=15pF, R_L=2k\Omega$	1.5	3.7	7.0	1.5	7.5		Figure 4	
t _{PHL} , t _{PLH}	Delay	$1.65 \leq V_{CC} \leq 1.95$	C _L =30pF,	C _L =30pF, R _L =500Ω	1.1	2.8	5.8	1.0	6.3	ns	Figure 5
		$2.30 \leq V_{CC} \leq 2.70$			0.6	2.0	4.0	0.6	4.5		
		$2.70 \leq V_{CC} \leq 3.60$		0.5	1.5	3.5	0.5	4.0		\geq	
C _{IN}	Input Capacitance	0			3				pF		
C _{PD}	Power Dissipation Capacitance	0.90 to 3.60	V _{IN} =0V or V _{CC} , f=10MHz		5				pF		

AC Loadings and Waveforms

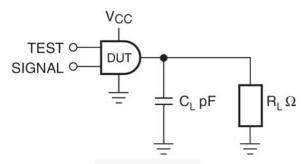


Figure 4. AC Test Circuit

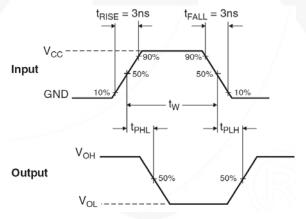


Figure 5. AC Waveforms

Symbol			СС			
Symbol $3.3V \pm 0.3V$ $2.5V \pm 0.2V$ $1.8V \pm 0.15V$ $1.5V \pm 0.00$					1.2V ± 0.1V	Ve.0
V _{mi}	1.5V	V _{CC} /2				
V_{mo}	1.5V	V _{CC} /2				

Physical Dimensions

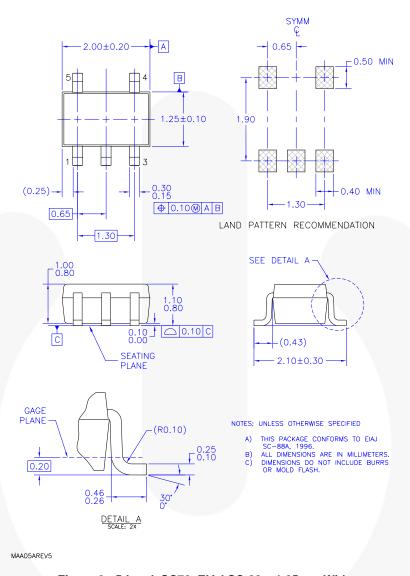


Figure 6. 5-Lead, SC70, EIAJ SC-88a, 1.25mm Wide

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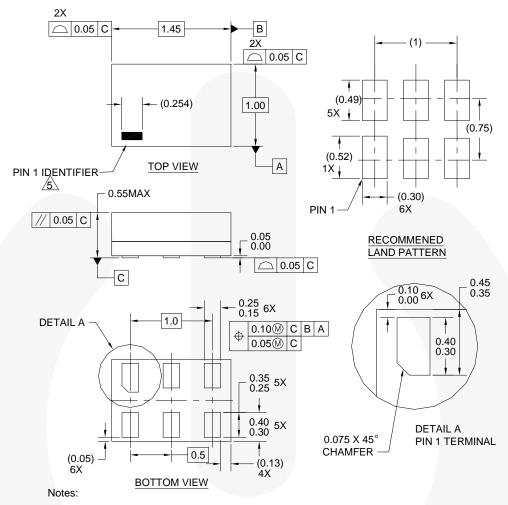
Always visit Fairchild Semiconductor's online packaging area for the most recent package drawings: http://www.fairchildsemi.com/packaging/.

Tape and Reel Specifications

Please visit Fairchild Semiconductor's online packaging area for the most recent tape and reel specifications: http://www.fairchildsemi.com/products/analog/pdf/sc70-5_tr.pdf.

Package Designator Tape Section		Cavity Number	Cavity Status	Cover Type Status
	Leader (Start End)	125 (Typical)	Empty	Sealed
P5X	Carrier	3000	Filled	Sealed
	Trailer (Hub End)	75 (Typical)	Empty	Sealed

Physical Dimensions



- 1. CONFORMS TO JEDEC STANDARD M0-252 VARIATION UAAD
- 2. DIMENSIONS ARE IN MILLIMETERS
- 3. DRAWING CONFORMS TO ASME Y14.5M-1994
- 4. FILENAME AND REVISION: MAC06AREV4
- 5 PIN ONE IDENTIFIER IS 2X LENGTH OF ANY

OTHER LINE IN THE MARK CODE LAYOUT.

Figure 7. 6-Lead, MicroPak™, 1.0mm Wide

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Tape and Reel Specifications

Please visit Fairchild Semiconductor's online packaging area for the most recent tape and reel specifications: http://www.fairchildsemi.com/products/logic/pdf/micropak_tr.pdf.

Package Designator	Tape Section	Cavity Number	Cavity Status	Cover Type Status
	Leader (Start End)	125 (Typical)	Empty	Sealed
L6X	Carrier	5000	Filled	Sealed
	Trailer (Hub End)	75 (Typical)	Empty	Sealed

Physical Dimensions 0.89 0.05 C В 1.00 2X 5X 0.40 PIN 1 0.66 MIN 250uM 1.00 1X 0.45 ○ 0.05 C TOP VIEW RECOMMENDED LAND PATTERN 2X FOR SPACE CONSTRAINED PCB 0.90 // 0.05 C 0.35 0.55MAX С 5X 0.52 SIDE VIEW 0.73 (0.08) 4X -1X 0.57 0.09 0.19 6X DETAIL A - 0.20 6X ALTERNATIVE LAND PATTERN FOR UNIVERSAL APPLICATION (0.05) 6X 5X 0.35 0.25 0.60 0.10M C B A 0.35 \oplus 0.40 (80.0).05 C 0.30 4X **BOTTOM VIEW** NOTES: A. COMPLIES TO JEDEC MO-252 STANDARD B. DIMENSIONS ARE IN MILLIMETERS.

E. DRAWING FILENAME AND REVISION: MGF06AREV3

Figure 8. 6-Lead, MicroPak2™, 1x1mm Body, .35mm Pitch

0.075X45°

CHAMFER

PIN 1 LEAD SCALE: 2X

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C. DIMENSIONS AND TOLERANCES PER ASME Y14.5M, 1994 D. LANDPATTERN RECOMMENDATION IS BASED ON FSC

Tape and Reel Specifications

DESIGN

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Package Designator	Tape Section	Cavity Number	Cavity Status	Cover Type Status
	Leader (Start End)	125 (Typical)	Empty	Sealed
FHX	Carrier	5000	Filled	Sealed
	Trailer (Hub End)	75 (Typical)	Empty	Sealed





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PRODUCT STATUS DEFINITIONS

Definition of Terms

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Advance Information	Formative / In Design	Datasheet contains the design specifications for product development. Specifications may change in any manner without notice.
Preliminary	First Production	Datasheet contains preliminary data; supplementary data will be published at a later date. Fairchild Semiconductor reserves the right to make changes at any time without notice to improve design.
No Identification Needed	Full Production	Datasheet contains final specifications. Fairchild Semiconductor reserves the right to make changes at any time without notice to improve the design.
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